

STGW20IH125DF STGWT20IH125DF

1250 V, 20 A IH series trench gate field-stop IGBT

Datasheet - production data

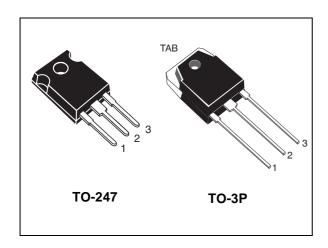
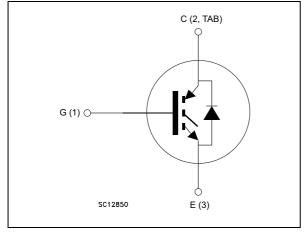


Figure 1. Internal schematic diagram



Features

- · Designed for soft commutation only
- Maximum junction temperature: T_J = 175 °C
- Minimized tail current
- $V_{CE(sat)} = 2.0 \text{ V (typ.)} @ I_C = 15 \text{ A}$
- Tight parameters distribution
- · Safe paralleling
- Very low V_F soft recovery co-packaged diode
- Low thermal resistance
- Lead free package

Applications

- Induction heating
- Microwave oven
- · Resonant converters

Description

These IGBTs are developed using an advanced proprietary trench gate field-stop structure and performance is optimized in both conduction and switching losses. A freewheeling diode with a low drop forward voltage is co-packaged. The result is a product specifically designed to maximize efficiency for any resonant and soft-switching application.

Table 1. Device summary

Order code	Marking	Package	Packaging
STGW20IH125DF	G20IH125DF	TO-247	Tube
STGWT20IH125DF	G20IH125DF	TO-3P	Tube

Contents

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{GE} = 0)	1250	V
I _C	Continuous collector current at T _C = 25 °C	40	Α
I _C	Continuous collector current at T _C = 100 °C	20	Α
I _{CP} ⁽¹⁾	Pulsed collector current	80	Α
V _{GE}	Gate-emitter voltage	±20	V
I _F	Continuous forward current at T _C = 25 °C	40	Α
I _F	Continuous forward current at T _C = 100 °C	20	Α
I _{FP} ⁽¹⁾	Pulsed forward current	80	Α
P _{TOT}	Total dissipation at T _C = 25 °C	259	W
T _{STG}	Storage temperature range	- 55 to 150	°C
T _J	Operating junction temperature	- 55 to 175	°C

^{1.} Pulse width limited by maximum junction temperature

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case IGBT	0.58	°C/W
R _{thJC}	Thermal resistance junction-case diode	1.47	°C/W
R _{thJA}	Thermal resistance junction-ambient	50	°C/W

2 Electrical characteristics

 $T_J = 25$ °C unless otherwise specified.

Table 4. Static characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)CES}	Collector-emitter breakdown voltage (V _{GE} = 0)	I _C = 2 mA	1250			٧
		V _{GE} = 15 V, I _C = 15 A		2	2.5	
V	Collector-emitter saturation	V _{GE} = 15 V, I _C = 15 A T _J = 125 °C		2.2		V
V _{CE(sat)} voltage	voltage	V _{GE} = 15 V, I _C = 15 A T _J = 175 °C		2.3		
		$V_{GE} = 15 \text{ V}, I_{C} = 30 \text{ A}$		2.55		
		I _F = 15 A		1.1	1.5	
N/	Forward on voltage	I _F = 15 A T _J = 125 °C		1.05		V
V _F	Forward on-voltage	I _F = 15 A T _J = 175 °C		1		V
		I _F = 30A		1.25		
V _{GE(th)}	Gate threshold voltage	$V_{CE} = V_{GE}, I_{C} = 500 \mu\text{A}$	5	6	7	V
I _{CES}	Collector cut-off current (V _{GE} = 0)	V _{CE} = 1250 V			25	μΑ
I _{GES}	Gate-emitter leakage current (V _{CE} = 0)	V _{GE} = ± 20 V			250	nA

Table 5. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{ies}	Input capacitance		-	1290	-	pF
C _{oes}	Output capacitance	$V_{CE} = 25 \text{ V, f} = 1 \text{ MHz,}$ $V_{GE} = 0$	-	96	-	pF
C _{res}	Reverse transfer capacitance		-	30.6	-	pF
Qg	Total gate charge	V _{CC} = 600 V, I _C = 15 A, V _{GF} = 15 V, see <i>Figure 25</i>	-	69	-	nC
Q _{ge}	Gate-emitter charge		-	7.2	-	nC
Q _{gc}	Gate-collector charge	1 GE 13 1, 110 7 1907 20	-	40.8	-	nC

Table 6. IGBT switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(off)}	Turn-off delay time	V _{CF} = 600 V, I _C = 15 A,		106		ns
t _f	Current fall time	$R_G = 10 \Omega$, $V_{GE} = 15 V$, see	-	79	-	ns
E _{off} ⁽¹⁾	Turn-off switching losses	Figure 23	-	410	-	μJ
t _{d(off)}	Turn-off delay time	V _{CF} = 600 V, I _C = 15 A,		109		ns
t _f	Current fall time	$R_G = 10 \Omega$, $V_{GE} = 15 V$,	-	176	-	ns
E _{off} ⁽¹⁾	Turn-off switching losses	$T_J = 175 ^{\circ}\text{C}$, see <i>Figure 23</i>	-	820	-	μJ

^{1.} Turn-off losses include also the tail of the collector current.

Table 7. IGBT switching characteristics (capacitive load)

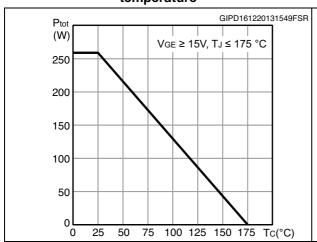
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
(1)	E _{off} ⁽¹⁾ Turn-off switching losses	$V_{CC} = 900 \text{V}, R_{G} = 10 \ \Omega,$ $I_{C} = 30 \ \text{A}, L = 500 \ \mu\text{H},$ $C_{\text{snub}} = 330 \ \text{nF}, \text{see}$ Figure 24	-	163	-	"1
Loff,		$V_{CC} = 900 \text{V}, R_G = 10 \ \Omega, \\ I_C = 30 \ \text{A}, L = 500 \ \mu\text{H}, \\ C_{\text{snub}} = 330 \ \text{nF}, T_J = 175 \ ^{\circ}\text{C}, \\ \text{see} \ \textit{Figure 24}$	-	366	-	μJ

^{1.} Turn-off losses include also the tail of the collector current.

2.1 Electrical characteristics (curves)

Figure 2. Power dissipation vs. case temperature

Figure 3. Collector current vs. case temperature



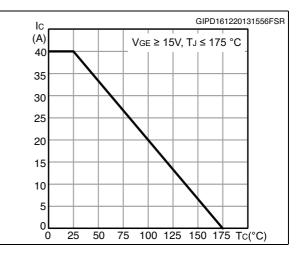
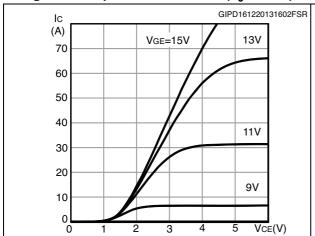


Figure 4. Output characteristics $(T_J = 25^{\circ}C)$

Figure 5. Output characteristics $(T_J = 175^{\circ}C)$



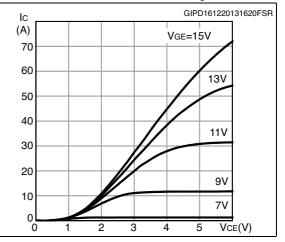
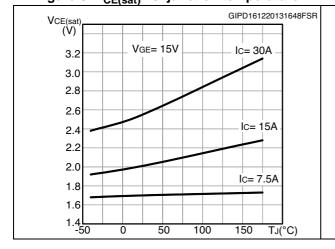


Figure 6. V_{CE(sat)} vs. junction temperature

Figure 7. V_{CE(sat)} vs. collector current



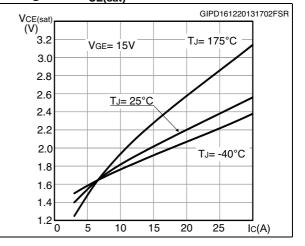


Figure 8. Forward bias safe operating area

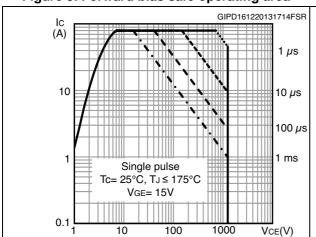


Figure 9. Transfer characteristics

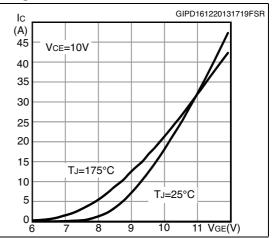
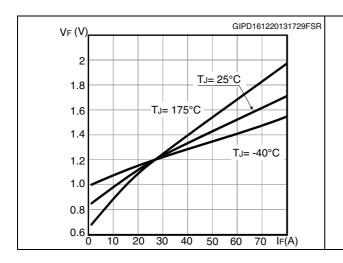


Figure 10. Diode V_F vs. forward current

Figure 11. Normalized $V_{GE(th)}$ vs junction temperature



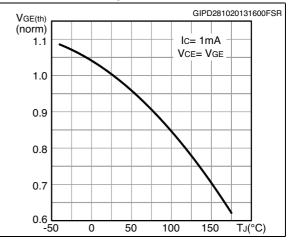
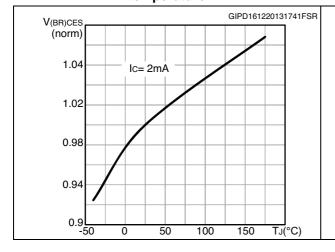
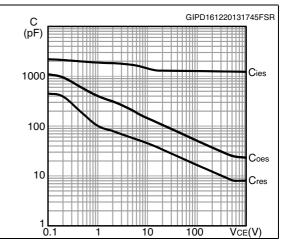


Figure 12. Normalized $V_{(BR)CES}$ vs. junction temperature

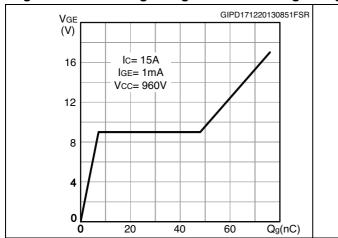
Figure 13. Capacitance variation





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Figure 14. Gate charge vs. gate-emitter voltage Figure 15. Switching loss vs collector current



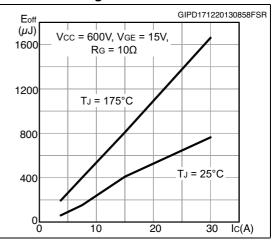
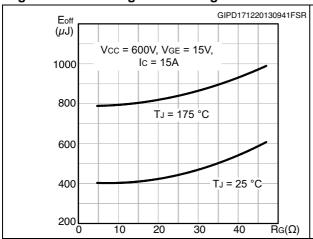


Figure 16. Switching-off loss vs gate resistance Figure 17. Switching-off loss vs temperature



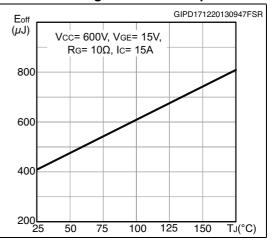
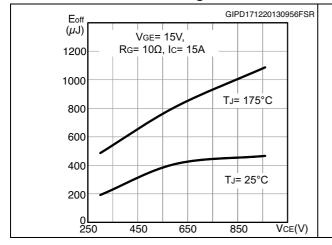


Figure 18. Switching-off loss vs collectoremitter voltage

Figure 19. Switching times vs. collector current



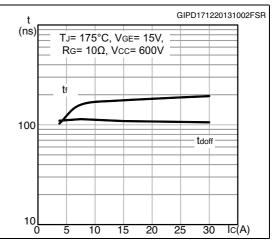
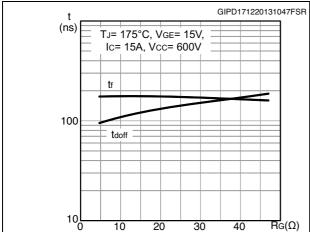


Figure 20. Switching times vs. gate resistance





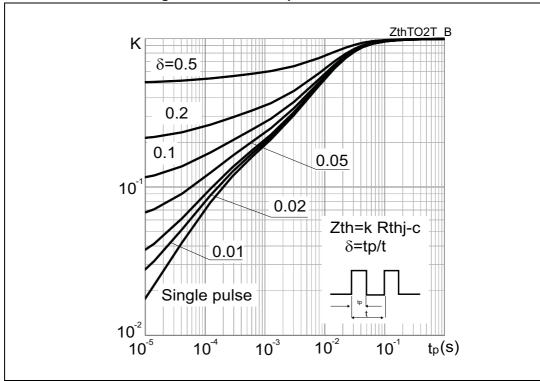
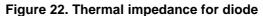
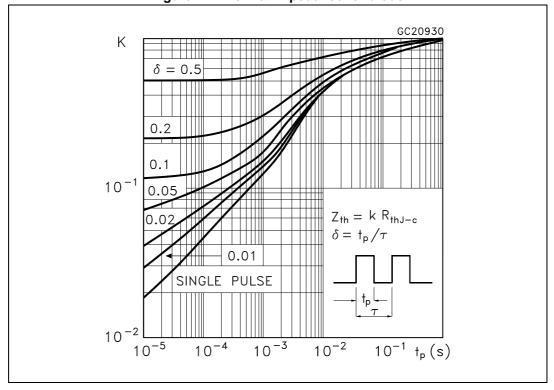


Figure 21. Thermal impedance for IGBT





3 Test circuits

Figure 23. Test circuit for inductive load switching

Figure 24. Test circuit for capacitive load switching

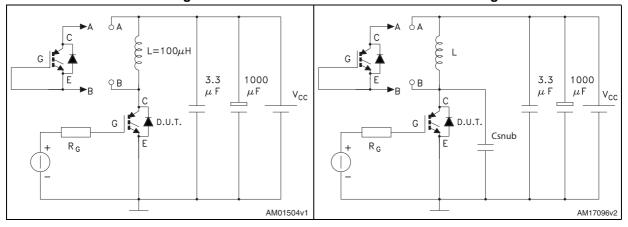
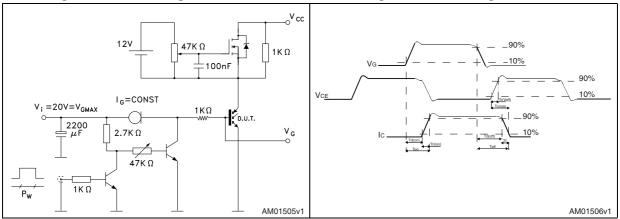


Figure 25. Gate charge test circuit

Figure 26. Switching waveform



4 Package mechanical data

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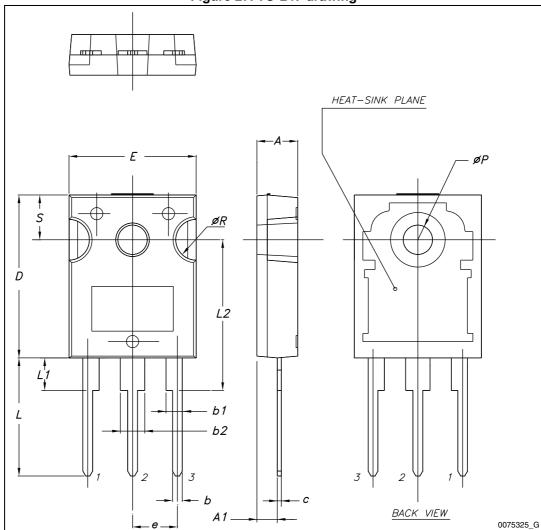


Figure 27. TO-247 drawing

Table 8. TO-247 mechanical data

Table 6. To 247 Incommon data			
Dim.		mm.	
Diiii.	Min.	Тур.	Max.
Α	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80
D	19.85		20.15
E	15.45		15.75
е	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

ш SEATING PLANE øP. Ε **-** A1 E2 -Q1 D D1 L2 L'1 *A2* - **b1**(2x) −**b** (3x) (2x)8045950_A

Figure 28. TO-3P drawing

Table 9. TO-3P mechanical data

		mm	
Dim.	Min.	Тур.	Max.
Α	4.60		5
A1	1.45	1.50	1.65
A2	1.20	1.40	1.60
b	0.80	1	1.20
b1	1.80		2.20
b2	2.80		3.20
С	0.55	0.60	0.75
D	19.70	19.90	20.10
D1		13.90	
Е	15.40		15.80
E1		13.60	
E2		9.60	
е	5.15	5.45	5.75
L	19.50	20	20.50
L1		3.50	
L2	18.20	18.40	18.60
øΡ	3.10		3.30
Q		5	
Q1		3.80	

5 Revision history

Table 10. Document revision history

Date	Revision	Changes
13-Jan-2014	1	Initial release.
03-Feb-2014	2	Added V _{CE(sat)} max value in <i>Table 5: Dynamic characteristics</i> Minor text changes.

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