

T1035H, T1050H

High temperature 10 A Snubberless™ Triacs

Features

- Medium current Triac
- 150 °C max. T_i turn-off commutation
- Low thermal resistance with clip bonding
- Very high 3 quadrant commutation capability
- Packages are RoHS (2002/95/EC) compliant
- UL certified (ref. file E81734)

Applications

Especially designed to operate in high power density or universal motor applications such as vacuum cleaner and washing machine drum motor, these 10 A Triacs provide a very high switching capability up to junction temperatures of 150 °C.

The heatsink can be reduced, compared to traditional Triacs, according to the high performance at given junction temperatures.

Description

Available in through-hole or surface mount packages, the T1035H and T1050H Triacs series are suitable for general purpose mains power ac switching.

By using an internal ceramic pad, the T10xxH-6l provides voltage insulation (rated at 2500 V rms).

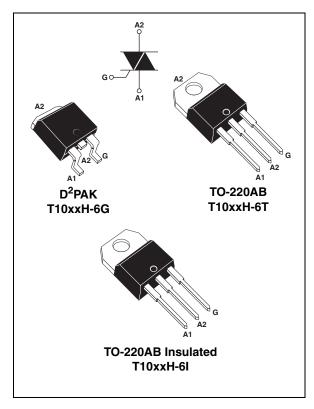


Table 1. Device summary

Symbol	Value	Unit	
I _{T(RMS)}	10	Α	
V_{DRM}/V_{RRM}	600	V	
I _{GT}	35 or 50	mA	

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Table 2. Absolute maximum ratings

Symbol	Parameter			Value	Unit
	On state rms surrent (full sine ways)	D ² PAK, TO-220AB $T_c = 135 ^{\circ}\text{C}$		10	^
I _{T(RMS)}	On-state rms current (full sine wave)	TO-220AB Ins	T _c = 125 °C	10	Α
_	Non repetitive surge peak on-state	F = 50 Hz	t = 20 ms	100	Α
I _{TSM}	current (full cycle, T _j initial = 25 °C)	F = 60 Hz	t = 16.7 ms	105	A
l ² t	I ² t Value for fusing	t _p = 10 ms		66	A ² s
dI/dt	Critical rate of rise of on-state current I_G = 2 x I_{GT} , $t_r \le 100$ ns	F = 120 Hz	T _j = 150 °C	50	A/µs
V _{DSM} /V _{RSM}	Non repetitive surge peak off-state voltage	t _p = 10 ms	T _j = 25 °C	V _{DRM} /V _{RRM} + 100	٧
I _{GM}	Peak gate current $t_p = 20 \ \mu s$ $T_j = 150 \ ^{\circ}C$		4	Α	
P _{G(AV)}	Average gate power dissipation $T_j = 150 ^{\circ}\text{C}$			1	W
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 150	°C

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Test Conditions	Quadrant		Va	Value	
Symbol	rest conditions	Quadrant		T1035H	T1050H	Unit
I _{GT} ⁽¹⁾	$V_{\rm D} = 12 \text{ V}, R_{\rm L} = 33 \Omega$	1 - 11 - 111	MAX.	35	50	mA
V_{GT}	VD = 12 V, 11[= 00 sz	1 - 11 - 111	MAX.	1.0		V
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$ I - II - III		MIN.	0.15		V
I _H ⁽²⁾	I _T = 500 mA	•	MAX.	35	75	mA
	$I_{G} = 1.2 I_{GT}$	I - III	MAX.	50	90	mA
I _L	IG = 1.2 IGT	II	IVIAA.	80	110	IIIA
dV/dt (2)	V _D = 67% V _{DRM,} gate open, T _j = 150 °C		MIN.	1000	1500	V/µs
(dl/dt)c (2)	Without snubber, T _j = 150 °C		MIN.	13	18	A/ms

^{1.} minimum $I_{\mbox{\scriptsize GT}}$ is guaranted at 20% of $I_{\mbox{\scriptsize GT}}$ max.

^{2.} for both polarities of A2 referenced to A1.

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Table 4. Static characteristics

Symbol	Test Conditions			Value	Unit
V _T ⁽¹⁾	$I_{TM} = 14 \text{ A}, t_p = 380 \ \mu \text{s}$	T _j = 25 °C	MAX.	1.5	V
V _{t0} ⁽¹⁾	Threshold voltage	T _j = 150 °C	MAX.	0.80	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	MAX.	34	mΩ
	V - V	T _j = 25 °C	MAX.	5	μΑ
I _{DRM}	$V_{DRM} = V_{RRM}$	T _j = 150 °C	MAX.	3.6	
I _{RRM} ⁽²⁾	V _D /V _R = 400 V (at peak mains voltage)	T _j = 150 °C	MAX.	3.0	mA
	V _D /V _R = 200 V (at peak mains voltage)	T _j = 150 °C	MAX.	2.5	

^{1.} for both polarities of A2 referenced to A1.

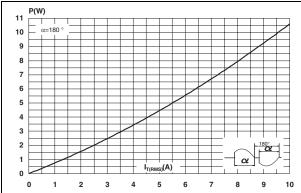
Table 5. Thermal resistance

Symbol	Parameter			Value	Unit
R _{th(j-c)} Junction to case (AC)			D ² PAK / TO-220AB		
			TO-220AB Ins	3.4	°C/W
R _{th(j-a)} Junction to ambient	$S = 1 \text{ cm}^2$	D ² PAK	45	C/VV	
	Junction to ambient		TO-220AB / TO-220AB Ins	60	

^{2.} $t_p = 380 \ \mu s$

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Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case on-state rms current (full cycle) temperature (full cycle)



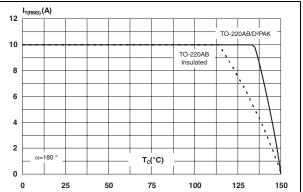
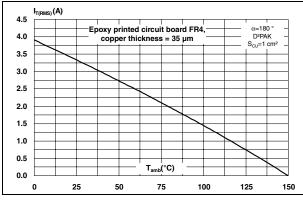


Figure 3. On-state rms current versus ambient temperature

Figure 4. Variation of thermal impedance versus pulse duration



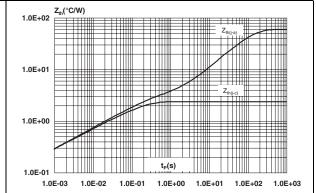
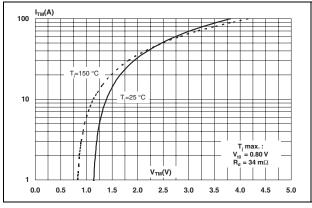
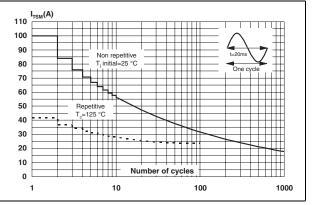


Figure 5. On-state characteristics (maximum values)

Figure 6. Surge peak on-state current versus number of cycles





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Figure 10.

Relative variation of I_{GT} , I_H , I_L vs Non-repetitive surge peak on-state Figure 8. Figure 7. current for a sinusoidal pulse with junction temperature

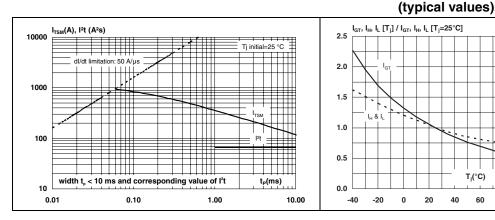


Figure 9. Relative variation of critical rate of decrease of main current (dl/dt)c versus reapplied (dV/dt)c

decrease of main current versus junction temperature $(dI/dt)_c [T_i] / (dI/dt)_c [T_i=150°C]$ 2

Relative variation of critical rate of

140

typical values

(dV/dt)_c (V/μs)

(dl/dt)_c [(dV/dt)_c] / Specified (dl/dt)_c

2.0

1.8 1.6 1.4 1.2 1.0 0.8 0.6

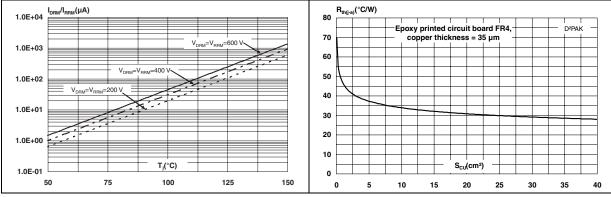
0.4 0.2

0.0 0.1

T_i(°C)

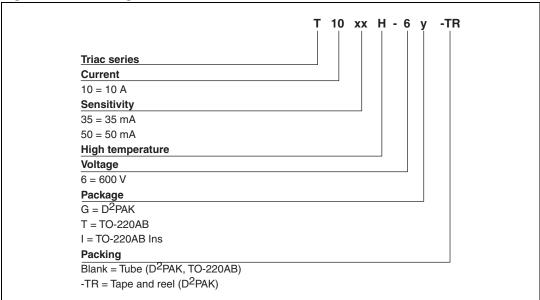
Figure 11. Leakage current versus junction temperature for different values of blocking voltage (typical values)

Figure 12. Variation of thermal resistance junction to ambient versus copper surface under tab



2 Ordering information scheme

Figure 13. Ordering information scheme



T1035H, T1050H Package information

3 Package information

- Epoxy meets UL94, V0
- Recommended torque 0.4 to 0.6 N⋅m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 6. D²PAK dimensions

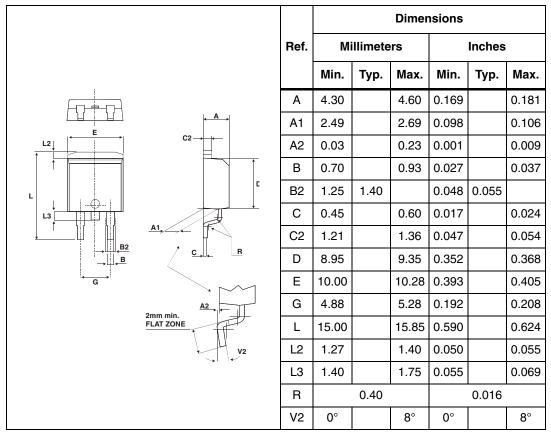
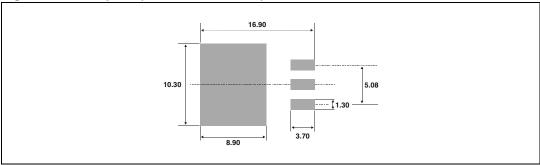


Figure 14. Footprint (dimensions in mm)

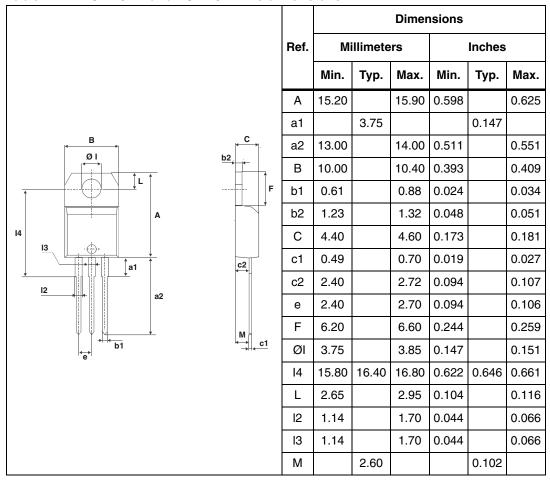


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Table 7. TO-220AB and TO-220AB Ins dimensions



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4 Ordering information

Table 8. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T10xxH-6G	T10xxH 6G	D ² PAK	1.5 g	50	Tube
T10xxH-6G-TR	T10xxH 6G	D ² PAK	1.5 g	1000	Tape and reel
T10xxH-6T	T10xxH 6T	TO-220AB	2.3 g	50	Tube
T10xxH-6l	T10xxH 6l	TO-220AB Ins	2.3 g	50	Tube

5 Revision history

Table 9. Document revision history

Date	Revision	Changes
17-Apr-2007	1	First issue
20-Sep-2011	2	Updated: Features, Description and Figure 2.

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