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IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss.

Features

- Optimized for Very Low V_{CEsat}
- Low Switching Loss Reduces System Power Dissipation
- 5 µs Short-Circuit Capability
- These are Pb-Free Devices

Typical Applications

• Power Factor Correction

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V _{CES}	600	V
Collector current @ Tc = 25°C @ Tc = 100°C	lc	60 30	A
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	120	Α
Short–circuit withstand time V_{GE} = 15 V, V_{CE} = 300 V, $T_{J} \le +150^{\circ}C$	t _{SC}	5	μS
Gate-emitter voltage Transient Gate Emitter Voltage (t _p = 5 μs, D < 0.010)	V _{GE}	±20 ±30	>
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	167 67	W
Operating junction temperature range	TJ	–55 to +150	°C
Storage temperature range	T _{stg}	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

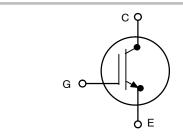
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

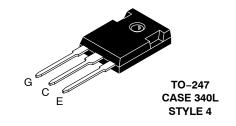


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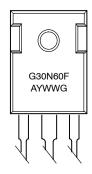
http://onsemi.com

30 A, 600 V V_{CEsat} = 1.5 V





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTG30N60FWG	TO-247 (Pb-Free)	30 Units / Rail

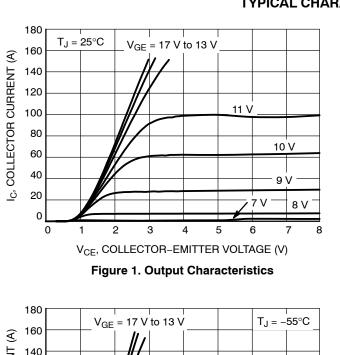
THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.75	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V, I}_{C} = 500 \mu\text{A}$	V _{(BR)CES}	600	_	-	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 30 A V _{GE} = 15 V, I _C = 30 A, T _J = 150°C	V _{CEsat}	1.25 -	1.45 1.75	1.70 –	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 200 \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V _{GE} = 0 V, V _{CE} = 600 V V _{GE} = 0 V, V _{CE} = 600 V, T _{J =} 150°C	I _{CES}	- -	- -	0.2 2	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V , V _{CE} = 0 V	I _{GES}	-	-	100	nA
DYNAMIC CHARACTERISTIC	•				•	
Input capacitance		C _{ies}	-	4100	-	pF
Output capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{oes}	-	115	_	1
Reverse transfer capacitance	7	C _{res}	-	95	-	1
Gate charge total		Q_g		170		nC
Gate to emitter charge	V _{CE} = 480 V, I _C = 30 A, V _{GE} = 15 V	Q _{ge}		34		
Gate to collector charge	7	Q _{gc}		83		
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD	•		•	1	•
Turn-on delay time		t _{d(on)}		81		ns
Rise time	7	t _r		31		1
Turn-off delay time		t _{d(off)}		190		1
Fall time	$V_{CC} = 400 \text{ V}, I_C = 30 \text{ A}$ $R_0 = 10 \text{ O}$	t _f		110		1
Turn-on switching loss	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V*}$	E _{on}		0.65		mJ
Turn-off switching loss	7	E _{off}		0.65		
Total switching loss	7	E _{ts}		1.30		1
Turn-on delay time		t _{d(on)}		80		ns
Rise time	$T_{J} = 150^{\circ}\text{C}$ $V_{CC} = 400 \text{ V, } I_{C} = 30 \text{ A}$ $R_{g} = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V*}$	t _r		32		1
Turn-off delay time		t _{d(off)}		200		1
Fall time		t _f		230		
Turn-on switching loss		E _{on}		0.80		mJ
Turn-off switching loss		E _{off}		1.1		
Total switching loss		E _{ts}		1.90		

^{*}Includes diode reverse recovery loss using NGTB30N60FWG.



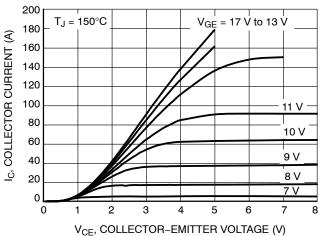
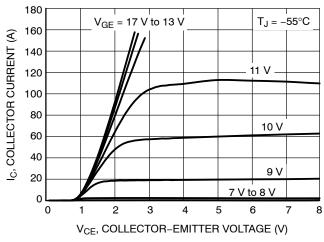


Figure 2. Output Characteristics



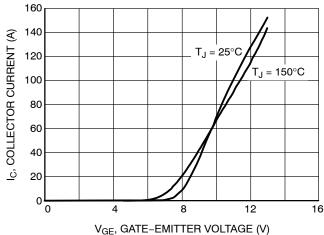
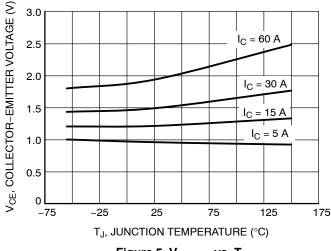


Figure 3. Output Characteristics

Figure 4. Typical Transfer Characteristics



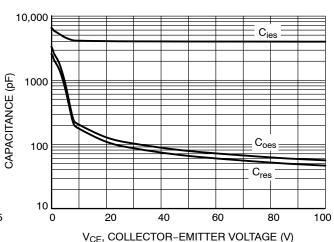


Figure 5. V_{CE(sat)} vs. T_J

Figure 6. Typical Capacitance

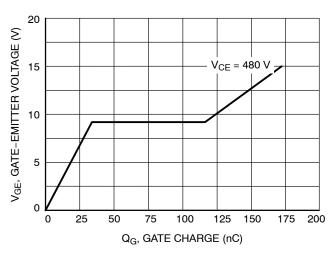


Figure 7. Typical Gate Charge

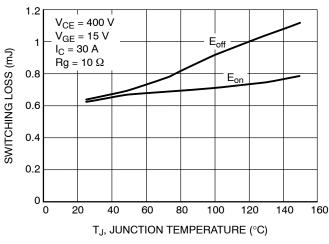


Figure 8. Switching Loss vs. Temperature

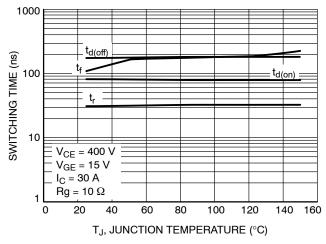


Figure 9. Switching Time vs. Temperature

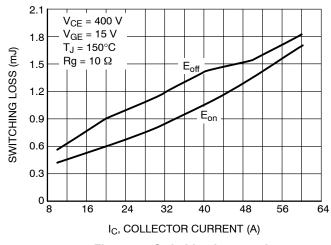


Figure 10. Switching Loss vs. I_C

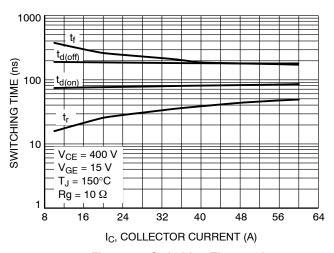
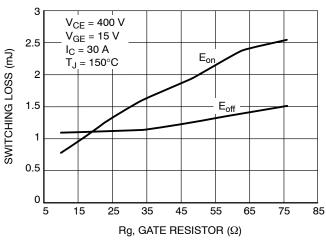


Figure 11. Switching Time vs. I_C



1000

t_d(off)

t_f

100

t_d(off)

t_r

V_{CE} = 400 V

V_{GE} = 15 V

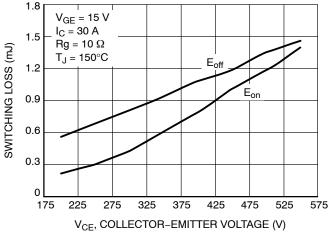
I_C = 30 A

T_J = 150°C

Rg, GATE RESISTOR (Ω)

Figure 12. Switching Loss vs. Rg

Figure 13. Switching Time vs. Rg



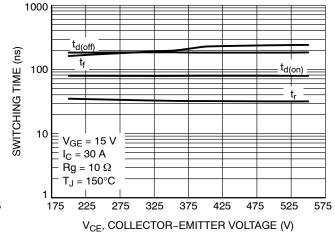
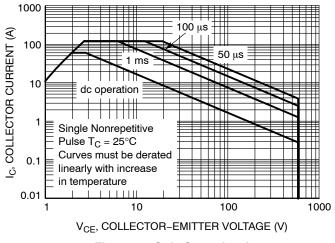


Figure 14. Switching Loss vs. V_{CE}

Figure 15. Switching Time vs. V_{CE}



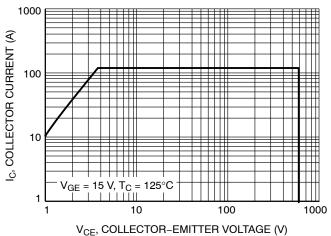


Figure 16. Safe Operating Area

Figure 17. Reverse Bias Safe Operating Area

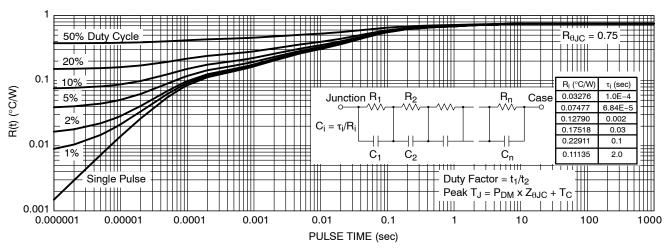


Figure 18. IGBT Transient Thermal Impedance

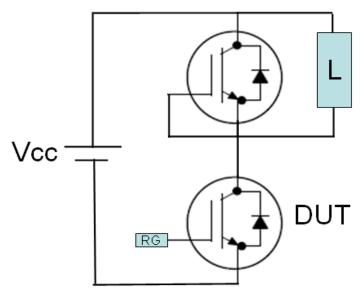


Figure 19. Test Circuit for Switching Characteristics

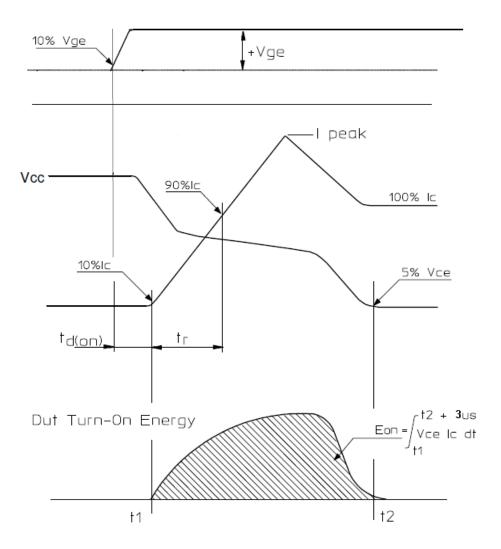


Figure 20. Definition of Turn On Waveform

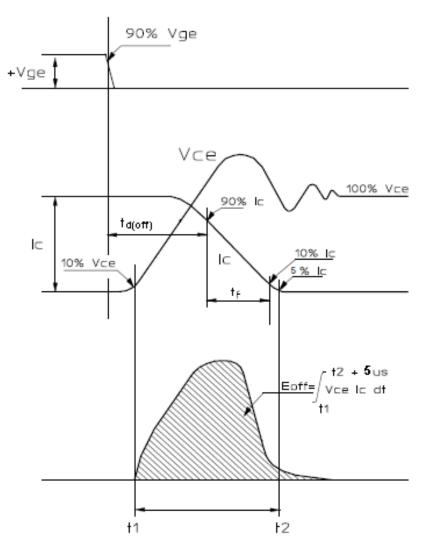
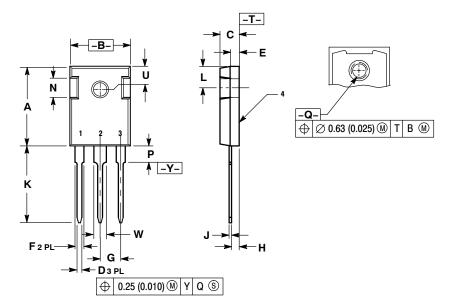


Figure 21. Definition of Turn Off Waveform

PACKAGE DIMENSIONS

TO-247 CASE 340L-02 ISSUE F



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.32	21.08	0.800	8.30	
В	15.75	16.26	0.620	0.640	
С	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
Е	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215 BSC		
Н	1.50	2.49	0.059	0.098	
J	0.40	0.80	0.016	0.031	
K	19.81	20.83	0.780	0.820	
L	5.40	6.20	0.212	0.244	
N	4.32	5.49	0.170	0.216	
Р		4.50		0.177	
Q	3.55	3.65	0.140	0.144	
U	6.15	6.15 BSC 0.2		BSC	
w	2 87	3 12	0 113	0.123	

STYLE 4:

- PIN 1. GATE 2. COLLECTOR 3. EMITTER

 - 4. COLLECTOR

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