



# Hi-Rel NPN dual matched bipolar transistor 60 V, 0.03 A

Datasheet — production data

#### **Features**

BV <sub>CEO</sub>	60 V
I <sub>C</sub> (max)	0.03 A
H <sub>FE</sub> at 10 V - 150 mA	> 300
Operating temperature range	-65°C to +200°C

- Hi-Rel NPN dual matched bipolar transistor
- Linear gain characteristics
- ESCC qualified
- European preferred part list EPPL
- Radiation level: lot specific total dose contact marketing for specified level

### **Description**

The 2N2920AHR is a silicon planar epitaxial NPN transistor in TO-77 and LCC-6 packages. It is specifically designed for aerospace Hi-Rel applications and ESCC qualified according to the 5207-002 specification. In case of conflict between this datasheet and ESCC detailed specification, the latter prevails.

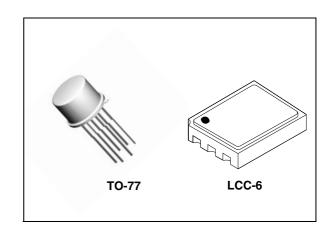


Figure 1. Internal schematic diagram

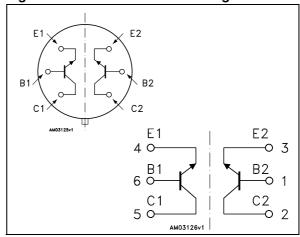


Table 1. Device summary

Order codes	Packages	Lead finish	Marking	Туре	EPPL	Packaging
2N2920AHR	TO-77	Gold Solder Dip	520700203 520700206	ESCC Flight		Strip pack
2N2920AT1	TO-77	Gold	2N2920AT1	Engineering model		Strip pack
SOC2920A	LCC-6	Gold	SOC2920A	Engineering model		Waffle pack
SOC2920AHRB	LCC-6	Gold Solder Dip	520700212 520700215	ESCC Flight	Yes	Waffle pack

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Electrical ratings 2N2920AHR

## 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)	60	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	60	V
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	6	V
I <sub>C</sub>	Collector current	30	mA
P <sub>TOT</sub>	Total dissipation at $T_{amb} \le 25  ^{\circ}\text{C}$ for SOC2920A $^{(1)}$ for SOC2920A $^{(2)}$ for 2N2920AHR $^{(1)}$ $^{(3)}$ for 2N2920AHR $^{(2)}$ $^{(3)}$ Total dissipation at $T_{c} \le 25  ^{\circ}\text{C}$ for SOC2920A $^{(1)}$ for SOC2920A $^{(2)}$	0.3 0.5 0.6 1 0.75 1.25	W W W W
T <sub>STG</sub>	Storage temperature	-65 to 200	°C
T <sub>J</sub>	Max. operating junction temperature	200	°C

<sup>1.</sup> One section.

Table 3. Thermal data for through-hole package

Symbol	Parameter	Value	Unit	
R <sub>thJC</sub>	Thermal resistance junction-case <sup>(1)</sup> Thermal resistance junction-case <sup>(2)</sup>	max max	233 140	°C/W
R <sub>thJA</sub>	Thermal resistance junction-ambient <sup>(1)</sup> Thermal resistance junction-ambient <sup>(2)</sup>	max max	583 350	°C/W

<sup>1.</sup> One section.

Table 4. Thermal data for SMD package

Symbol	Parameter	Value	Unit
В	Thermal resistance junction-ambient (1)(3) max	291	°C/W
R <sub>thJA</sub>	Thermal resistance junction-ambient (2)(3) max	175	°C/W

<sup>1.</sup> One section.

<sup>2.</sup> Both sections.

<sup>3.</sup> When mounted on a 15  $\times$  15  $\times$  0.6 mm ceramic substrate.

<sup>2.</sup> Both sections.

<sup>2.</sup> Both sections.

<sup>3.</sup> When mounted on a 15  $\times$  15  $\times$  0.6 mm ceramic substrate.

## 2 Electrical characteristics

 $T_{case}$  = 25 °C unless otherwise specified

Table 5. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector-base cut-off current (I <sub>E</sub> = 0)	V <sub>CB</sub> = 45 V V <sub>CB</sub> = 45 V T <sub>C</sub> = 150 °C			2 10	nΑ μΑ
I <sub>CEO</sub>	Collector cut-off current (I <sub>B</sub> = 0)	V <sub>CE</sub> = 5 V			2	nA
I <sub>EBO</sub>	Emitter-base cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 5 V			2	nA
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage (I <sub>E</sub> = 0)	Ι <sub>C</sub> = 10 μΑ	60			٧
V <sub>(BR)CEO</sub> (1)	Collector-emitter breakdown voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA	60			٧
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage (I <sub>C</sub> = 0)	Ι <sub>Ε</sub> = 10 μΑ	6			٧
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_C = 1 \text{ mA}$ $I_B = 0.1 \text{ mA}$			0.35	٧
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	$I_C = 1 \text{ mA}$ $I_B = 0.1 \text{ mA}$	0.5		1	V
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$\begin{split} I_{C} &= 10 \; \mu A & V_{CE} &= 5 \; V \\ I_{C} &= 100 \; \mu A & V_{CE} &= 5 \; V \\ I_{C} &= 1 \; m A & V_{CE} &= 5 \; V \\ I_{C} &= 10 \; \mu A & V_{CE} &= 5 \; V \\ T_{amb} &= -55 \; ^{\circ}C \end{split}$	150 225 300 50		600	
h <sub>FE2-1</sub> / h <sub>FE2-2</sub>	DC current transfer ratio comparison	$I_{C} = 100 \ \mu A$ $V_{CE} = 5 \ V$ $T_{amb} = -55 \ ^{\circ}C \ to +25 \ ^{\circ}C$	0.91		1.1	
h <sub>FE2-1</sub> / h <sub>FE2-2</sub>	DC current transfer ratio comparison	$I_C = 100 \mu A$ $V_{CE} = 5 V$ $T_{amb} = -55 °C to +125 °C$	0.85		1.18	
Δ V <sub>BE1</sub> - V <sub>BE2</sub>	Base-emitter voltage differential	$V_{CE} = 5 \text{ V}$ $I_{C} = 10 \mu\text{A}$ $V_{CE} = 5 \text{ V}$ $I_{C} = 100 \mu\text{A}$ $V_{CE} = 5 \text{ V}$ $I_{C} = 1 m\text{A}$			2 1.5 2	mV mV mV
Δ V <sub>BE1</sub> - V <sub>BE2</sub>	Base-emitter voltage differential	$V_{CE} = 5 \text{ V}$ $I_{C} = 100 \mu\text{A}$ $T_{amb} = -55 ^{\circ}\text{C} \text{ to } +25 ^{\circ}\text{C}$ $T_{amb} = +25 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$			0.4 0.5	mV mV
I <sub>Lk</sub>	Leakage current between active devices	$V = 50 \text{ V to } E_2, B_2, C_2$ $V = 0 \text{ V to } E_1, B_1, C_1$			5	μΑ

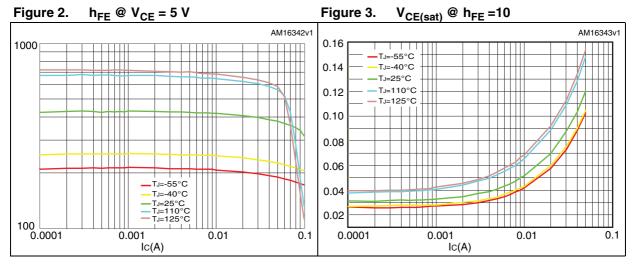


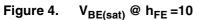
 Table 5.
 Electrical characteristics (continued)

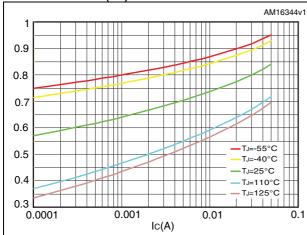
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
f <sub>T</sub>	Transition frequency	$I_C = 0.5 \text{ mA}$ $V_{CE} = 5 \text{ V}$	60			MHz
h <sub>ob</sub>	Output admittance	$V_{CE} = 5 V$ $I_{C} = 1 \text{ mA}$ $f = 1 \text{ kHz}$			1	μmho
h <sub>ib</sub>	Input impedance	$V_{CB} = 5 V$ $I_C = 1 mA$ $f = 1 kHz$	25		32	W
C <sub>obo</sub>	Output capacitance (I <sub>E</sub> = 0)	$V_{CB} = 5 \text{ V}$ 100 kHz $\leq f \leq$ 1 MHz			6	pF
NF	Noise figure	$V_{CE} = 5 \text{ V}$ $I_{C} = 10 \mu\text{A}$ $R_{S} = 10 k\Omega$ $f = 1 k\text{Hz}$ Bandwidth = 200 Hz			3	dB
NF	Noise figure	$V_{CE}$ = 5 V $I_{C}$ = 10 μA $R_{S}$ = 10 kΩ 10 Hz ≤ f ≤ 15.7 kHz Bandwidth = 200 Hz			3	dB

<sup>1.</sup> Pulsed duration = 300  $\mu$ s, duty cycle  $\leq$ 1.5%

#### **Electrical characteristics (curves)** 2.1







## 3 Package mechanical data

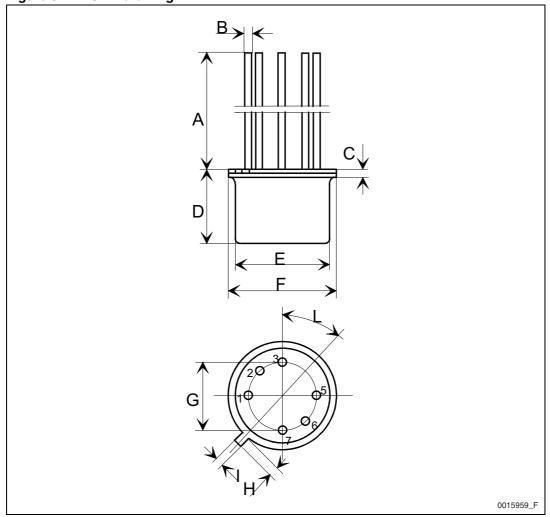
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TO-77 mechanical data

Dim.		mm	
Dilli.	Min.	Тур.	Max.
А	12.70		13.70
В	0.40		0.47
С	0.55		0.76
D	6.11		6.47
Е	8.15		8.25
F	9.05		9.25
G	4.85	5.08	5.31
Н	0.71		0.85
I	0.90		1.00
L	42°		48°

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Figure 5. TO-77 drawing

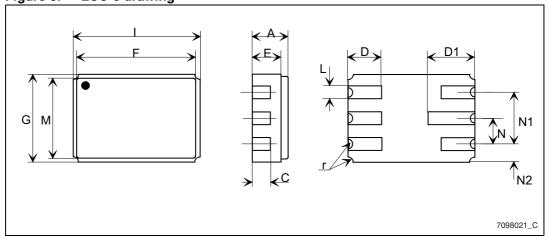


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Table 7. LCC-6 mechanical data

Dim.		mm	
Dilli.	Min.	Тур.	Max.
А	1.53		1.96
С	0.78	0.89	0.99
D	1.52	1.65	1.78
Е	12.4	1.40	1.55
F	5.77	5.84	5.92
G	4.19	4.31	4.45
I	6.10	6.22	6.35
L	0.56	0.63	0.71
M	3.86	3.94	4.01
N	1.14	1.27	1.40
N1	2.41	2.54	2.67
N2	0.64	0.89	1.14
r		0.23	
D1	2.08	2.28	2.49

Figure 6. LCC-6 drawing



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2N2920AHR Revision history

# 4 Revision history

Table 8. Document revision history

Date	Revision	Changes
10-Dec-2008	1	Initial release
05-Jan-2010	2	Modified Table 1 on page 1
15-Nov-2012	3	Added: Section 2.1: Electrical characteristics (curves) Updated: Section 3: Package mechanical data

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