General Purpose Transistors

PNP Silicon

Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-45	V
Collector – Base Voltage	V _{CBO}	-50	V
Emitter – Base Voltage	V _{EBO}	-5.0	V
Collector Current – Continuous	Ι _C	-500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C	P _D	460	mW
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	272	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

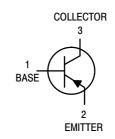
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

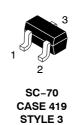
1. FR-4 Board, 1 oz. Cu, 100 mm².



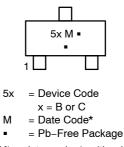
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MARKING DIAGRAM



(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted.)

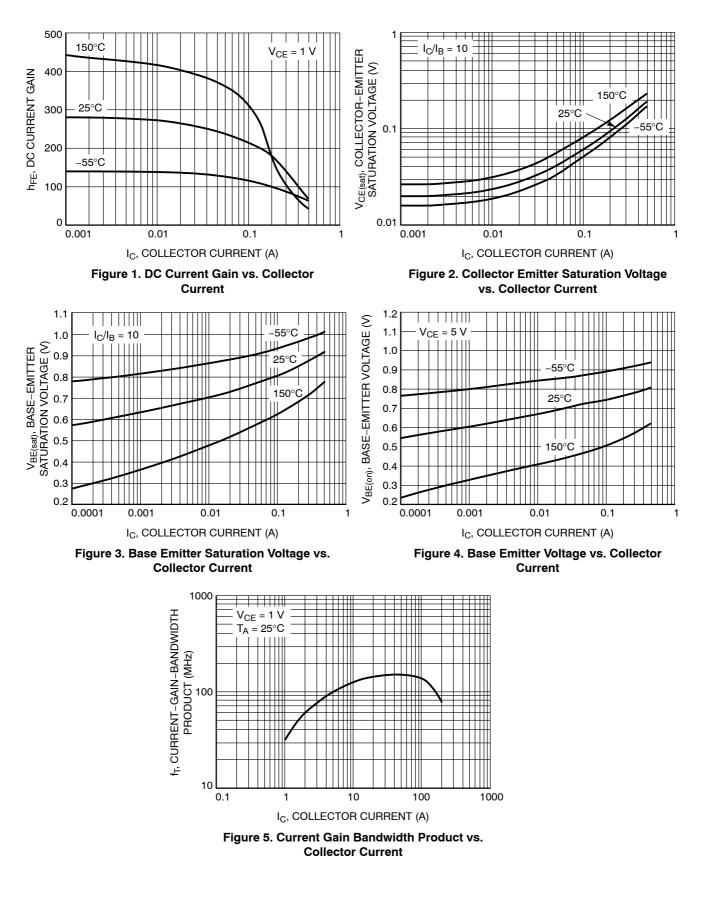
Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage $(I_C = -10 \text{ mA})$		V _{(BR)CEO}	-45	-	-	V
Collector – Emitter Breakdown Voltage (V _{EB} = 0, I_C = –10 μ A)		V _{(BR)CES}	-50	-	-	V
Emitter – Base Breakdown Voltage ($I_E = -1.0 \ \mu A$)		V _{(BR)EBO}	-5.0	-	-	V
Collector Cutoff Current ($V_{CB} = -20 \text{ V}$) ($V_{CB} = -20 \text{ V}$, $T_J = 150^{\circ}\text{C}$)		I _{CBO}			-100 -5.0	nA μA
ON CHARACTERISTICS						
DC Current Gain ($I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{ V}$) ($I_C = -500 \text{ mA}, V_{CE} = -1.0 \text{ V}$)	BC807-25, SBC807-25 BC807-40, SBC807-40	h _{FE}	160 250 40	- - -	400 600 -	_
Collector – Emitter Saturation Voltage ($I_C = -500$ mA, $I_B = -50$ mA)		V _{CE(sat)}	_	-	-0.7	V
Base – Emitter On Voltage ($I_C = -500$ mA, $I_B = -1.0$ V)		V _{BE(on)}	_	-	-1.2	V
SMALL-SIGNAL CHARACTERISTICS						
Current – Gain – Bandwidth Product ($I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, f = 100 MHz)		f _T	100	-	_	MHz
Output Capacitance (V _{CB} = -10 V, f = 1.0 MHz)		C _{obo}	-	10	-	pF

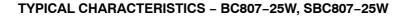
ORDERING INFORMATION

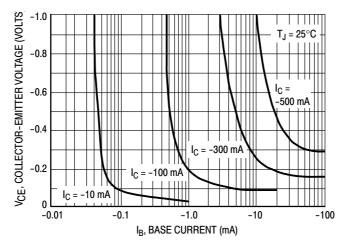
Device	Specific Marking	Package	Shipping [†]	
BC807-25WT1G			2000 / Tana & Daal	
SBC807-25T1G	5B	SC–70 (Pb–Free)	3000 / Tape & Reel	
BC807-25WT3G		(10,000 / Tape & Reel	
BC807-40WT1G		SC-70 (Pb-Free)	3000 / Tape & Reel	
SBC807-40WT1G	5C			
BC807-40WT3G		(* 2 * * * * * *)	10,000 / Tape & Reel	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

TYPICAL CHARACTERISTICS - BC807-25W, SBC807-25W









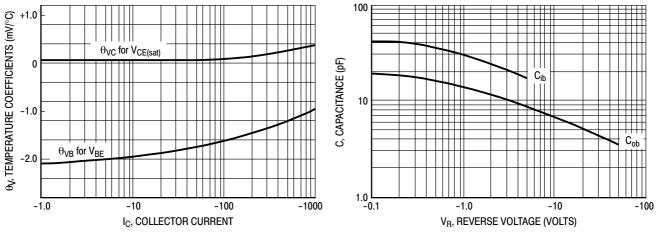
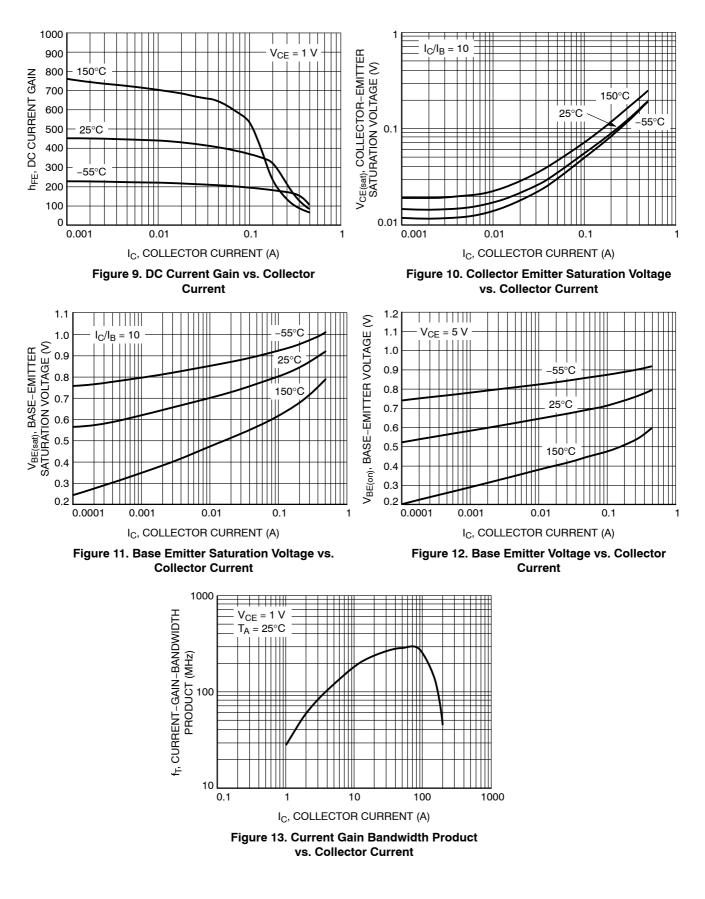
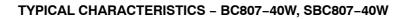


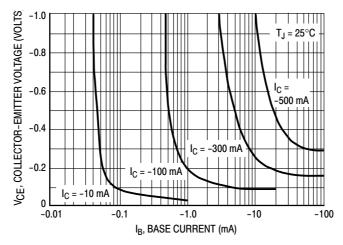
Figure 7. Temperature Coefficients

Figure 8. Capacitances

TYPICAL CHARACTERISTICS - BC807-40W, SBC807-40W









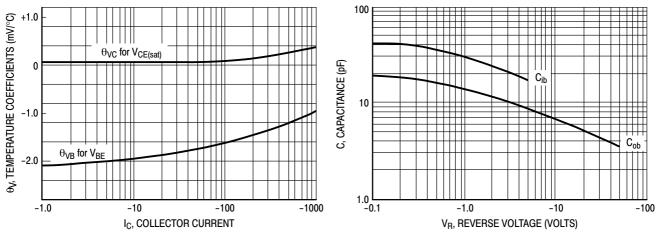


Figure 15. Temperature Coefficients

Figure 16. Capacitances

TYPICAL CHARACTERISTICS - BC807-25W, SBC807-25W, BC807-40W, SBC807-40W

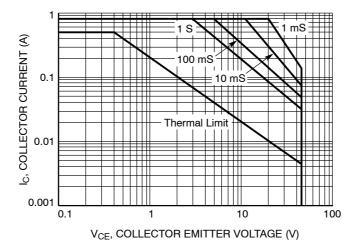


Figure 17. Safe Operating Area

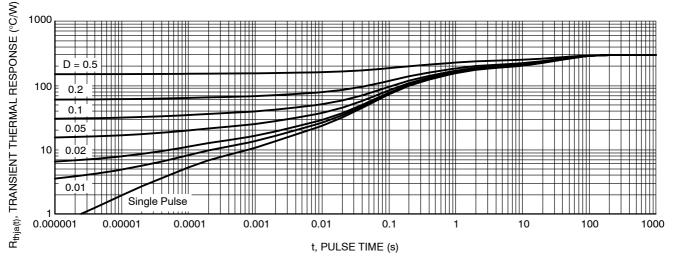
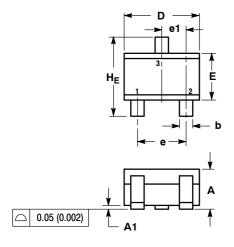
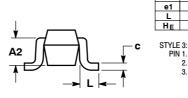


Figure 18. Thermal Response

PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE N



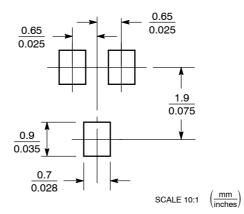


NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH

PIN 1. BASE 2. EMITTER 3. COLLECTOR

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
Ĺ	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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