

MMBT5401WT1G

High Voltage Transistor

PNP Silicon

Features

- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V_{CEO}	-150	Vdc
Collector - Base Voltage	V_{CBO}	-160	Vdc
Emitter - Base Voltage	V_{EBO}	-5.0	Vdc
Collector Current - Continuous	I_C	-500	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

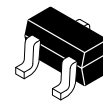
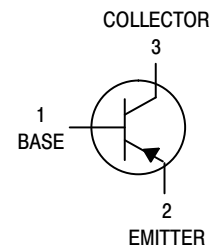
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 2) $T_A = 25^\circ\text{C}$ Derate Above 25°C	P_D	400 3.2	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	312	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

1. FR-5 @ 100 mm², 0.5 oz. copper traces, still air.
2. FR-5 = 1.0 × 0.75 × 0.062 in.



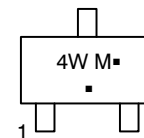
ON Semiconductor®

<http://onsemi.com>



SC-70 (SOT-323)
CASE 419
STYLE 3

MARKING DIAGRAM



4W = Specific Device Code
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
MMBT5401WT1G	SC-70 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage (I _C = –1.0 mA _{dc} , I _B = 0)	V _{(BR)CEO}	–150	–	V _{dc}
Collector–Base Breakdown Voltage (I _C = –100 μA _{dc} , I _E = 0)	V _{(BR)CBO}	–160	–	V _{dc}
Emitter–Base Breakdown Voltage (I _E = –10 μA _{dc} , I _C = 0)	V _{(BR)EBO}	–5.0	–	V _{dc}
Collector–Base Cutoff Current (V _{CB} = –120 V _{dc} , I _E = 0) (V _{CB} = –120 V _{dc} , I _E = 0, T _A = 100°C)	I _{CBO}	–	–50	nA _{dc} μA _{dc}

ON CHARACTERISTICS

DC Current Gain (I _C = –1.0 mA _{dc} , V _{CE} = –5.0 V _{dc}) (I _C = –10 mA _{dc} , V _{CE} = –5.0 V _{dc}) (I _C = –50 mA _{dc} , V _{CE} = –5.0 V _{dc})	h _{FE}	50 60 50	– 240 –	–
Collector–Emitter Saturation Voltage (I _C = –10 mA _{dc} , I _B = –1.0 mA _{dc}) (I _C = –50 mA _{dc} , I _B = –5.0 mA _{dc})	V _{CE(sat)}	– –	–0.2 –0.5	V _{dc}
Base–Emitter Saturation Voltage (I _C = –10 mA _{dc} , I _B = –1.0 mA _{dc}) (I _C = –50 mA _{dc} , I _B = –5.0 mA _{dc})	V _{BE(sat)}	– –	–1.0 –1.0	V _{dc}

SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product (I _C = –10 mA _{dc} , V _{CE} = –10 V _{dc} , f = 100 MHz)	f _T	100	300	MHz
Output Capacitance (V _{CB} = –10 V _{dc} , I _E = 0, f = 1.0 MHz)	C _{obo}	–	6.0	pF
Small Signal Current Gain (I _C = –1.0 mA _{dc} , V _{CE} = –10 V _{dc} , f = 1.0 kHz)	h _{fe}	40	200	–
Noise Figure (I _C = –200 μA _{dc} , V _{CE} = –5.0 V _{dc} , R _S = 10 Ω, f = 1.0 kHz)	NF	–	8.0	dB

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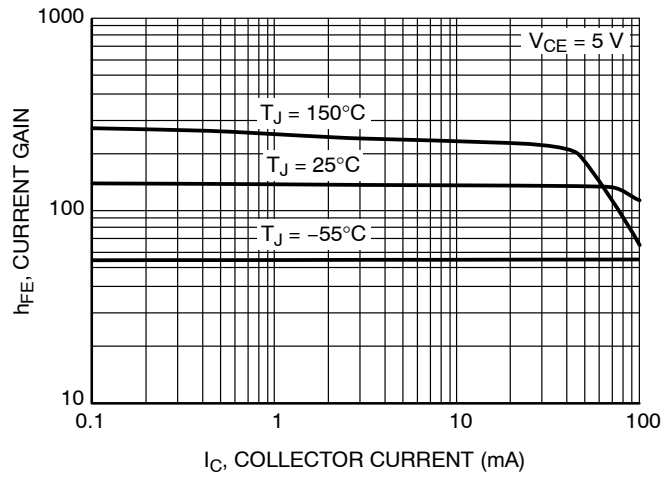


Figure 1. DC Current Gain

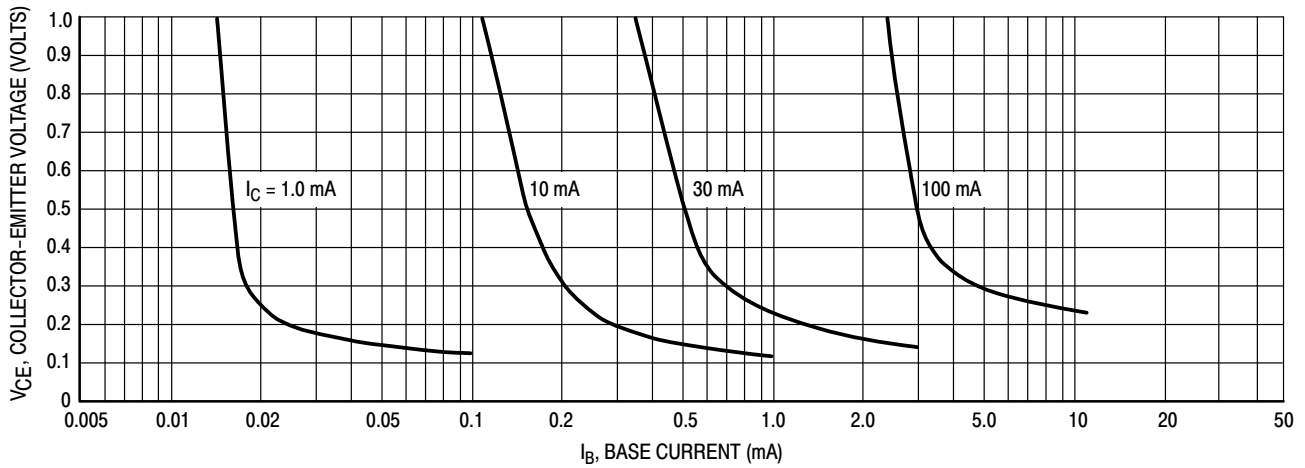


Figure 2. Collector Saturation Region

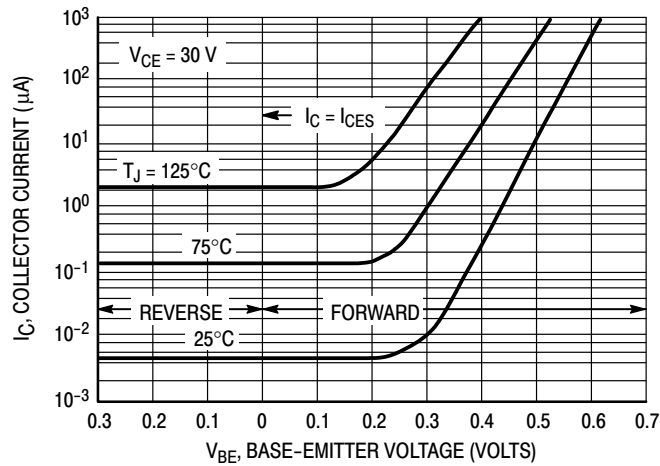


Figure 3. Collector Cut-Off Region

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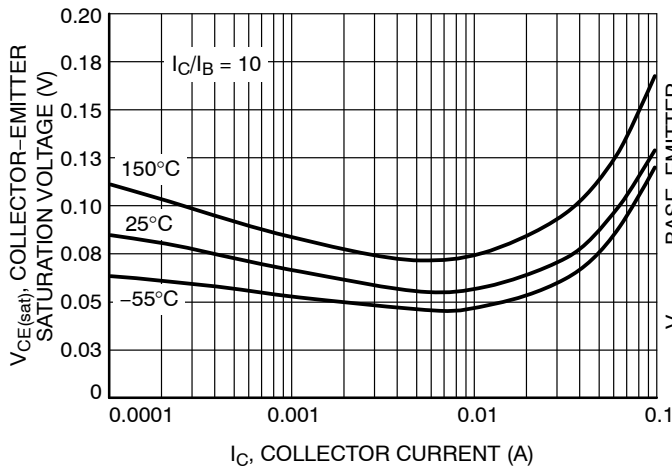


Figure 4. Collector Emitter Saturation Voltage vs. Collector Current

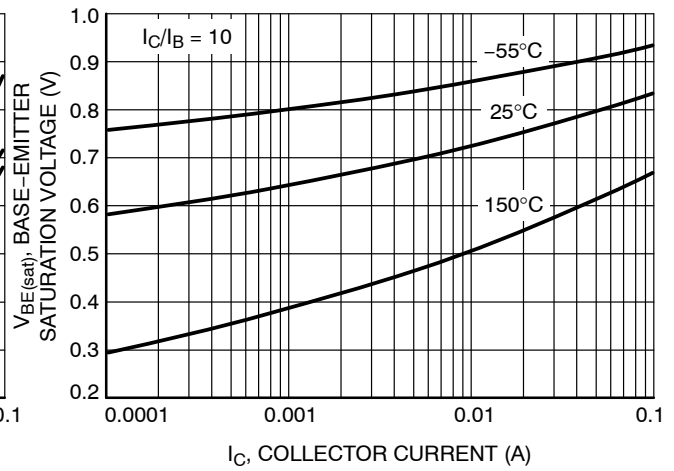


Figure 5. Base Emitter Saturation Voltage vs. Collector Current

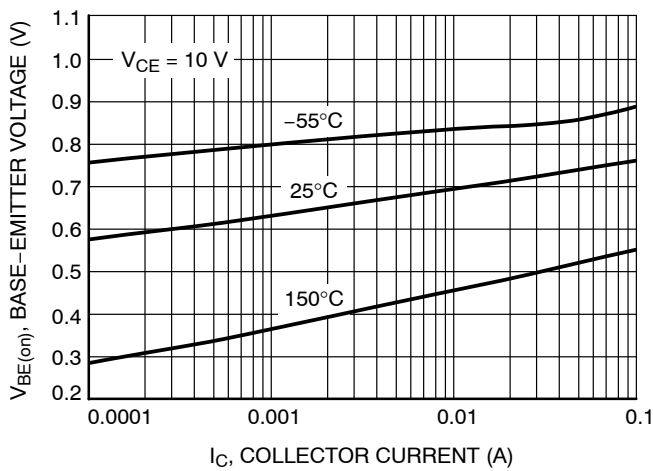


Figure 6. Base Emitter Voltage vs. Collector Current

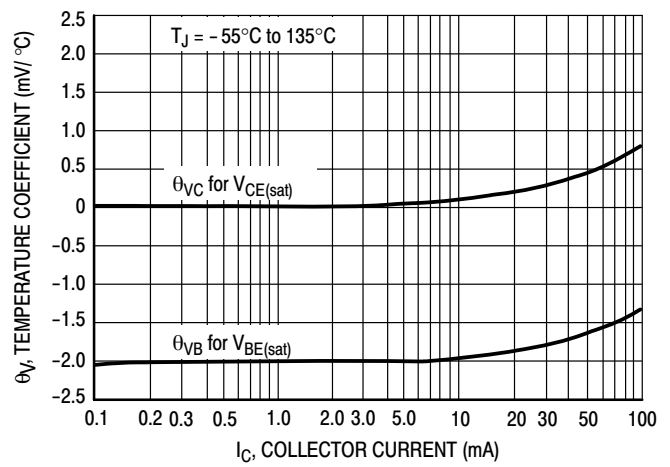
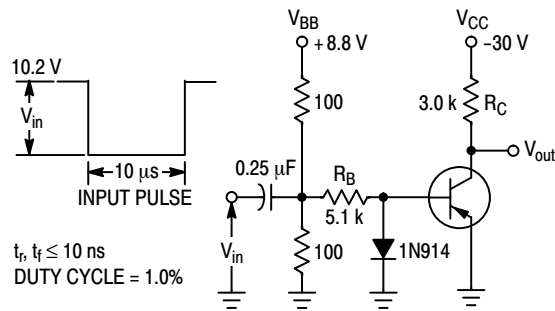


Figure 7. Temperature Coefficients



Values Shown are for I_C @ 10 mA

Figure 8. Switching Time Test Circuit

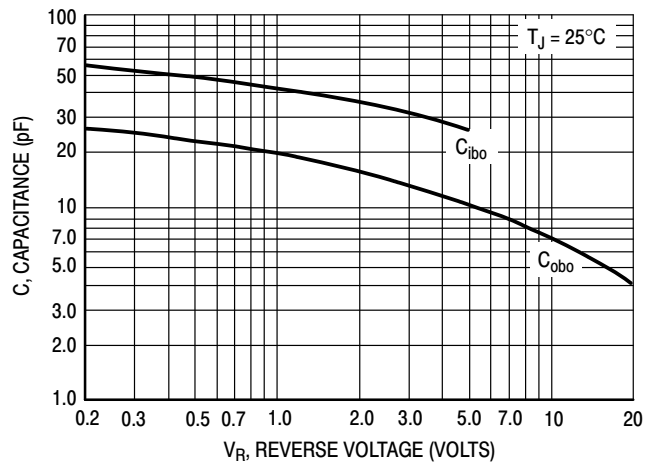


Figure 9. Capacitances

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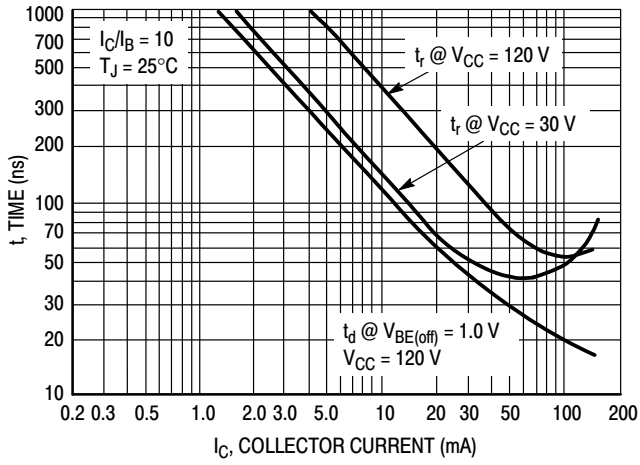


Figure 10. Turn-On Time

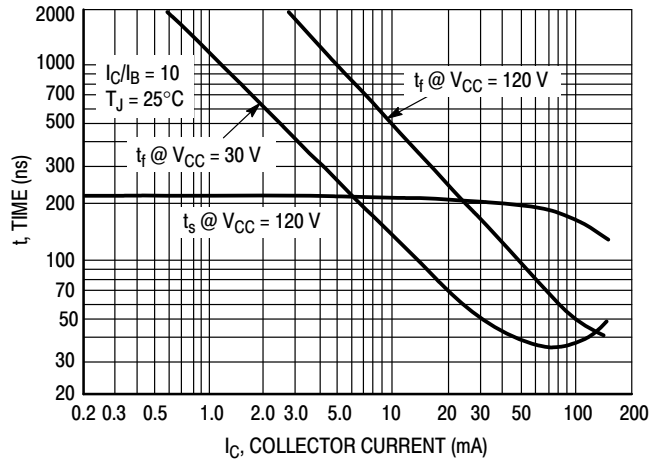


Figure 11. Turn-Off Time

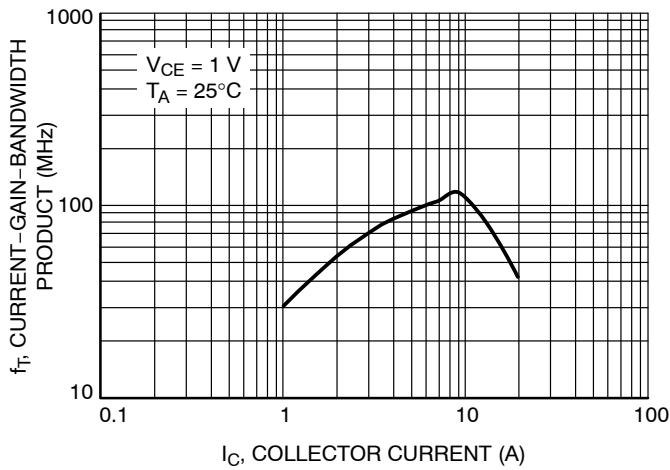


Figure 12. Current Gain Bandwidth Product

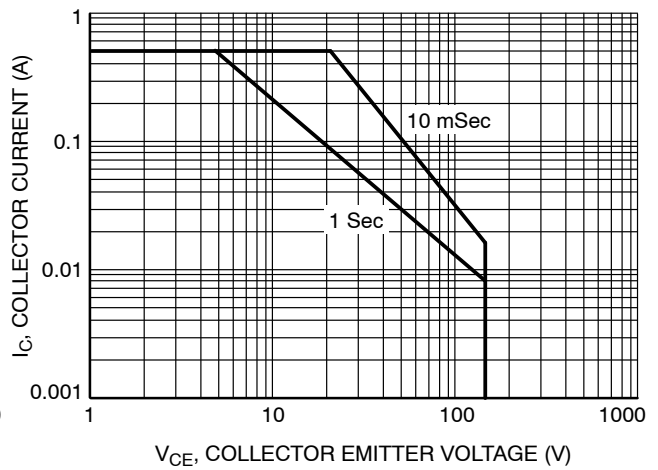
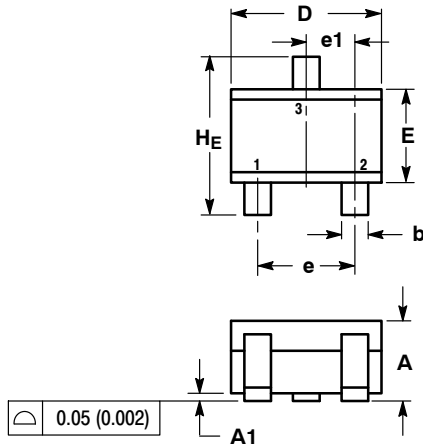


Figure 13. Safe Operating Area

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PACKAGE DIMENSIONS

SC-70 (SOT-323)
CASE 419-04
ISSUE N



NOTES:

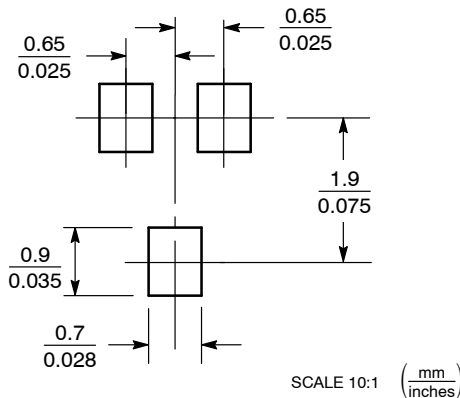
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095

STYLE 3:

1. BASE
2. EMITTER
3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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