

STX817A

PNP Medium power transistor

General features

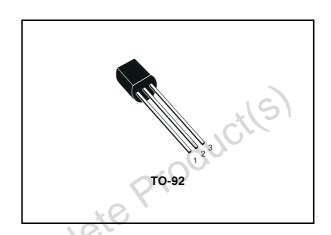
- TO-92 package suitable for through-hole PCB assembly
- In compliance with the 2002/93/EC European Directive

Applications

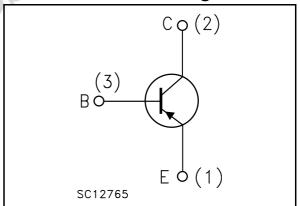
- Voltage regulation
- Relay driver
- Generic switch



The STX817A is a PNP transistor manufactured using Planar Technology resulting in rugged high performance devices.



Internal schematic diagram

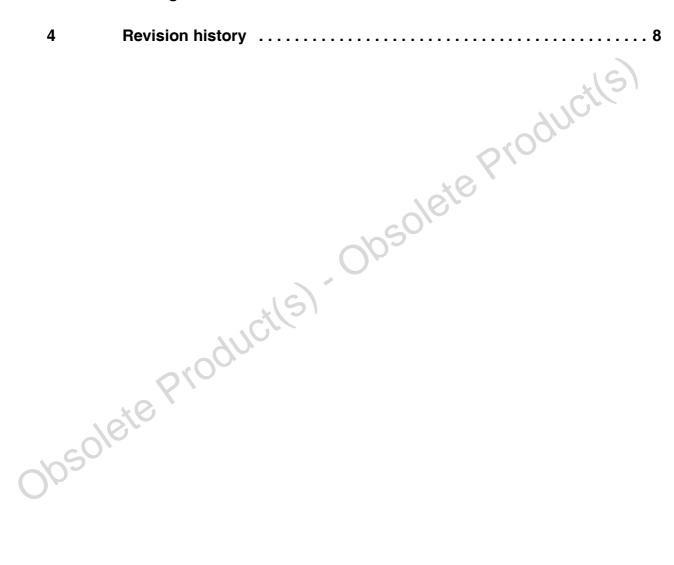


Order codes

Part Number	Marking	Package	Packing
STX817A	X817A	TO-92	Bulk
STX817A-AP	X817A	TO-92 AP	Ammopack

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STX817A Electrical ratings

1 Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	-80	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-80	٧
V _{EBO}	Emitter-base voltage (I _C = 0)	-5	٧
I _C	Collector current	-1.5	Α
I _{CM}	Collector peak current (t _P < 5ms)	-2	А
I _B	Base current	-0.3	Α
I _{BM}	Base peak current (t _P < 5ms)	-0.6	Α
P _{tot}	Total dissipation at T _{amb} = 25°C	0.9	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 2. Thermal data

	Symbol	Parameter		Value	Unit
	R _{thj-amb}	Thermal resistance junction-amb	max	139	°C/W
1501F	ie P	rodució			
Op					

Electrical characteristics STX817A

2 Electrical characteristics

(T_{case} = 25°C unless otherwise specified)

Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} =0)	V _{CE} =-80V			-500	μА
I _{CEO}	Collector cut-off current (I _B =0)	V _{CE} =-80V			-1 ,	mA
I _{EBO}	Emitter cut-off current (I _C =0)	V _{EB} =-5V			-100	μА
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B =0)	I _C =-10mA	-80	5		V
v (1)	Collector-emitter	I _C =-100mA I _B =-10mA			-0.25	V
V _{CE(sat)} (1)	saturation voltage	$I_C = -1A$ $I_B = -100 \text{mA}$			-0.5	V
v (1)	Base-emitter saturation	I _C =-100mA I _B =-10mA			-1	V
V _{BE(sat)} (1)	voltage	$I_C = -1A$ $I_B = -100 \text{mA}$			-1.1	V
		I _C =-100mA V _{CE} =-2V	140			
h _{FE} ⁽¹⁾	DC current gain	$I_C = -500 \text{mA}$ $V_{CE} = -2 \text{V}$	80			
	.15)	$I_C = -1A$ $V_{CE} = -2V$	25			
f _t	Transition frequency	$I_C = -0.1A$ $V_{CE} = -10V$		50		MHz

Note (1) Pulsed duration = 300μs, duty cycle ≤1.5%

3 Package mechanical data

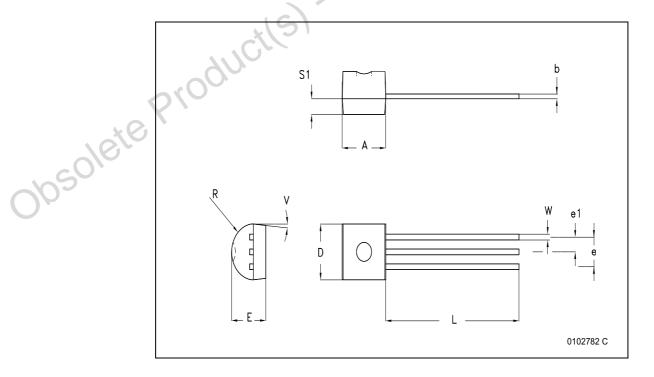
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

Obsolete Product(s). Obsolete Product(s)

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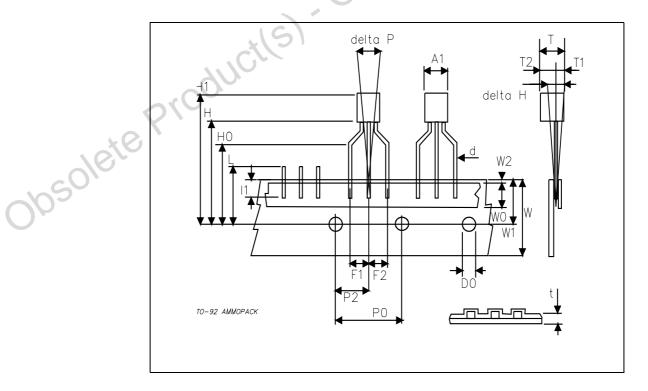
TO-92 BULK SHIPMENT MECHANICAL DATA

	mm.				
DIM.	MIN.	TYP	MAX.		
Α	4.32		4.95		
b	0.36		0.51		
D	4.45		4.95		
Е	3.30		3.94		
е	2.41		2.67		
e1	1.14		1.40		
L	12.70	Y	15.49		
R	2.16	10,10	2.41		
S1	0.92	20/0	1.52		
W	0.41	102	0.56		
V		5 °			



TO-92 AMMOPACK SHIPMENT (Suffix"-AP") MECHANICAL DATA

DIM.	mm.				
DIN.	MIN.	TYP	MAX.		
A1			4.80		
Т			3.80		
T1			1.60		
T2			2.30		
d			0.48		
P0	12.50	12.70	12.90		
P2	5.65	6.35	7.05		
F1,F2	2.44	2.54	2.94		
delta H	-2.00		2.00		
W	17.50	18.00	19.00		
W0	5.70	6.00	6.30		
W1	8.50	9.00	9.25		
W2			0.50		
Н	18.50		20.50		
H0	15.50	16.00	16.50		
H1		40,	25.00		
D0	3.80	4.00	4.20		
t			0.90		
L		<0°	11.00		
I1	3.00	102			
delta P	-1.00		1.00		



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Revision history STX817A

4 Revision history

Table 4. Revision history

Date	Revision	Changes	
06-July-2004	1	Initial release.	
22-Jan-2006	2	The minimum hfe value has been modified on page 4.	



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