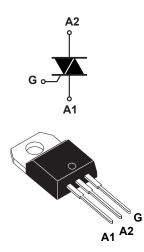




Datasheet

30 A - 800 V - 150 °C 8H Triac in TO-220AB insulated



TO-220AB insulated

Product status link					
T3035H-8I					
Product	Product summary				
I _{T(RMS)}	30 A				
V _{DRM} /V _{RRM}	800 V				
V _{DSM} /V _{RSM}	900 V				
I _{GT}	35 mA				
1					

Features

- 30 A high current Triac
- 800 V symmetrical blocking voltage
- 150 °C maximum junction temperature T_i
- Three triggering quadrants
- High noise immunity static dV/dt
- Robust dynamic turn-off commutation (dl/dt)c
- ECOPACK2 compliant component
- Comply with UL1557 insulation: 2.5 kV – Reference file: E81734

Applications

- Home automation Smart AC plug
- Water heater, room heater and coffee machine
- AC Induction and Universal Motor control
- Inrush current limiter in AC DC rectifiers
- Lighting and automation I/O control
- General purpose AC line load control

Description

Specifically designed to operate at 800 V and 150 °C, the T3035H-8I Triac housed in TO-220AB insulated provides an enhanced thermal management: this 30 A Triac is the right choice for a compact drive of heavy AC loads and enables the heatsink size reduction.

Based on the ST Snubberless high temperature technology, it offers higher specified turn off commutation and noise immunity levels up to the T_i max.

The T3035H-8I safely optimizes the control of the hardest universal motors, heaters and inductive loads for industrial control and home appliances.

By using an internal ceramic pad, it provides a recognized voltage insulation, rated at 2500 ${\rm V}_{\rm RMS}.$

1 Characteristics

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Symbol	Parameter	Value	Unit	
I _{T(RMS)}	RMS on-state current (full sine wave)	T _c = 91 °C	30	А
I	Non repetitive surge peak on-state current (full cycle,	t = 16.7 ms	283	А
ITSM	T _j initial = 25 °C)	t = 20 ms	270	A
l ² t	I ² t value for fusing	t _p = 10 ms	482	A ² s
dl/dt	Critical rate of rise of on-state current, I_G = 2 x I_{GT} , tr ≤ 100 ns, f = 100 Hz	100	A/µs	
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage	800	V	
V _{DSM} /V _{RSM}	Non Repetitive peak off-state voltage	epetitive peak off-state voltage $t_p = 10 \text{ ms}, T_j = 25 \text{ °C}$		V
I _{GM}	Peak gate current	4	А	
P _{GM}	Maximum gate power dissipation	t _p = 20 μs, Τ _j = 150 °C	5	W
P _{G(AV)}	Average gate power dissipation	T _j = 150 °C	1	W
T _{stg}	Storage temperature range		-40 to +150	°C
Тj	Operating junction temperature range	-40 to +150	°C	
TL	Maximum lead temperature for soldering during 10 s		260	°C
V _{INS}	Insulation RMS voltage, 1 minute		2.5	kV

Table 2. Electrical characteristics (T_j = 25 °C, unless otherwise specified)

Symbol	Test conditio	Quadrants		Value	Unit	
	V_D = 12 V, R _L = 30 Ω		1 - 11 - 111	Min.	5	mA
I _{GT}	V_D = 12 V, R _L = 30 Ω	V_D = 12 V, R _L = 30 Ω		Max.	35	mA
V _{GT}	V_D = 12 V, R _L = 30 Ω		1 - 11 - 111	Max.	1.3	V
V _{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	T _j = 150 °C	1 - 11 - 111	Min.	0.15	V
IL.	$I_{G} = 1.2 \times I_{GT}$		1 - 111	Max.	75	mA
۰Ľ	$I_L I_G = 1.2 \times I_{GT}$		II	Max.	90	mA
I _H ⁽¹⁾	I _T = 500 mA, gate open		Max.	60	mA	
dV/dt (1)	V _D = 536 V, gate open	T _j = 150 °C	Min.	2000	V/µs	
(dl/dt)c ⁽¹⁾	Without snubber network	T _j = 150 °C	Min.	25	A/ms	

1. For both polarities of A2 referenced to A1.

Table 3. Static characteristics

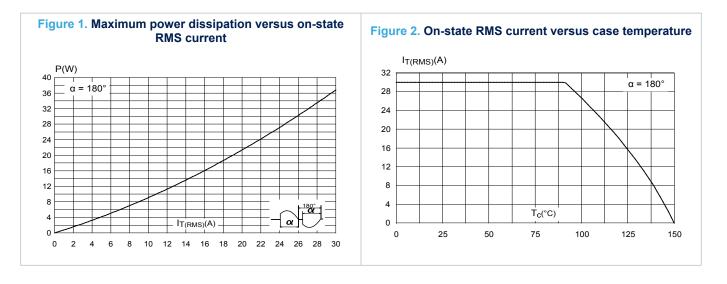
Symbol	Test conditions	Тј		Value	Unit
V _{TM} ⁽¹⁾	I _T = 42 A, t _p = 380 μs	25 °C	Max.	1.55	V
V _{TO} ⁽¹⁾	Threshold voltage	150 °C	Max.	0.83	V
R _D ⁽¹⁾	Dynamic resistance	150 °C	Max.	16	mΩ
	$V_{D} = V_{R} = V_{DRM} = V_{RRM}$	25 °C	Max.	2.5	μA
I _{DRM} /I _{RRM}	$\nabla D = \nabla R = \nabla D R M = \nabla R R M$	150°C	Wax.	8.5	mA
	$V_D = V_R = 400 V$, peak voltage	150 °C	Max.	3.6	mA

1. For both polarities of A2 referenced to A1.

Table 4. Thermal resistance

Symbol	Parameter	Value	Unit	
R _{th(j-c)}	Junction to case (AC)	Max.	1.6	°C/W
R _{th(j-a)}	Junction to ambient	Тур.	60	°C/W

1.1 Characteristics (curves)



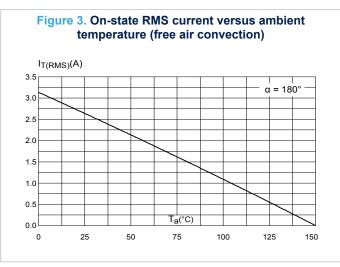
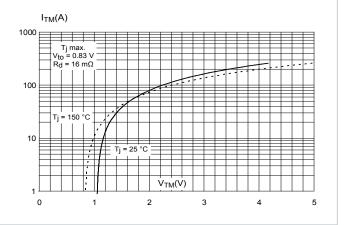
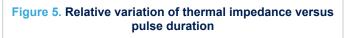
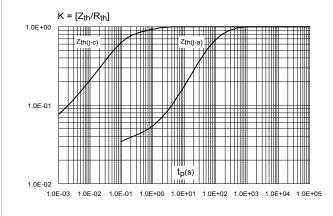


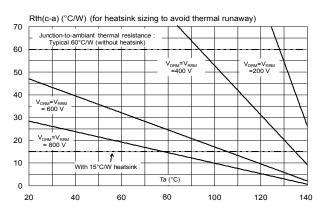
Figure 4. On-state characteristics (maximum values)











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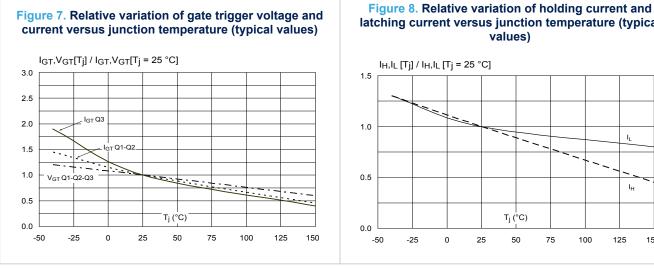
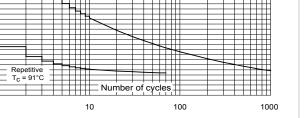


Figure 9. Surge peak on-state current versus number of cycles ITSM(A) 300 250 Non repetitive Tj initial = 25 °C 200 150 100



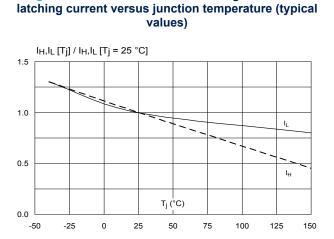
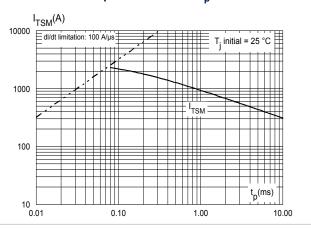
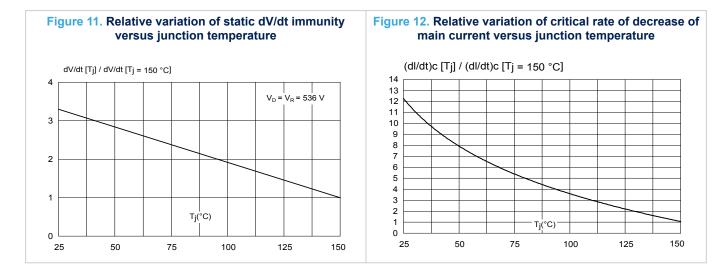


Figure 10. Non repetitive surge peak on-state current for a sinusoidal pulse with width t_p < 10 ms





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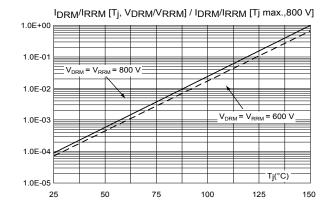


Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage

2 Package information

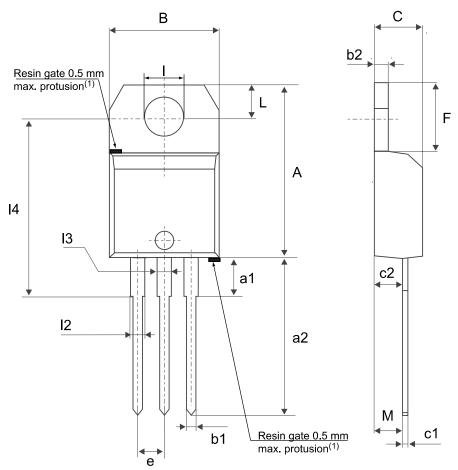
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In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 TO-220AB insulated package information

- Epoxy resin is halogen free and meets UL94 flammability standard, level V0
- Lead-free plating package leads
- Recommended torque: 0.4 to 0.6 N·m





(1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

Table 5. TO-220AB insulated package mechanical data

		Dimensions				
Ref.	Millimeters				Inches ⁽¹⁾	
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	15.20		15.90	0.5984		0.6260

	Dimensions						
Ref.		Millimeters		Inches ⁽¹⁾			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
a1		3.75			0.1476		
a2	13.00		14.00	0.5118		0.5512	
В	10.00		10.40	0.3937		0.4094	
b1	0.61		0.88	0.0240		0.0346	
b2	1.23		1.32	0.0484		0.0520	
С	4.40		4.60	0.1732		0.1811	
c1	0.49		0.70	0.0193		0.0276	
c2	2.40		2.72	0.0945		0.1071	
е	2.40		2.70	0.0945		0.1063	
F	6.20		6.60	0.2441		0.2598	
I	3.73		3.88	0.1469		0.1528	
L	2.65		2.95	0.1043		0.1161	
12	1.14		1.70	0.0449		0.0669	
13	1.14		1.70	0.0449		0.0669	
14	15.80	16.40	16.80	0.6220	0.6457	0.6614	
М		2.6			0.1024		

1. Inch dimensions are for reference only.

3 Ordering information

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Figure 15. Ordering information scheme

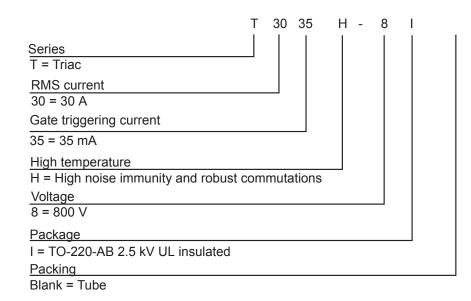


Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
T3035H-8I	T3035H-8I	TO-220AB Ins.	2.3 g	50	Tube

Revision history

Date	Version	Changes
27-Jul-2018	1	Initial release.
24-Jun-2019	2	Minor text changed.
20-Dec-2019	3	Inserted Figure 10.
15-Jan-2020	4	Updated Table 6.
21-Dec-2020	5	Updated general description.

Table 7. Document revision history

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