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FAIRCHILD

SEMICONDUCTOR TM

KSD880

Low Frequency Power Amplifier Complement to KSB834



KSD880

1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum	Ratings	T _C =25°C unless otherwise noted
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Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	60	V
V _{CEO}	Collector-Emitter Voltage	60	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current	3	А
I _B	Base Current	0.3	А
P _C	Collector Dissipation (T _C =25°C)	30	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

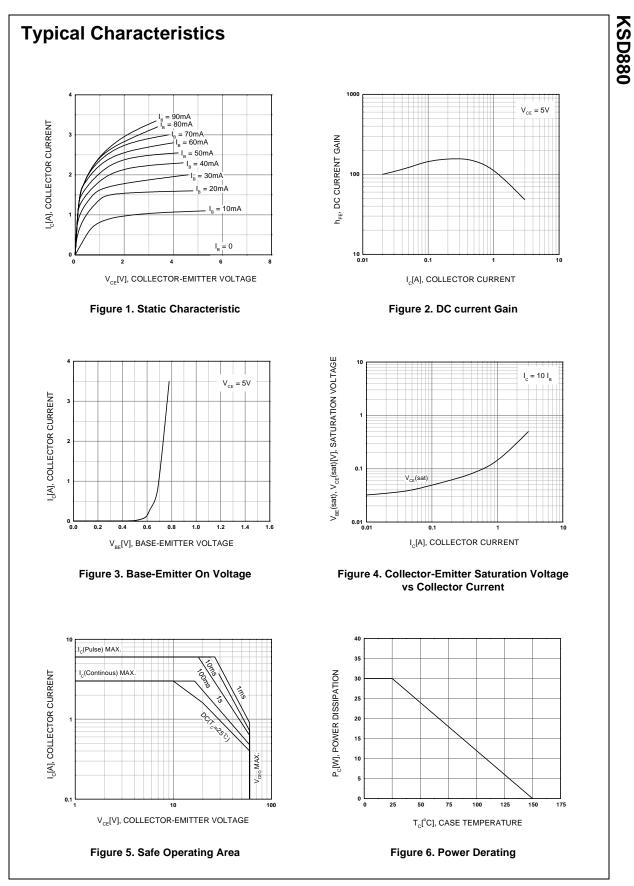
Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = 60V, I_E = 0$			100	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 7V, I_{C} = 0$			100	μA
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 50 {\rm mA}, I_{\rm B} = 0$	60			V
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = 5V, I_{C} = 0.5A$ $V_{CE} = 5V, I_{C} = 3A$	60 20		300	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 3A, I _B = 0.3A		0.4	1	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = 5V, I_{C} = 0.5A$		0.7	1	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.5A$		3		MHz
C _{ob}	Output Capacitance	$V_{CB} = 10V, I_E = 0, f = 1MHz$		70		pF
t _{ON}	Turn ON Time	$V_{CC} = 30V, I_{C} = 1A$		0.8		μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 0.2A$		1.5		μs
t _F	Fall Time	$R_L = 30\Omega$		0.8		μs

h_{FE} Classification

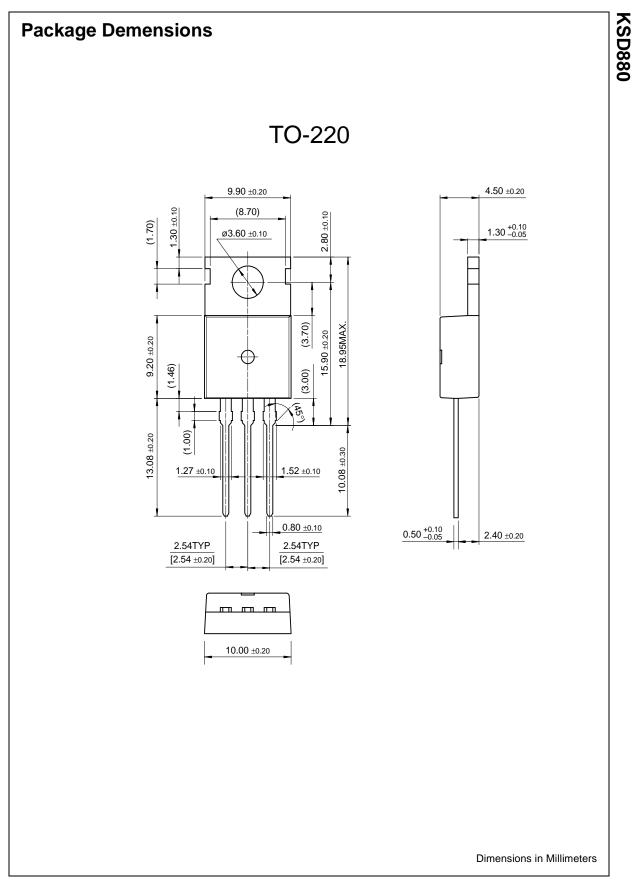
Classification	0	Y	G
h _{FE1}	60 ~ 120	100 ~ 200	150 ~ 300

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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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