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November 2014

BC817 / BC818 NPN Epitaxial Silicon Transistor

Features

- · Switching and Amplifier Applications
- · Suitable for AF-Driver Stages and Low Power Output Stages
- · Complement to BC807 / BC808



Ordering Information(1)

Part Number	Marking	Package	Packing Method	
BC81716MTF	8FA	SOT-23 3L	Tape and Reel	
BC81725MTF	8FB	SOT-23 3L	Tape and Reel	
BC81740MTF	8FC	SOT-23 3L	Tape and Reel	
BC81816MTF	8GA	SOT-23 3L	Tape and Reel	
BC81825MTF	8GB	SOT-23 3L	Tape and Reel	
BC81840MTF	8GC	SOT-23 3L	Tape and Reel	

Note:

1. Affix "-16,-25,-40" means h_{FE} classification. Affix "-M" means the matte type package. Affix "-TF" means the tape and reel type packing.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter		Value	Unit
V _{CBO} Collector-Bas	Callector Page Voltage	BC817	50	V
	Collector-base voltage	BC818	30	
V _{CEO} Coll	Callagter Emitter Valtage	BC817	45	V
	Collector-Emitter Voltage	BC818	25	V
V _{EBO}	Emitter-Base Voltage		5	V
I _C	Collector Current (DC)		800	mA
TJ	Junction Temperature		150	°C
T _{STG}	Storage Temperature		-65 to +150	°C

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Thermal Characteristics(1)

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
D	Power Dissipation	310	mW
P _D	Derate Above 25°C	2.48	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	403	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics(2)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	BC817	I _C = 10 mA, I _B = 0	45			V
		BC818		25			
BV _{CES} Collector-Emitter Bre Voltage	Collector-Emitter Breakdown	BC817	I _C = 0.1 mA, V _{BE} = 0	50			V
	Voltage	BC818		30			
BV _{EBO}	Emitter-Base Breakdown Voltage		I _E = 0.1 mA, I _C = 0	5			V
I _{CES}	Collector Cut-Off Current		V _{CE} = 25 V, V _{BE} = 0			100	nA
I _{EBO}	Emitter Cut-Off Current		$V_{EB} = 4 \text{ V}, I_{C} = 0$			100	nA
h _{FE1}	h _{FE1} DC Current Gain		$V_{CE} = 1 \text{ V, } I_{C} = 100 \text{ mA}$	100		630	
h _{FE2}			$V_{CE} = 1 \text{ V, } I_{C} = 300 \text{ mA}$	60			
V _{CE} (sat)	Collector-Emitter Saturation Voltage		I _C = 500 mA, I _B = 50 mA			0.7	V
V _{BE} (on)	Base-Emitter On Voltage		$V_{CE} = 1 \text{ V, } I_{C} = 300 \text{ mA}$			1.2	V
f _T	Current Gain Bandwidth Product		V _{CE} = 5 V, I _C = 10 mA, f = 50 MHz		100		MHz
C _{ob}	Output Capacitance		V _{CB} = 10 V, f = 1 MHz			12	pF

Note:

2. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2%

h_{FE} Classification

Classification	16	25	40
h _{FE1}	100 ~ 250	160 ~ 400	250 ~ 630
h _{FE2}	60 ~	100 ~	170 ~

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Typical Performance Characteristics

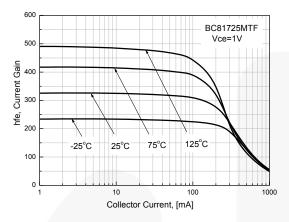


Figure 1. DC Current Gain

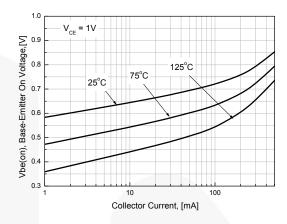


Figure 2. Base-Emitter On Voltage

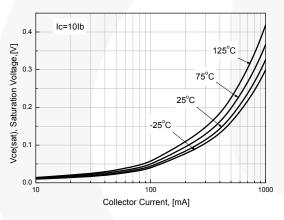


Figure 3. Collector-Emitter Saturation Voltage

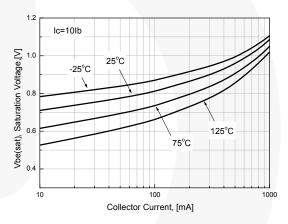


Figure 4. Base-Emitter Saturation Voltage

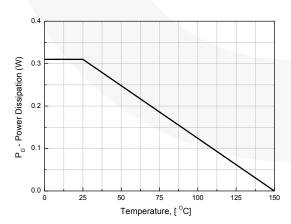


Figure 5. Power Dissipation vs Ambient Temperature

Physical Dimensions 0.95 2.92±0.20 3 1.40 1.30+0.20 2.20 2 0.60 0.37 (0.29) -0.95 ⊕ 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A -1.20 MAX 0.10 (0.93) ○ 0.10 M C С 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. 0.23 B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994. 0.20 MIN SEATING E) DRAWING FILE NAME: MA03DREV10 **PLANE** (0.55)**DETAIL A** Figure 6. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE





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