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February 2012

ISL9V5045S3ST_F085 EcoSPARK® N-Channel Ignition IGBT

500mJ, 450V

Features

- SCIS Energy = 500mJ at T_J = 25°C
- Logic Level Gate Drive
- Qualified to AEC Q101
- RoHS Compliant

Applications

- Automotive Ignition Coil Driver Circuits
- Coil On Plug Applications

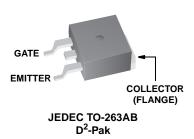
General Description

The ISL9V5045S3ST_F085 is next generation ignition IGBT that offer outstanding SCIS capability in the industry standard D2-Pak (TO-263) plastic package. This device is intended for use in automotive ignition circuits, specifically as a coil drivers. Internal diodes provide voltage clamping without the need for external components.

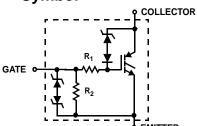
EcoSPARK® devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.



Package



Symbol



Device Maximum Ratings $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Ratings	Units
BV _{CER}	Collector to Emitter Breakdown Voltage (I _C = 1 mA)	480	V
BV _{ECS}	Emitter to Collector Voltage - Reverse Battery Condition (I _C = 10 mA)	24	V
E _{SCIS25}	At Starting $T_J = 25$ °C, $I_{SCIS} = 39.2$ A, $L = 650 \mu Hy$	500	mJ
E _{SCIS150}	At Starting $T_J = 150$ °C, $I_{SCIS} = 31.1$ A, $L = 650 \mu Hy$	315	mJ
I _{C25}	Collector Current Continuous, At T _C = 25°C, See Fig 9	51	Α
I _{C110}	Collector Current Continuous, At T _C = 110°C, See Fig 9	43	Α
V_{GEM}	Gate to Emitter Voltage Continuous	±10	V
P _D	Power Dissipation Total T _C = 25°C	300	W
	Power Dissipation Derating T _C > 25°C	2	W/°C
T _J	Operating Junction Temperature Range	-40 to 175	°C
T _{STG}	Storage Junction Temperature Range	-40 to 175	°C
T _L	Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)	300	°C
T _{pkg}	Max Lead Temp for Soldering (Package Body for 10s)	260	°C
ESD	Electrostatic Discharge Voltage at 100pF, 1500Ω	4	kV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
V5045S	ISL9V5045S3ST_F085	TO-263AB	330mm	24mm	800

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Collector to Emitter Saturation Voltage

Collector to Emitter Saturation Voltage

Symbol	Parameter	Test Conditions		Min	Тур	Max	Units
ff State	Characteristics						
BV _{CER}	Collector to Emitter Breakdown Voltage	I_C = 2mA, V_{GE} = 0, R_G = 1KΩ, See Fig. 15 $T_{.1}$ = -40 to 150°C		420	450	480	V
BV _{CES}	Collector to Emitter Breakdown Voltage	$I_C = 10$ mA, $V_{GE} = 0$, $R_G = 0$, See Fig. 15 $T_J = -40$ to 150°C		445	475	505	V
BV _{ECS}	Emitter to Collector Breakdown Voltage	$I_C = -75 \text{mA}, V_{GE} = 0 \text{V},$ $T_C = 25 ^{\circ}\text{C}$		30	-	-	V
BV _{GES}	Gate to Emitter Breakdown Voltage	I _{GES} = ± 2mA		±12	±14	-	V
I _{CER}	Collector to Emitter Leakage Current	to Emitter Leakage Current $V_{CER} = 320V, R_G = 1K\Omega, See$ Fig. 11		-	-	25	μA
			T _C = 150°C	-	-	1	mA
I _{ECS}	Emitter to Collector Leakage Current	$V_{EC} = 24V$, See	$T_C = 25$ °C	-	-	1	mA
		Fig. 11	$T_{\rm C} = 150^{\circ}{\rm C}$	-	-	40	mA
R ₁	Series Gate Resistance			-	100	-	Ω
R_2	Gate to Emitter Resistance			10K	-	30K	Ω

 $V_{CE(SAT)}$

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1.25

1.47

1.60

1.80

 $I_C = 10A$,

I_C = 15A,

 $V_{GE} = 4.0V$

 $V_{GE} = 4.5V$

 $T_C = 25$ °C,

See Fig. 4

T_C = 150°C

Dynamic Characteristics

Q _{G(ON)}	Gate Charge	I _C = 10A, V _{CE} = 12V, V _{GE} = 5V, See Fig. 14		-	32	-	nC
V _{GE(TH)}	Gate to Emitter Threshold Voltage	$I_C = 1.0 \text{mA},$	T _C = 25°C	1.3	-	2.2	V
		V _{CE} = V _{GE,} See Fig. 10	$T_C = 150$ °C	0.75	-	1.8	V
V _{GEP}	Gate to Emitter Plateau Voltage	I _C = 10A,	V _{CE} = 12V	-	3.0	-	V

Switching Characteristics

t _{d(ON)R}	Current Turn-On Delay Time-Resistive	$V_{CE} = 14V, R_L = 1\Omega,$	-	0.7	4	μs
t _{rR}	Current Rise Time-Resistive	V_{GE} = 5V, R_G = 1K Ω T_J = 25°C, See Fig. 12	-	2.1	7	μs
t _{d(OFF)L}	Current Turn-Off Delay Time-Inductive	$V_{CE} = 300V, L = 2mH,$	-	10.8	15	μs
t _{fL}	Current Fall Time-Inductive	V_{GE} = 5V, R_G = 1K Ω T_J = 25°C, See Fig. 12	-	2.8	15	μs
SCIS	Self Clamped Inductive Switching	T_J = 25°C, L = 650 μ H, R_G = 1K Ω , V_{GE} = 5V, See Fig. 1 & 2	-	-	500	mJ

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction-Case	TO-263	-	-	0.5	°C/W

Typical Characteristics

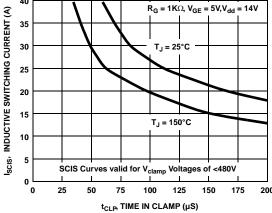


Figure 1. Self Clamped Inductive Switching
Current vs Time in Clamp

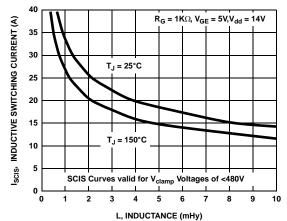
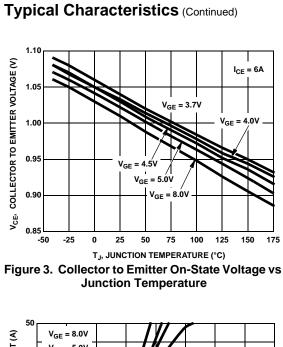


Figure 2. Self Clamped Inductive Switching Current vs Inductance



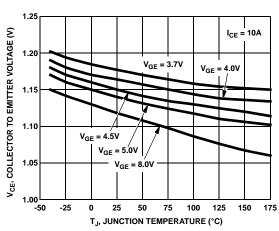
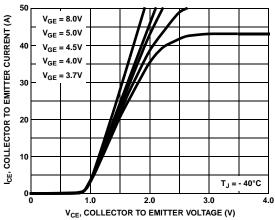


Figure 4.Collector to Emitter On-State Voltage vs **Junction Temperature**



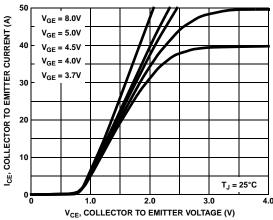
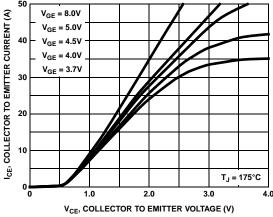


Figure 5. Collector Current vs Collector to Emitter **On-State Voltage**

Figure 6. Collector Current vs Collector to Emitter **On-State Voltage**



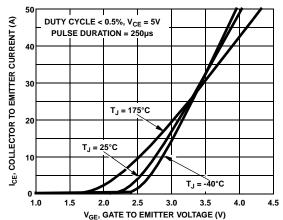
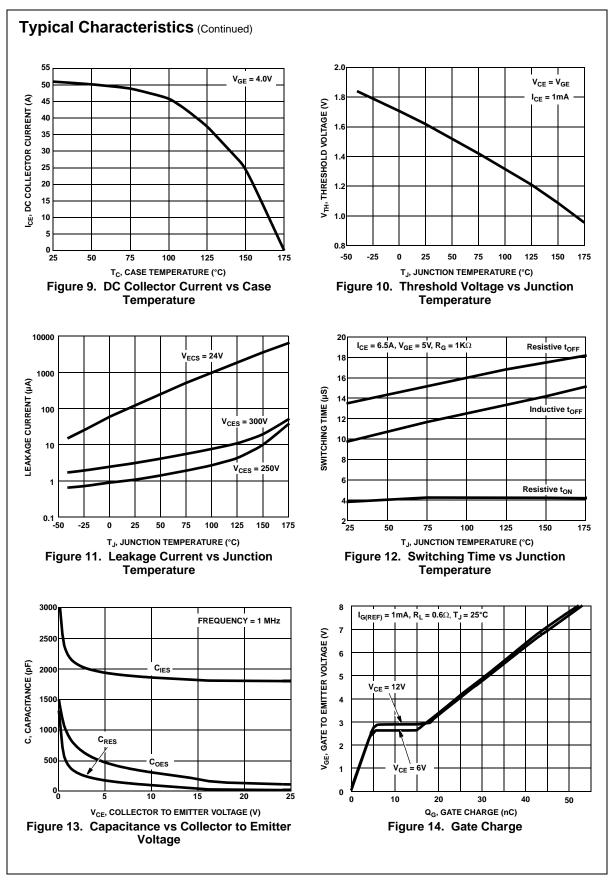


Figure 7. Collector to Emitter On-State Voltage vs **Collector Current**

Figure 8. Transfer Characteristics



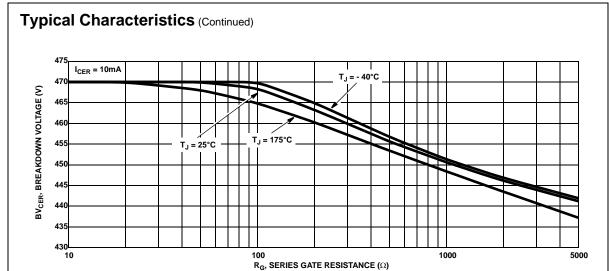


Figure 15. Breakdown Voltage vs Series Gate Resistance

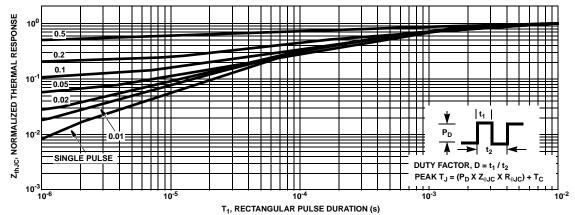


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

Test Circuits and Waveforms

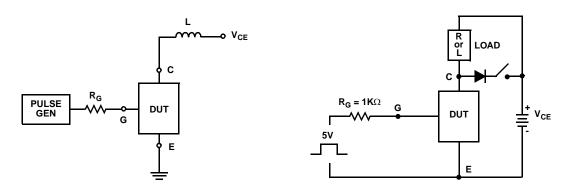
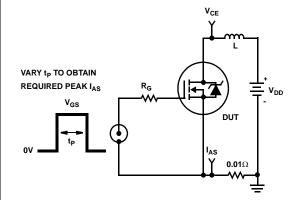


Figure 17. Inductive Switching Test Circuit

Figure 18. t_{ON} and t_{OFF} Switching Test Circuit

Test Circuits and Waveforms (Continued)





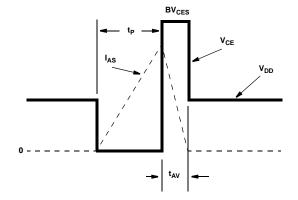


Figure 20. Energy Waveforms

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