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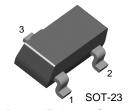
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## BCX70J

### **General Purpose Transistor**



### 1. Base 2. Emitter 3. Collector

# **NPN Epitaxial Silicon Transistor**

## **Absolute Maximum Ratings** $T_a$ =25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	45	V
V <sub>CEO</sub>	Collector-Emitter Voltage	45	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current	200	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	-55 ~ 150	°C

Refer to KST3904 for graphs

## **Electrical Characteristics** $T_a$ =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =2.0mA, I <sub>B</sub> =0	45		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> =1.0μA, I <sub>C</sub> =0	5		V
I <sub>CES</sub>	Collector Cut-off Current	V <sub>CE</sub> =32V, V <sub>BE</sub> =0		20	nA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB}=4V$ , $I_{C}=0$		20	nA
h <sub>FE</sub>	DC Current Gain	$V_{CE}$ =5V, $I_{C}$ =10 $\mu$ A $V_{CE}$ =5V, $I_{C}$ =2.0mA $V_{CE}$ =1V, $I_{C}$ =50mA	40 250 90	460	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =10mA, I <sub>B</sub> =0.25mA I <sub>C</sub> =50mA, I <sub>B</sub> =1.25mA		0.35 0.55	V V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> =10mA, I <sub>B</sub> =0.25mA I <sub>C</sub> =50mA, I <sub>B</sub> =1.25mA	0.6 0.7	0.85 1.05	V V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	I <sub>C</sub> =2.0mA, V <sub>CE</sub> =5V	0.55	0.75	V
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> =10mA, V <sub>CE</sub> =5V, f=100MHz	125		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz		4.5	pF
NF	Noise Figure	$V_{CE}$ =5V, $I_{C}$ =0.2mA $R_{S}$ =2K $\Omega$ , f=1KHz		6	dB
t <sub>ON</sub>	Turn On Time	I <sub>C</sub> =10mA, I <sub>B1</sub> =1.0mA		150	ns
t <sub>OFF</sub>	Turn Off Time	$V_{BB}$ =3.6V, $I_{B2}$ =1.0mA $R_1$ = $R_2$ =5K $\Omega$ , $R_L$ =990 $\Omega$		800	ns

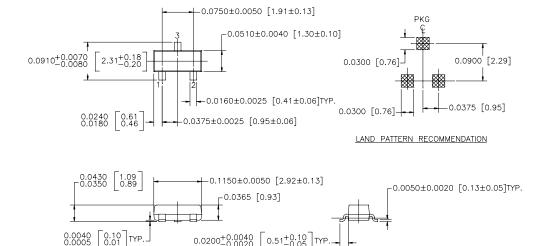
Marking



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# **Package Dimensions**

# **SOT-23**



NOTE: UNLESS OTHERWISE SPECIFIED

- 1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42
- 2. REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE G, DATED JUL 1993

Dimensions in Millimeters

SOT 23, 3 LEADS LOW PROFILE

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EnSigna™	$I^2C^{TM}$	$OCX^{TM}$	RapidConfigure™	UHC™
Across the board.	Around the world.™	OCXPro™	RapidConnect™	UltraFET <sup>®</sup>
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