

STN2NE06

N-CHANNEL 60V - 0.18Ω - 2A - SOT-223 STripFETTM POWER MOSFET

TYPE	V _{DSS}	R _{DS(on)}	Ι _D
STN2NE06	60 V	< 0.25 Ω	2 A

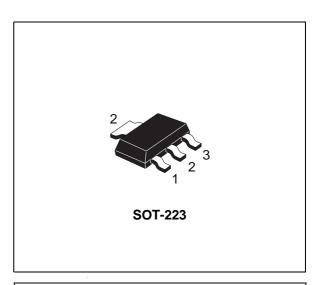
- TYPICAL R_{DS(on)} = 0.18 Ω
- EXCEPTIONAL dv/dt CAPABILITY
- AVALANCHE RUGGED TECHNOLOGY
- 100 % AVALANCHE TESTED
- APPLICATION ORIENTED CHARACTERIZATION

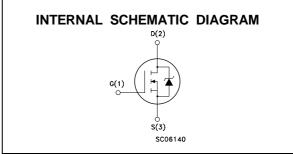
DESCRIPTION

This Power Mosfet is the latest development of STMicroelectronics unique "Single Feature SizeTM" stip-based process. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

APPLICATIONS

- DC MOTOR CONTROL (DISK DRIVES,etc.)
- DC-DC & DC-AC CONVERTERS
- SYNCHRONOUS RECTIFICATION





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source Voltage (V _{GS} = 0)	60	V
V_{DGR}	Drain- gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)	60	V
V_{GS}	Gate-source Voltage	± 20	V
I_D	Drain Current (continuous) at T _c = 25 °C	2	Α
I_D	Drain Current (continuous) at T _c = 100 °C	1.3	Α
I _{DM} (•)	Drain Current (pulsed)	8	Α
P _{tot}	Total Dissipation at T _c = 25 °C	2.5	W
	Derating Factor	0.02	W/°C
dv/dt(1)	Peak Diode Recovery voltage slope	6	V/ns
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

^(•) Pulse width limited by safe operating area

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⁽¹⁾ $I_{SD} \le 8$ A, $di/dt \le 200$ A/ μ s, $V_{DD} \le V_{(BR)DSS}$, $T_j \le T_{JMAX}$

THERMAL DATA

R _{thj-pcb} R _{thi-amb}	Thermal Resistance Junction-PC Board Thermal Resistance Junction-ambient	Max Max	50 60	°C/W °C/W
,	(Surface Mounted)			
T _I	Maximum Lead Temperature For Soldering Purpose)	260	°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max)	2	А
E _{AS}	Single Pulse Avalanche Energy (starting $T_j = 25$ °C, $I_D = I_{AR}$, $V_{DD} = 25$ V)	20	mJ

ELECTRICAL CHARACTERISTICS ($T_{case} = 25$ $^{\circ}C$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	$I_D = 250 \ \mu A$ $V_{GS} = 0$	60			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	$V_{DS} = Max Rating$ $V_{DS} = Max Rating$ $T_c = 125 ^{\circ}C$			1 10	μΑ μΑ
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 20 V			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250 \mu A$	2	3	4	>
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10 V I _D = 1 A		0.18	0.25	Ω
I _{D(on)}	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10 \text{ V}$	2			Α

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
gfs (*)	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 1 A$		1.8		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25 \text{ V}$ f = 1 MHz $V_{GS} = 0 \text{ V}$		310 45 12.5	420 61 17	pF pF pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r	Turn-on Time Rise Time	$V_{DD} = 30 \text{ V}$ $I_{D} = 1 \text{ A}$ $R_{G} = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$		9 10	13 13.5	ns ns
$egin{array}{c} Q_g \ Q_{gs} \ Q_{gd} \end{array}$	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 40 \text{ V}$ $I_D = 2 \text{ A}$ $V_{GS} = 10 \text{ V}$		12 5.1 2.7	17 7 4	nC nC nC

SWITCHING OFF

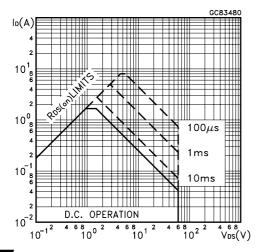
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _f	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 48 \text{ V}$ $I_D = 2 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$		4.5 5 12	6 7 16	ns ns ns

SOURCE DRAIN DIODE

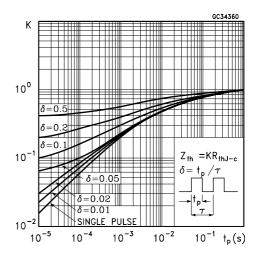
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{SD} I _{SDM} (∙)	Source-drain Current Source-drain Current (pulsed)				2 8	A A
V _{SD} (*)	Forward On Voltage	$I_{SD} = 2 A V_{GS} = 0$			1.2	V
t _{rr}	Reverse Recovery Time	$I_{SD} = 2 \text{ A}$ di/dt = 100 A/ μ s $V_{DD} = 30 \text{ V}$ $T_i = 150 ^{\circ}\text{C}$		40		ns
Q_{rr}	Reverse Recovery Charge	,		50		nC
I_{RRM}	Reverse Recovery Current			2.5		A

^(*) Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

Safe Operating Area



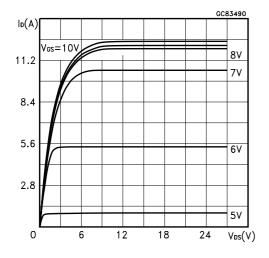
Thermal Impedance



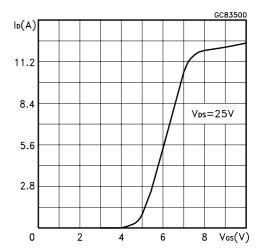
^(•) Pulse width limited by safe operating area

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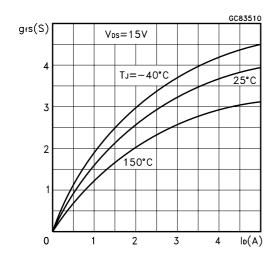
Output Characteristics



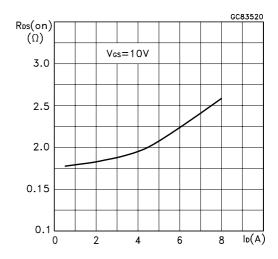
Transfer Characteristics



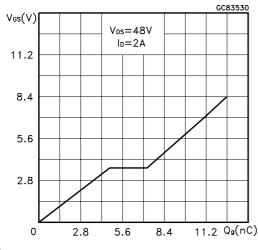
Transconductance



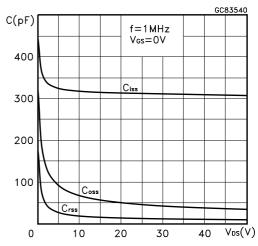
Static Drain-source On Resistance



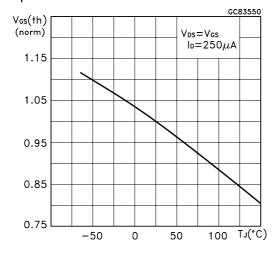
Gate Charge vs Gate-source Voltage



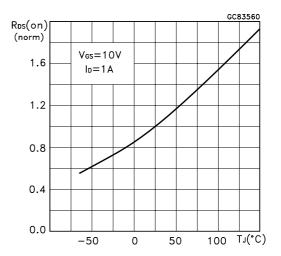
Capacitance Variations



Normalized Gate Threshold Voltage vs Temperature



Normalized On Resistance vs Temperature



Source-drain Diode Forward Characteristics

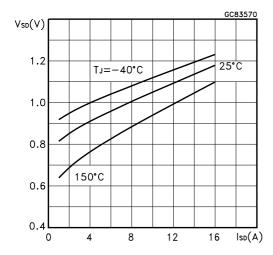


Fig. 1: Unclamped Inductive Load Test Circuit

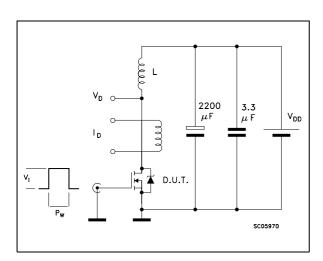


Fig. 3: Switching Times Test Circuits For Resistive Load

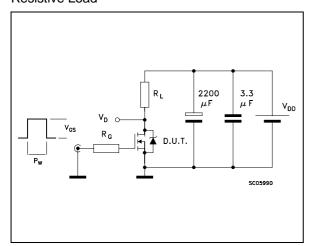


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times

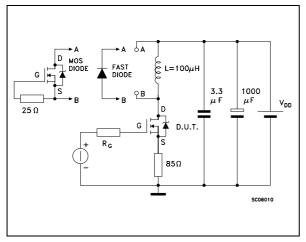


Fig. 2: Unclamped Inductive Waveform

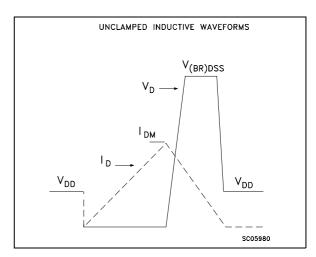
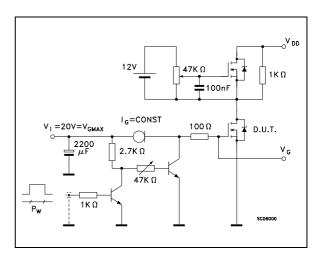
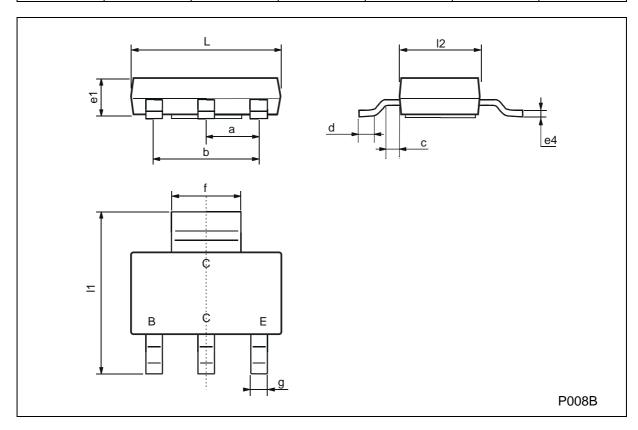


Fig. 4: Gate Charge test Circuit



SOT-223 MECHANICAL DATA

DIM.		mm			mils			
2 .	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
а	2.27	2.3	2.33	89.4	90.6	91.7		
b	4.57	4.6	4.63	179.9	181.1	182.3		
С	0.2	0.4	0.6	7.9	15.7	23.6		
d	0.63	0.65	0.67	24.8	25.6	26.4		
e1	1.5	1.6	1.7	59.1	63	66.9		
e4			0.32			12.6		
f	2.9	3	3.1	114.2	118.1	122.1		
g	0.67	0.7	0.73	26.4	27.6	28.7		
I1	6.7	7	7.3	263.8	275.6	287.4		
12	3.5	3.5	3.7	137.8	137.8	145.7		
L	6.3	6.5	6.7	248	255.9	263.8		



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