

# STTH5L04DEE

# Turbo 2 ultrafast recovery diode

#### Datasheet - production data

## Features

- Very low switching losses
- High frequency and high pulse current operation
- Low thermal resistance
- High junction temperature
- ECOPACK<sup>®</sup>2 compliant component

## Description

The STTH5L04 series uses ST's new 400 V planar Pt doping technology. The STTH5L04 is specially suited for switching mode base drive and transistor circuits.

Packaged in PowerFLAT<sup>™</sup>, this device is intended for use in low profile applications.

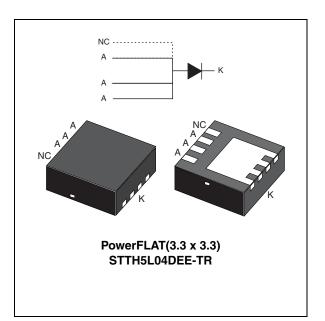


Table 1. Device summary

Symbol	Value
I <sub>F(AV)</sub>	5 A
V <sub>RRM</sub>	400 V
T <sub>j</sub> (max)	150 °C
V <sub>F</sub> (typ)	0.85 V
T <sub>RR</sub> (typ)	35 ns

TM: PowerFLAT is a trademark of STMicroelectronics

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This is information on a product in full production.

# 1 Characteristics

# Table 2.Absolute ratings (limiting values $T_{amb} = 25$ °C unless otherwise specified)

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Repetitive peak reverse voltage	400	V	
I <sub>F(RMS)</sub>	Forward rms current		15	А
I <sub>F(AV)</sub>	Average forward current	5	А	
I <sub>FSM</sub>	Surge non repetitive forward current	60	А	
T <sub>stg</sub>	Storage temperature range	-65 to +150	°C	
Тj	Maximum operating junction tempera	150	°C	

## Table 3. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case	4.5	°C/W
R <sub>th(j-a)</sub>	Junction to ambient on printed circuit board (with recommended footprint dimension, copper thickness = $35 \ \mu m$ )	250	°C/W

### Table 4. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I <sub>B</sub> <sup>(1)</sup>	Reverse leakage	T <sub>j</sub> = 25 °C	V <sub>R</sub> = V <sub>RRM</sub>	-		2.5	μΑ
'R`	current	T <sub>j</sub> = 125 °C	VR − VRRM	-	2.5	25	μA
V <sub>F</sub> <sup>(2)</sup> Forward voltage drop		T <sub>j</sub> = 25 °C	I <sub>F</sub> = 5A		1.05	1.25	V
VF.	Torward voltage drop	T <sub>j</sub> = 150 °C		-	0.85	1.05	v

1. Pulse test:  $t_p = 5 \text{ ms}, \delta < 2\%$ 

2. Pulse test:  $t_p = 380 \ \mu s, \ \delta < 2\%$ 

To evaluate the conduction losses use the following equation:

 $P = 0.85 \text{ x } I_{F(AV)} + 0.04 \text{ x } {I_{F}}^{2}_{(RMS)}$ 

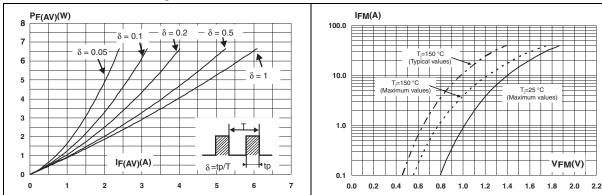
### Table 5. Dynamic electrical characteristics

Symbol	Parameter	Test conditions			Тур.	Max.	Unit
I <sub>RM</sub>	Reverse recovery current	T <sub>i</sub> = 125 °C	$T_j = 125 \text{ °C} \begin{vmatrix} I_F = 5A, V_R = 320 \text{ V}, \\ dI_F/dt = -200 \text{ A/}\mu\text{s} \end{vmatrix}$		8	11	А
S <sub>factor</sub>	Softness factor	,	αι <sub>F</sub> /αt = -200 Α/μs	-	0.7		
+	Reverse recovery time	T <sub>i</sub> = 25 °C	I <sub>F</sub> = 1A, V <sub>R</sub> = 30 V, dI <sub>F</sub> /dt = -50 A/μs		43	60	ns
t <sub>rr</sub>	neverse recovery time	$r_j = 25 \ C$	I <sub>F</sub> = 1A, V <sub>R</sub> = 30 V, dI <sub>F</sub> /dt = -100 A/μs	-	35	50	115
t <sub>fr</sub>	Forward recovery time	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 5 A, V <sub>FB</sub> = 1.2 V			110	ns
V <sub>FP</sub>	Forward recovery voltage	T <sub>j</sub> = 25 °C	$dl_{\rm F}/dt = 100  {\rm A}/{\mu {\rm s}}$		2	3	v



# Figure 1. Average forward power dissipation Figure 2. versus average forward current





### Figure 3. Relative variation of thermal impedance junction to case versus pulse duration

Figure 4. Peak reverse recovery current versus dl<sub>F</sub>/dt (typical values)

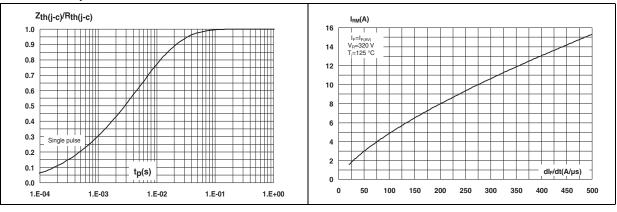
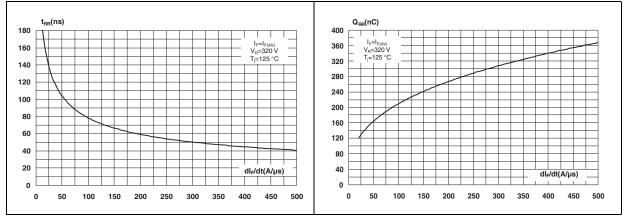


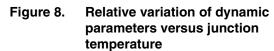
Figure 5. Reverse recovery time versus dl<sub>F</sub>/dt Figure 6. (typical values)

Reverse recovery charges versus dl<sub>F</sub>/dt (typical values)





# Figure 7. Reverse recovery softness factor versus dl<sub>F</sub>/dt (typical values)



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 $\begin{matrix} I_F=I_{F(AV)}\\ V_R=320 \ V \\ Reference: T_j=125 \ ^{\circ}C \end{matrix}$ 

125

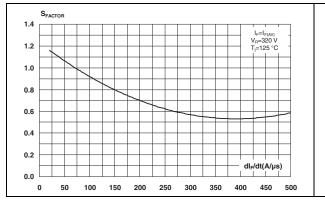
100

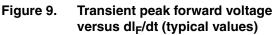
\_\_\_\_ S<sub>FACTOR</sub> \_\_\_\_\_

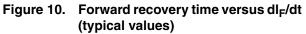
Q<sub>RR</sub>

50

-

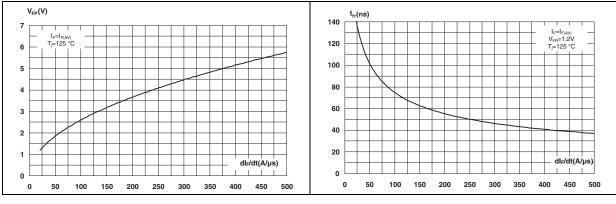






Tj(°C)

75



1.4

1.2

1.0

0.8

0.6

0.4

0.2

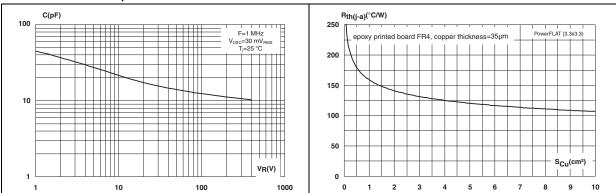
0.0

25

. . .

Figure 11. Junction capacitance versus reverse voltage applied (typical values)

Figure 12. Thermal resistance junction to ambient versus copper surface under tab



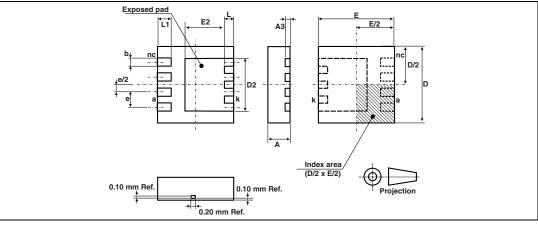


# 2 Package information

- Epoxy meets UL94,V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com.* ECOPACK<sup>®</sup> is an ST trademark.

Figure 13. PowerFLAT (3.3 x 3.3) dimensions (definitions)

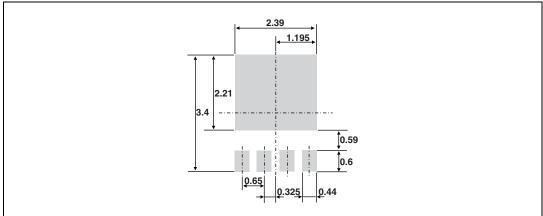


### Table 6. PowerFLAT (3.3 x 3.3) dimensions (values)

	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
A	0.95		1.0	0.037		0.039	
A3		0.2			0.008		
b	0.29	0.34	0.39	0.011	0.013	0.015	
D	3.20	3.30	3.40	0.126	0.130	0.134	
D2	2.24	2.29	2.34	0.088	0.090	0.092	
E	3.20	3.30	3.40	0.126	0.130	0.134	
E2	1.66	1.71	1.76	0.065	0.067	0.069	
e		0.65			0.026		
L		0.40			0.016		
L1	0.45	0.50	0.55	0.018	0.20	0.22	









# **3** Ordering information

## Table 7.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STTH5L04DEE-TR	TH5L04	PowerFLAT (3.3 x 3.3)	34 mg	3000	Tape and reel 13" reel

# 4 Revision history

## Table 8.Document revision history

Date	Revision	Changes
11-Sep-2012	1	First issue.



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8/8

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