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<u>Silicon Carbide (SiC)</u> <u>MOSFET</u> – 20 mohm, 900 V, M2, D2PAK-7L NVBG020N090SC1

Features

- Typ. $R_{DS(on)} = 20 \text{ m}\Omega @ V_{GS} = 15 \text{ V}$
- Typ. $R_{DS(on)} = 16 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge (typ. $Q_{G(tot)} = 200 \text{ nC}$)
- Low Effective Output Capacitance (typ. Coss = 295 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- Automotive On Board Charger
- Automotive DC-DC Converter for EV/HEV

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

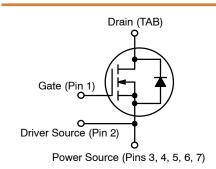
Dara	meter		Symbol	Value	Unit
	-				
Drain-to-Source Voltage			V _{DSS}	900	V
Gate-to-Source Voltag	ge		V _{GS}	+22/-8	V
Recommended Operat Values of Gate-Source		T _C < 175°C	V _{GSop}	+15/-5	V
Continuous Drain Current $R_{\theta JC}$ (Note 2)	Steady State	T _C = 25°C	Ι _D	112	A
Power Dissipation $R_{\theta JC}$ (Note 2)			PD	477	W
Continuous Drain Current R _{0JA} (Notes 1, 2)	Steady State	$T_A = 25^{\circ}C$	۱ _D	9.8	A
Power Dissipation $R_{\theta JA}$ (Notes 1, 2)			PD	3.7	W
Pulsed Drain Current (Note 3)	$T_A = 25^{\circ}C$	I _{DM}	448	А
$ \begin{array}{ c c c c } \hline Single \mbox{ Pulse Surge} & T_A = 25^\circ \mbox{C}, t_p = 10 \mu \mbox{s}, \\ \hline Drain \mbox{ Current} & R_G = 4.7 \Omega \\ \hline Capability \mbox{ (Note 4)} & \end{array} $			I _{DSC}	854	A
Operating Junction and Storage Temperature Range			T _J , T _{stg}	–55 to +175	°C
Source Current (Body Diode)			ا _S	148	Α
Single Pulse Drain-to-Source Avalanche Energy ($I_L = 23 A_{pk}, L = 1 mH$) (Note 5)			E _{AS}	264	mJ
Maximum Lead Tempe 1/8" from Case for 10 \$	rature for So Seconds	oldering,	ΤL	245	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on a FR-4 board using1 in² pad of 2 oz copper.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- Repetitive rating, limited by max junction temperature.
- 4. Peak current might be limited by transconductance.
- 5. E_{AS} of 264 mJ is based on starting T_J = 25°C; L = 1 mH, I_{AS} = 23 A, V_{DD} = 100 V, V_{GS} = 15 V.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
900 V	28 mΩ @ 15 V	112 A



N-CHANNEL MOSFET



D2PAK-7L CASE 418BJ

MARKING DIAGRAM



A = Assembly Location

- Y = Year
- WW = Work Week
- ZZ = Lot Traceability
- NVBG020N090SC1 = Specific Device Code

ORDERING INFORMATION

Device		Package	Shipping †
NVBG020N090	DSC1	D2PAK-7L	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, <u>BRD8011/D</u>.

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Table 1. THERMAL CHARACTERISTICS

Parameter	Symbol	Мах	Unit
Thermal Resistance Junction-to-Case (Note 2)	$R_{ extsf{ heta}JC}$	0.31	°C/W
Thermal Resistance Junction-to-Ambient (Notes 1, 2)	$R_{ hetaJA}$	41	°C/W

Table 2. ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•			•		
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_{D} = 1 mA$	900			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	$I_D = 1$ mA, refer to 25°C		440		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}				100 250	μΑ μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = +22/-8 V, V _{DS} = 0 V			±1	μΑ
ON CHARACTERISTICS						·
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 20 \text{ mA}$	1.8	2.6	4.3	V
Recommended Gate Voltage	V _{GOP}		-5		+15	V
Drain-to-Source On Resistance	R _{DS(on)}	V_{GS} = 15 V, I _D = 60 A, T _J = 25°C		20	28	mΩ
		V_{GS} = 18 V, I_D = 60 A, T_J = 25°C		16		-
		V_{GS} = 15 V, I_D = 60 A, T_J = 175°C		27		
Forward Transconductance	9FS	V _{DS} = 20 V, I _D = 60 A		49		S
CHARGES, CAPACITANCES & GATE RE	ESISTANCE					
Input Capacitance	C _{ISS}	$V_{GS} = 0 V, f = 1 MHz,$		4415		pF
Output Capacitance	C _{OSS}	V _{DS} = 450 V		295		
Reverse Transfer Capacitance	C _{RSS}			25		1
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/15 \text{ V}, V_{DS} = 720 \text{ V},$		200		nC
Threshold Gate Charge	Q _{G(TH)}	I _D = 60 A		42		
Gate-to-Source Charge	Q _{GS}			76		
Gate-to-Drain Charge	Q _{GD}			56		1
Gate-Resistance	R _G	f = 1 MHz		1.5		Ω
SWITCHING CHARACTERISTICS	-					
Turn-On Delay Time	t _{d(ON)}	V _{GS} = -5/15 V, V _{DS} = 720 V,		39		ns
Rise Time	t _r	$I_D = 60 \text{ A}, R_G = 2.5 \Omega,$ Inductive Load		52		
Turn-Off Delay Time	t _{d(OFF)}			58		1
Fall Time	t _f			13		1
Turn-On Switching Loss	E _{ON}			1551		μJ
Turn-Off Switching Loss	E _{OFF}			179		1
Total Switching Loss	E _{TOT}			1730	1	1

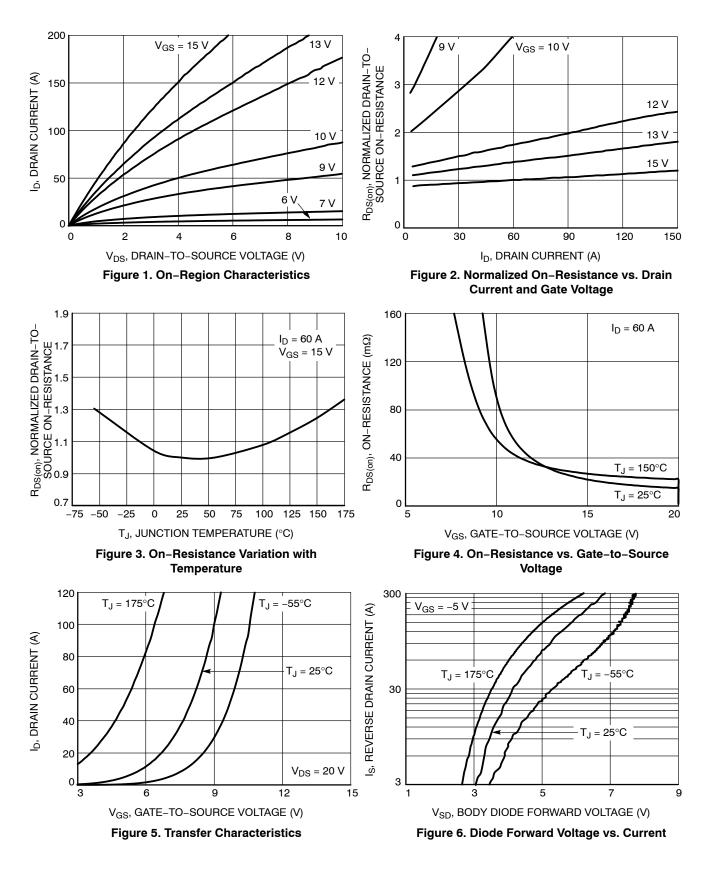
Continuous Drain-Source Diode Forward Current	I _{SD}	V_{GS} = -5 V, T_J = 25°C		148	A
Pulsed Drain-Source Diode Forward Current (Note 3)	I _{SDM}	V_{GS} = -5 V, T_{J} = 25°C		448	A
Forward Diode Voltage	V _{SD}	V_{GS} = –5 V, I_{SD} = 30 A, T_J = 25°C	3.7		V

Table 2. ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise stated) (continued)

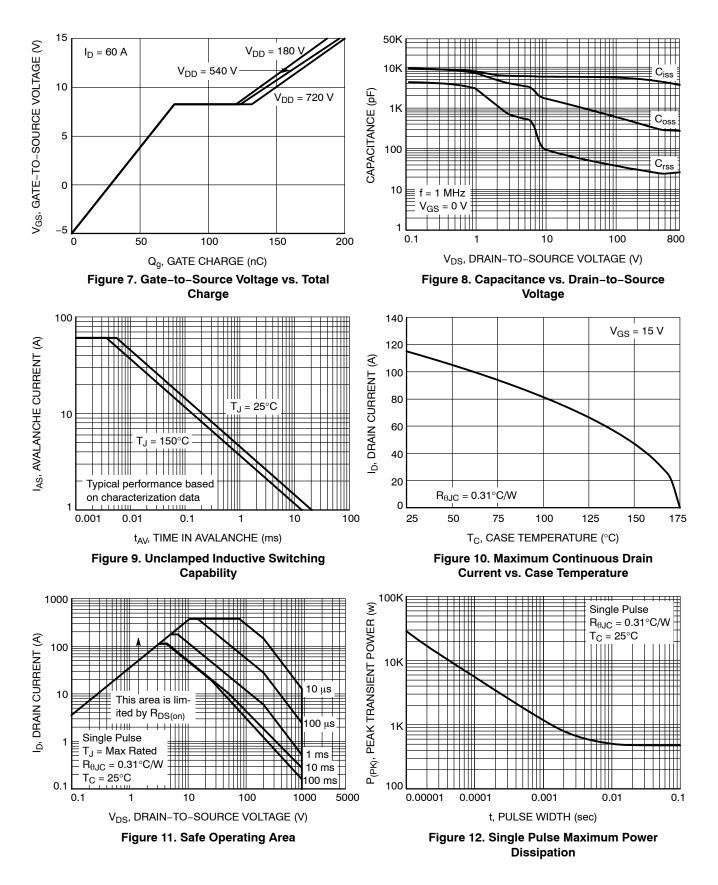
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
DRAIN-SOURCE DIODE CHARACTER	ISTICS					
Reverse Recovery Time	t _{RR}	V _{GS} = -5/15 V, I _{SD} = 60 A, dI _S /dt = 1000 A/µs, V _{DS} = 720 V		28		ns
Reverse Recovery Charge	Q _{RR}	αl _S /at = 1000 Α/μs, V _{DS} = 720 V		186		nC
Reverse Recovery Energy	E _{REC}			4		μJ
Peak Reverse Recovery Current	I _{RRM}			14		А
Charge Time	Та]		17		ns
Discharge Time	Tb			11		ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (continued)



TYPICAL CHARACTERISTICS (continued)

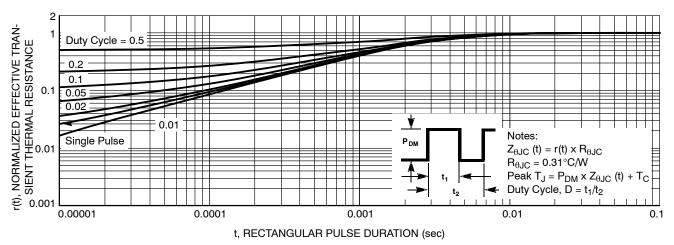
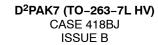


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

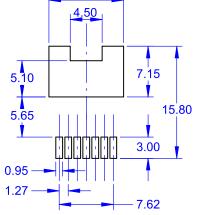
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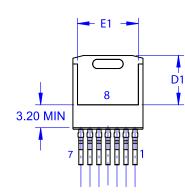


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- XXXX = Specific Device Code А = Assembly Location Y = Year
- WW = Work Week

Г

G = Pb-Free Package

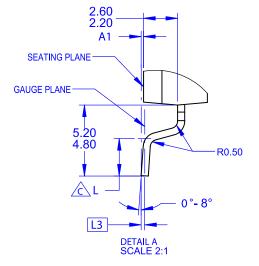
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

TERN RECOM	IMENDATION
В	A
c2 —	
H	

NOTES:

- A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED. B. ALL DIMENSIONS ARE IN MILLIMETERS.
- OUT OF JEDEC STANDARD VALUE.
 D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
 E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

DIM	MIL	LIMETER	S
DIM	MIN	NOM	MAX
Α	4.30	4.50	4.70
A1	0.00	0.10	0.20
b2	0.60	0.70	0.80
b	0.51	0.60	0.70
С	0.40	0.50	0.60
c2	1.20	1.30	1.40
D	9.00	9.20	9.40
D1	6.15	6.80	7.15
E	9.70	9.90	10.20
E1	7.15	7.65	8.15
е	~	1.27	~
Н	15.10	15.40	15.70
L	2.44	2.64	2.84
L1	1.00	1.20	1.40
L3	~	0.25	~
aaa	~	~	0.25



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