



ECH8662

N-Channel Power MOSFET 40V, 6.5A, 30mΩ, Dual ECH8

ON Semiconductor®

<http://onsemi.com>

Features

- Low ON-resistance
- 2.5V drive
- Halogen free compliance
- Protection diode in

Specifications

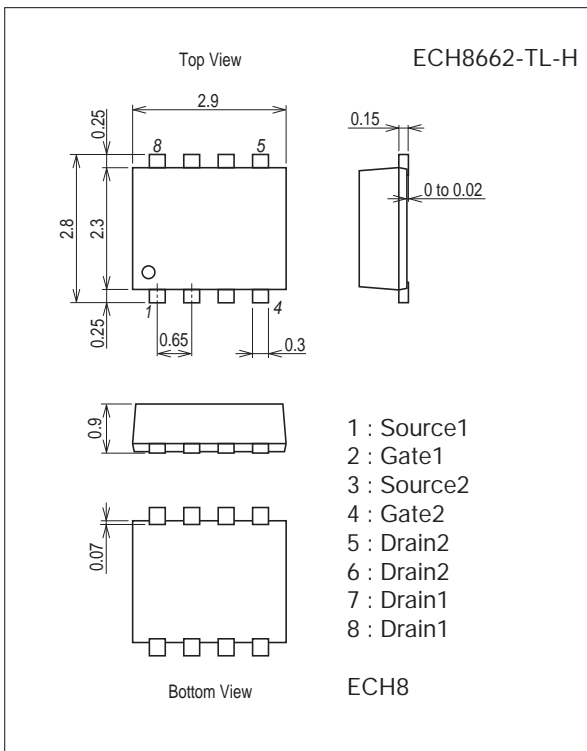
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		40	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	I _D		6.5	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	40	A
Allowable Power Dissipation	P _D	When mounted on ceramic substrate (900mm ² ×0.8mm) 1unit	1.3	W
Total Dissipation	P _T	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.5	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

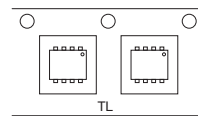
unit : mm (typ)
7011A-001



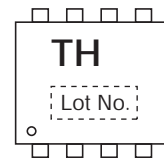
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

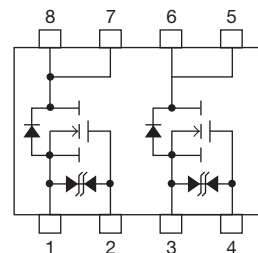
Packing Type : TL



Marking



Electrical Connection

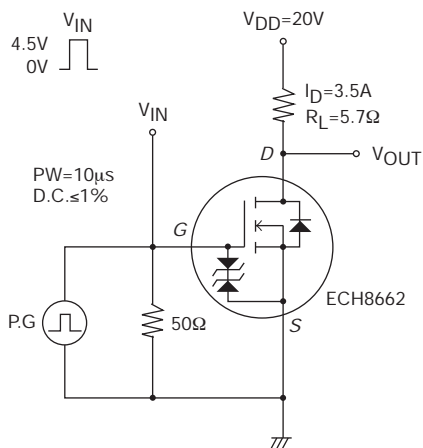


ECH8662

Electrical Characteristics at Ta=25°C

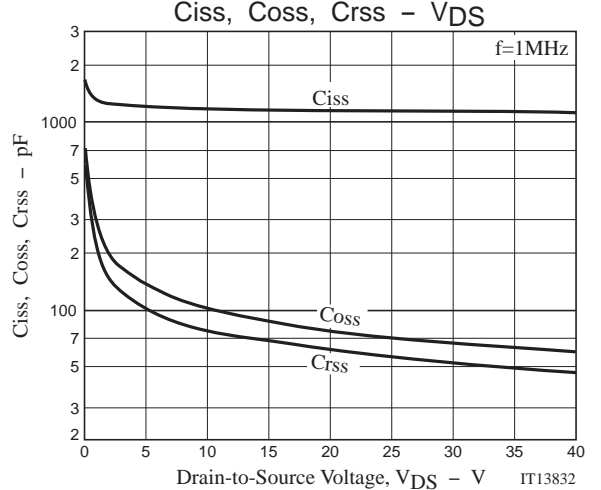
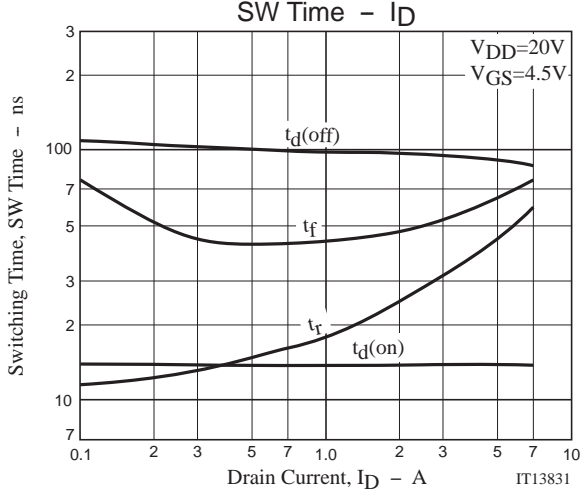
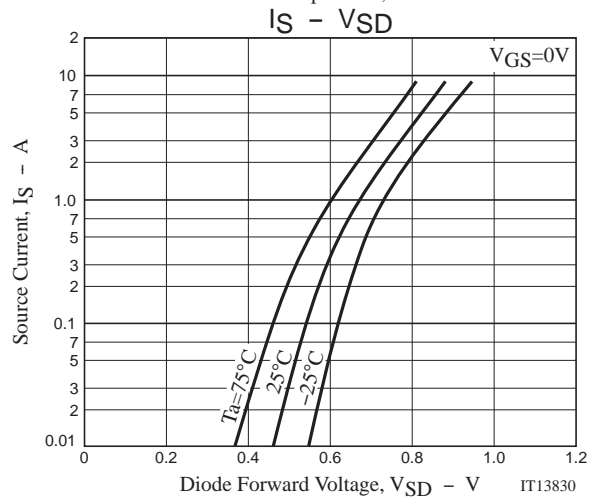
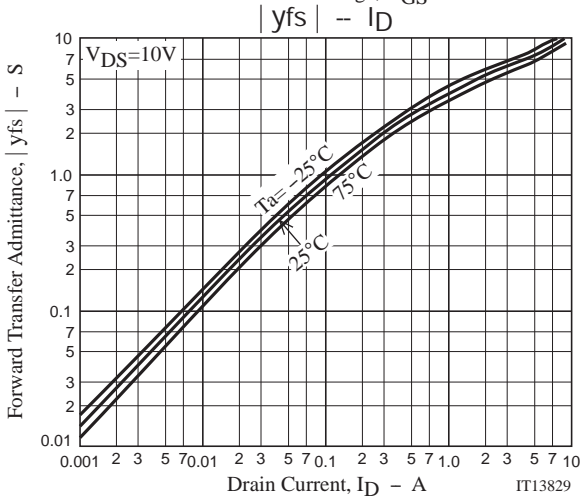
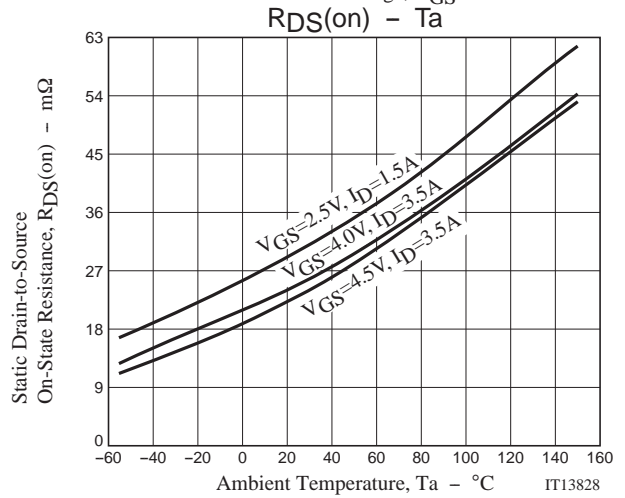
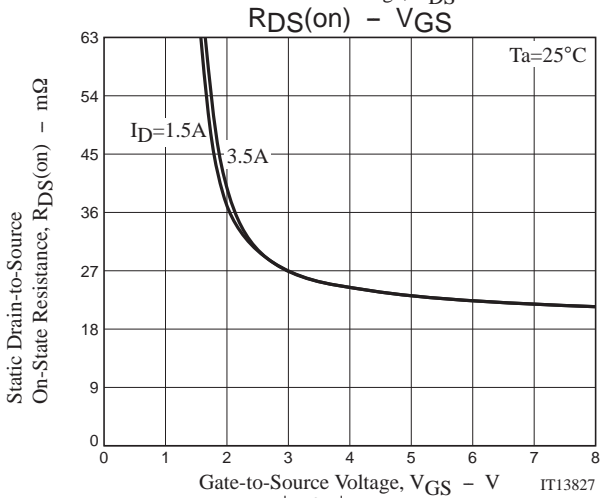
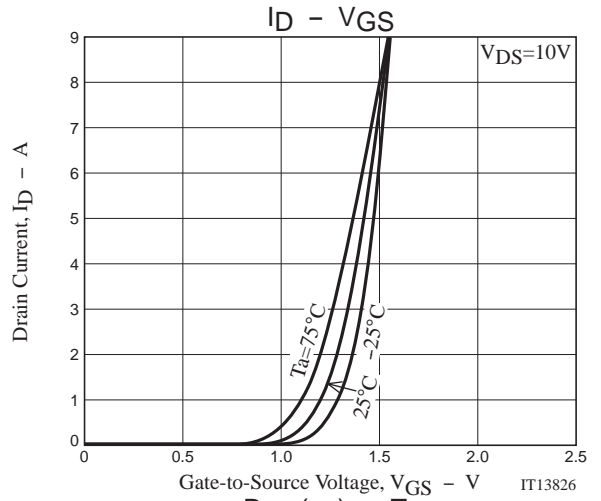
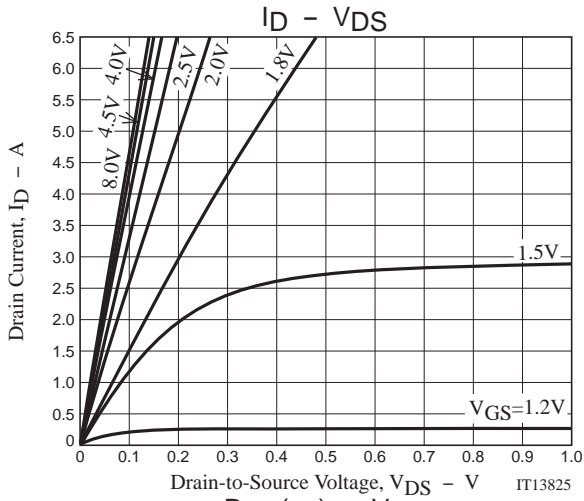
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	40			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =3.5A	3.9	6.5		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =3.5A, V _{GS} =4.5V		23	30	mΩ
	R _{DS(on)2}	I _D =3.5A, V _{GS} =4V		25	33	mΩ
	R _{DS(on)3}	I _D =1.5A, V _{GS} =2.5V		30	42	mΩ
Input Capacitance	C _{iss}	V _{DS} =20V, f=1MHz		1130		pF
Output Capacitance	C _{oss}	V _{DS} =20V, f=1MHz		77		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =20V, f=1MHz		60		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		14		ns
Rise Time	t _r			34		ns
Turn-OFF Delay Time	t _{d(off)}			93		ns
Fall Time	t _f			55		ns
Total Gate Charge	Q _g				12	
Gate-to-Source Charge	Q _{gs}	V _{DS} =20V, V _{GS} =4.5V, I _D =6.5A		2.2		nC
Gate-to-Drain "Miller" Charge	Q _{gd}			3.4		nC
Diode Forward Voltage	V _{SD}		I _S =6.5A, V _{GS} =0V		0.85	1.2

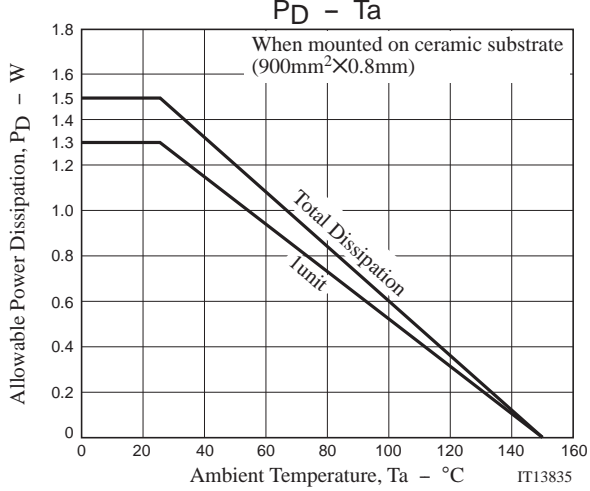
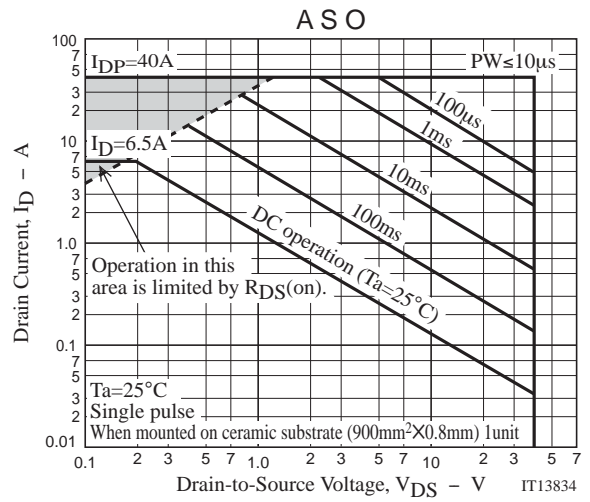
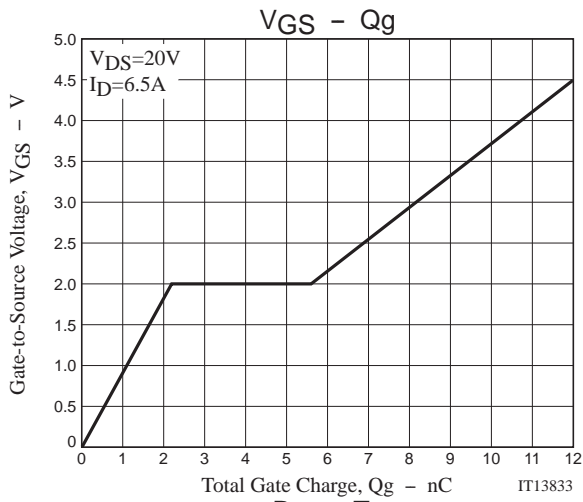
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ECH8662-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free





Embossed Taping Specification

ECH8662-TL-H

1. Packing Format

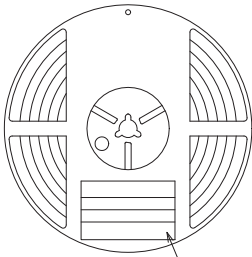
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label
(unit :mm)

Outer box label

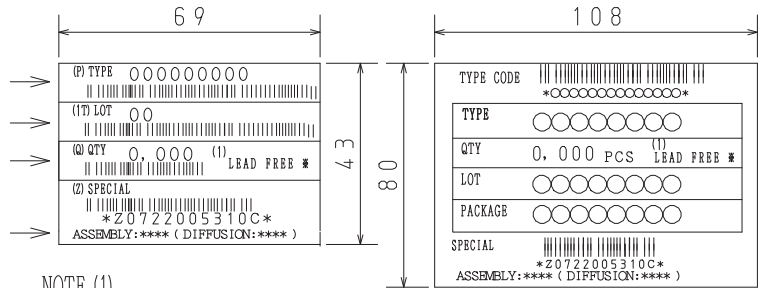
It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

Packing method



Reel label

Type No.
LOT No.
Quantity
Origin



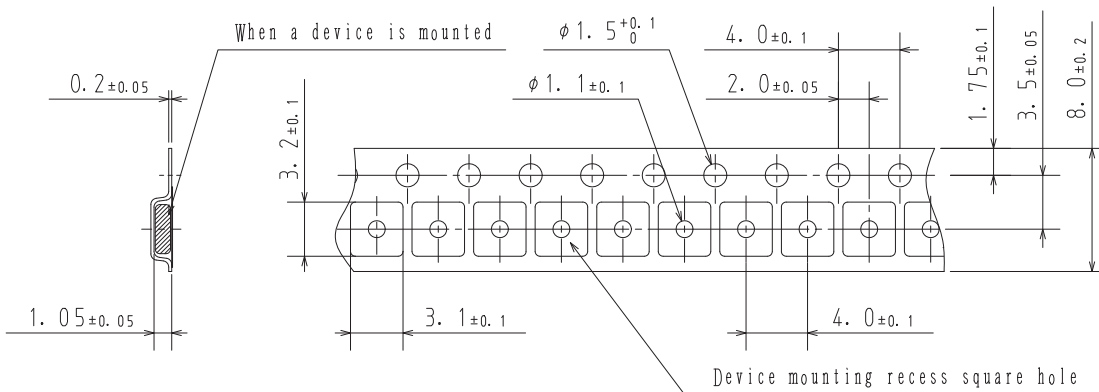
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

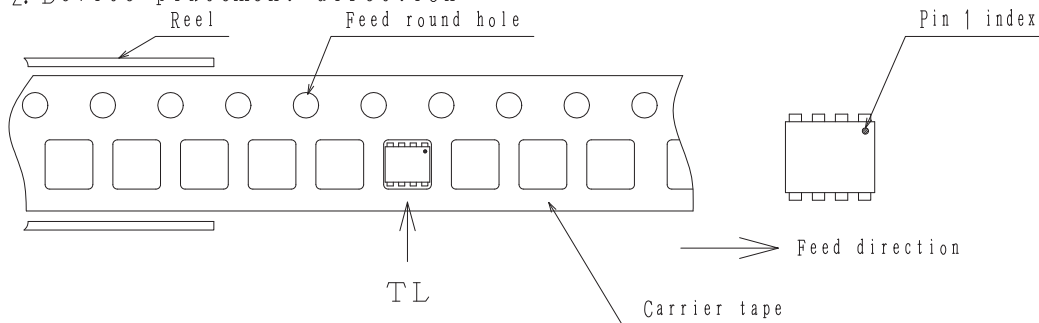
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

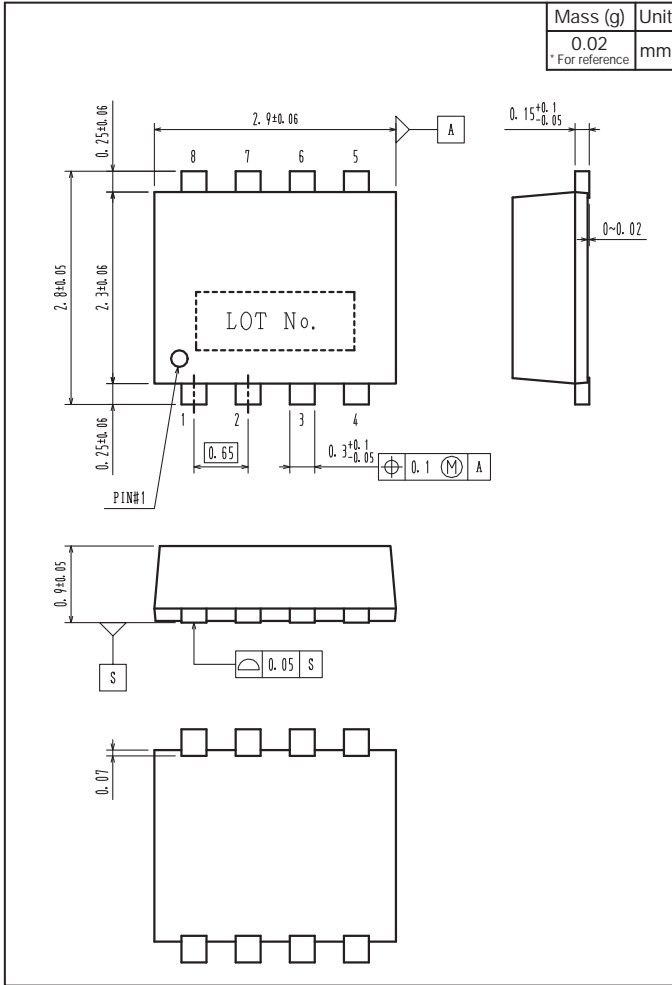


2-2. Device placement direction

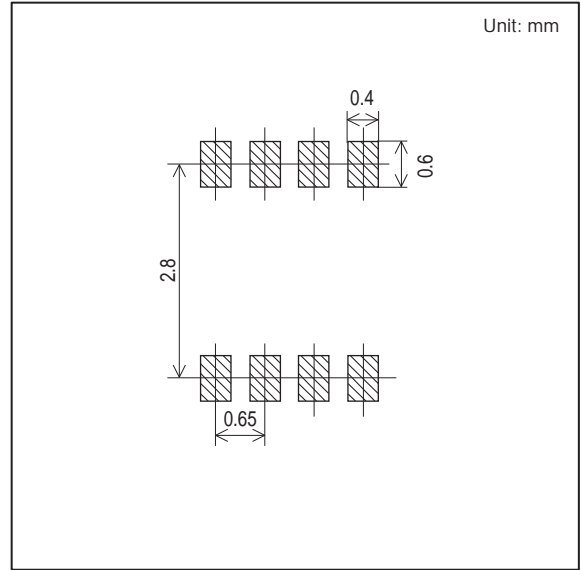


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
ECH8662-TL-H



Land Pattern Example



Note on usage : Since the ECH8662 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.