

NTLUS3C18PZ

MOSFET – Power, Single, P-Channel, UDFN, 1.6x1.6x0.5 mm

-12 V, -7.0 A

Features

- Ultra Low $R_{DS(on)}$
- UDFN Package with Exposed Drain Pads for Excellent Thermal Conduction
- Low Profile UDFN 1.6 x 1.6 x 0.5 mm for Board Space Saving
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Optimized for Power Management Applications for Portable Products, Such as Smart Phones and Media Tablets
- Battery Switch
- High Side Load Switch

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise stated)

| Parameter | | | Symbol | Value | Unit |
|---|---------------------|-------------------------------|----------------|------------|--------------------|
| Drain-to-Source Voltage | | | V_{DSS} | −12 | V |
| Gate-to-Source Voltage | | | V_{GS} | ±10 | V |
| Continuous Drain Current (Note 1) | Steady State | $T_A = 25^{\circ}\text{C}$ | I_D | −7.0 | A |
| | | $T_A = 85^{\circ}\text{C}$ | | −5.1 | |
| | $t \leq 5\text{ s}$ | $T_A = 25^{\circ}\text{C}$ | | −10.5 | |
| Power Dissipation (Note 1) | Steady State | $T_A = 25^{\circ}\text{C}$ | P_D | 1.71 | W |
| | $t \leq 5\text{ s}$ | $T_A = 25^{\circ}\text{C}$ | | 3.83 | |
| Continuous Drain Current (Note 2) | Steady State | $T_A = 25^{\circ}\text{C}$ | I_D | −4.4 | A |
| | | $T_A = 85^{\circ}\text{C}$ | | −3.1 | |
| Power Dissipation (Note 2) | | $T_A = 25^{\circ}\text{C}$ | P_D | 0.66 | W |
| Pulsed Drain Current | | $t_p = 10\text{ }\mu\text{s}$ | I_{DM} | −21 | A |
| Operating Junction and Storage Temperature | | | T_J, T_{STG} | −55 to 150 | $^{\circ}\text{C}$ |
| Source Current (Body Diode) (Note 2) | | | I_S | −1.7 | A |
| Lead Temperature for Soldering Purposes (1/8" from case for 10 s) | | | T_L | 260 | $^{\circ}\text{C}$ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
2. Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm², 2 oz. Cu.

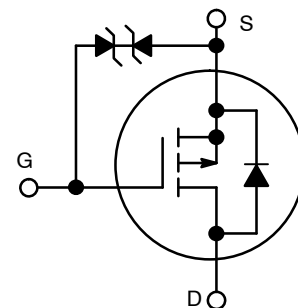


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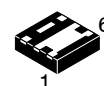
MOSFET

| $V_{(BR)DSS}$ | $R_{DS(on)}$ MAX | I_D MAX |
|---------------|------------------------|-----------|
| -12 V | 24 m Ω @ -4.5 V | -7.0 A |
| | 27 m Ω @ -3.7 V | -6.6 A |
| | 30 m Ω @ -3.3 V | -6.3 A |
| | 36 m Ω @ -2.5 V | -5.7 A |
| | 70 m Ω @ -1.8 V | -4.1 A |

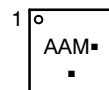


P-Channel MOSFET

MARKING DIAGRAM



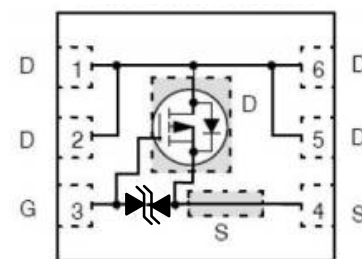
UDFN6
CASE 517AU



AA = Specific Device Code
M = Date Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS



(Top View)

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

NTLUS3C18PZ

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Max | Unit |
|---|-----------------|-------|------|
| Junction-to-Ambient – Steady State (Note 3) | $R_{\theta JA}$ | 72 | °C/W |
| Junction-to-Ambient – $t \leq 5$ s (Note 3) | $R_{\theta JA}$ | 32.6 | |
| Junction-to-Ambient – Steady State min Pad (Note 4) | $R_{\theta JA}$ | 190.4 | |

3. Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
4. Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm², 2 oz. Cu.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Test Condition | Min | Typ | Max | Units |
|-----------|--------|----------------|-----|-----|-----|-------|
|-----------|--------|----------------|-----|-----|-----|-------|

OFF CHARACTERISTICS

| | | | | | | |
|---|-------------------|---|-----|-----|----------|---------------|
| Drain-to-Source Breakdown Voltage | $V_{(BR)DSS}$ | $V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$ | -12 | | | V |
| Drain-to-Source Breakdown Voltage Temperature Coefficient | $V_{(BR)DSS}/T_J$ | $I_D = -250\text{ }\mu\text{A}$, ref to 25°C | | 7.3 | | mV/°C |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{GS} = 0\text{ V}, V_{DS} = -9.6\text{ V}$ $T_J = 25^\circ\text{C}$ | | | -1.0 | μA |
| Gate-to-Source Leakage Current | I_{GSS} | $V_{DS} = 0\text{ V}, V_{GS} = \pm 10\text{ V}$ | | | ± 10 | μA |

ON CHARACTERISTICS (Note 5)

| | | | | | | |
|--------------------------------------|------------------|--|------|------|------|------------|
| Gate Threshold Voltage | $V_{GS(TH)}$ | $V_{GS} = V_{DS}, I_D = -250\text{ }\mu\text{A}$ | -0.4 | | -1.0 | V |
| Negative Threshold Temp. Coefficient | $V_{GS(TH)}/T_J$ | | | 3.0 | | mV/°C |
| Drain-to-Source On Resistance | $R_{DS(on)}$ | $V_{GS} = -4.5\text{ V}, I_D = -7.0\text{ A}$ | | 20 | 24 | m Ω |
| | | $V_{GS} = -3.7\text{ V}, I_D = -6.6\text{ A}$ | | 22 | 27 | |
| | | $V_{GS} = -3.3\text{ V}, I_D = -5.7\text{ A}$ | | 24 | 30 | |
| | | $V_{GS} = -2.5\text{ V}, I_D = -5.1\text{ A}$ | | 29 | 36 | |
| | | $V_{GS} = -1.8\text{ V}, I_D = -2.0\text{ A}$ | | 44 | 70 | |
| Forward Transconductance | g_{FS} | $V_{DS} = -5\text{ V}, I_D = -7.0\text{ A}$ | | 21.8 | | S |

CHARGES, CAPACITANCES & GATE RESISTANCE

| | | | | | | |
|------------------------------|--------------|---|--|------|--|----|
| Input Capacitance | C_{ISS} | $V_{GS} = 0\text{ V}, f = 1\text{ MHz}, V_{DS} = -6.0\text{ V}$ | | 1570 | | pF |
| Output Capacitance | C_{OSS} | | | 200 | | |
| Reverse Transfer Capacitance | C_{RSS} | | | 240 | | |
| Total Gate Charge | $Q_{G(TOT)}$ | $V_{GS} = -4.5\text{ V}, V_{DS} = -6.0\text{ V}; I_D = -7.0\text{ A}$ | | 15.8 | | nC |
| Threshold Gate Charge | $Q_{G(TH)}$ | | | 0.7 | | |
| Gate-to-Source Charge | Q_{GS} | | | 1.9 | | |
| Gate-to-Drain Charge | Q_{GD} | | | 4.6 | | |

SWITCHING CHARACTERISTICS (Note 6)

| | | | | | | |
|---------------------|--------------|--|--|------|--|----|
| Turn-On Delay Time | $t_{d(ON)}$ | $V_{GS} = -4.5\text{ V}, V_{DD} = -6\text{ V}, I_D = -7.0\text{ A}, R_G = 1\text{ }\Omega$ | | 8.5 | | ns |
| Rise Time | t_r | | | 52.5 | | |
| Turn-Off Delay Time | $t_{d(OFF)}$ | | | 40 | | |
| Fall Time | t_f | | | 59 | | |

DRAIN-SOURCE DIODE CHARACTERISTICS

| | | | | | | | |
|-----------------------|----------|--|---------------------------|--|------|-----|---|
| Forward Diode Voltage | V_{SD} | $V_{GS} = 0\text{ V}, I_S = -1.7\text{ A}$ | $T_J = 25^\circ\text{C}$ | | 0.71 | 1.0 | V |
| | | | $T_J = 125^\circ\text{C}$ | | 0.58 | | |

5. Pulse Test: pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
6. Switching characteristics are independent of operating junction temperatures.

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

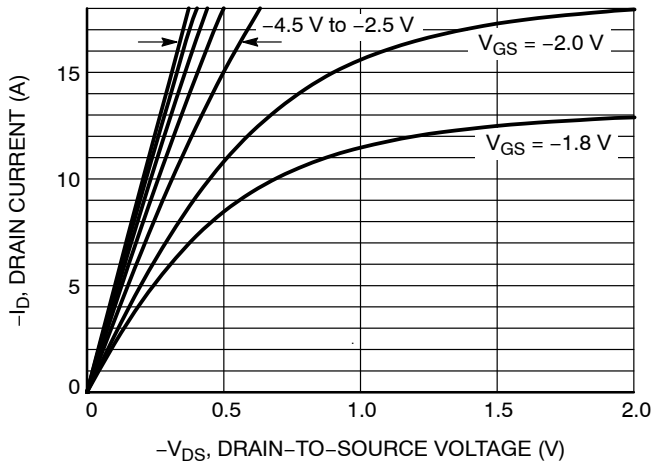


Figure 1. On-Region Characteristics

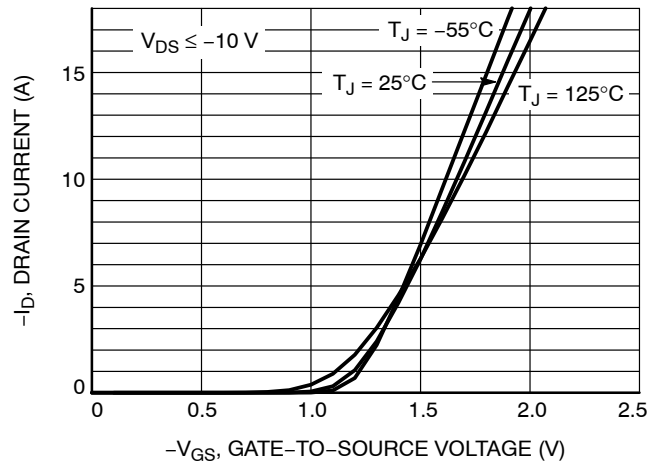


Figure 2. Transfer Characteristics

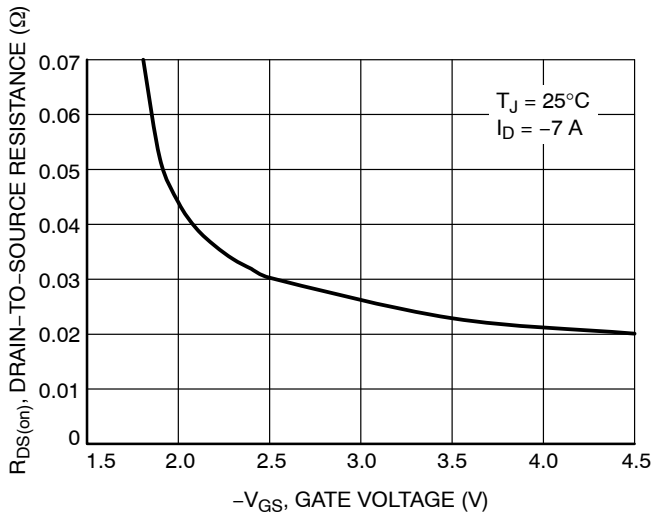


Figure 3. On-Resistance vs. Gate-to-Source Voltage

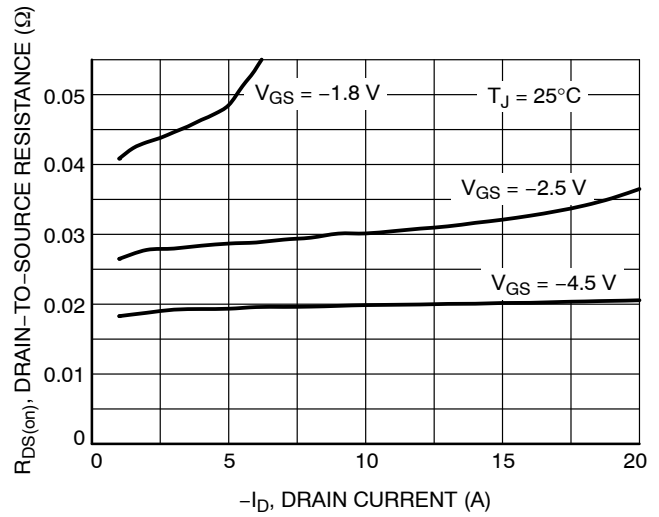


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

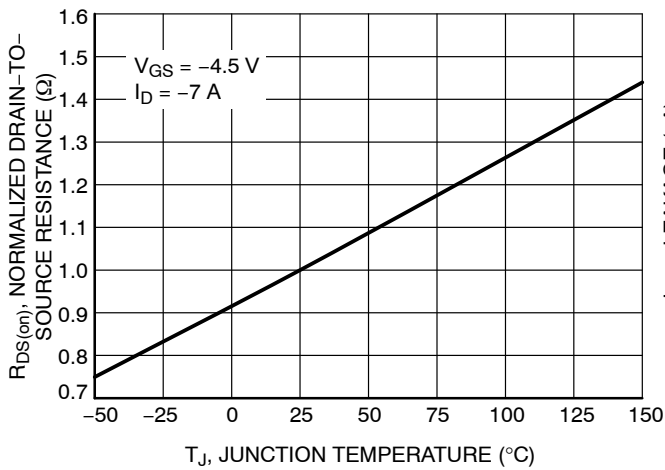


Figure 5. On-Resistance Variation with Temperature

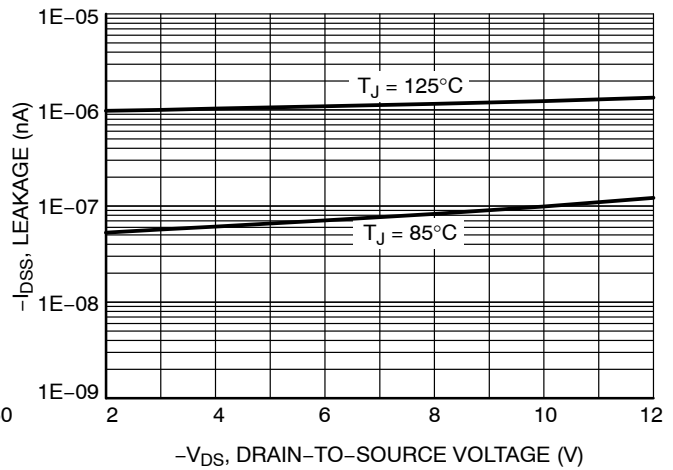


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS

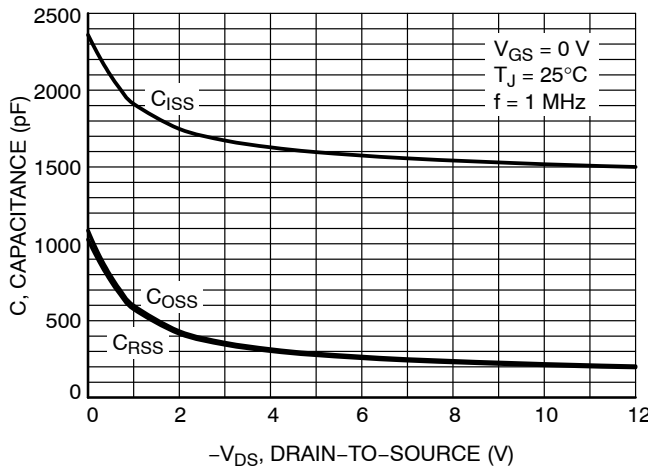


Figure 7. Capacitance Variation

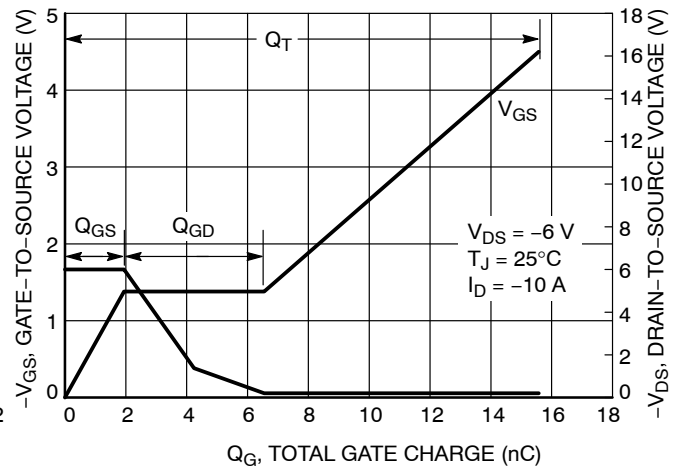


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

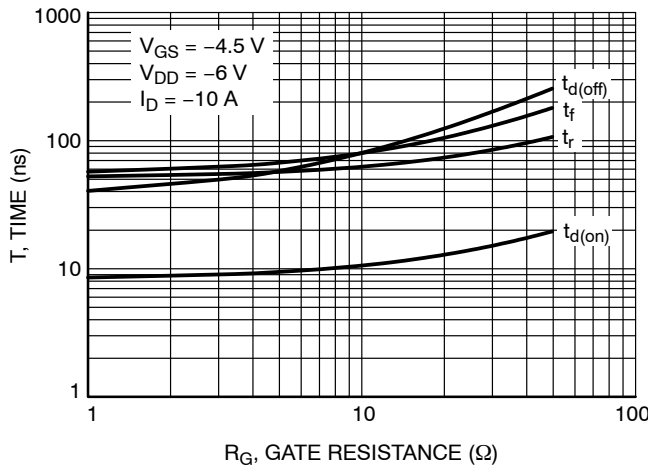


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

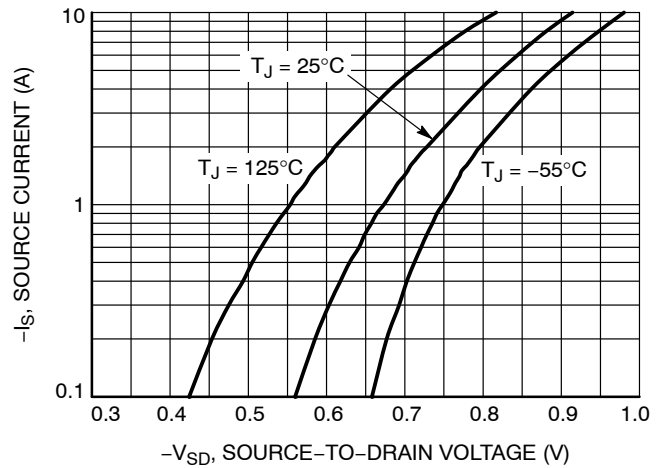


Figure 10. Diode Forward Voltage vs. Current

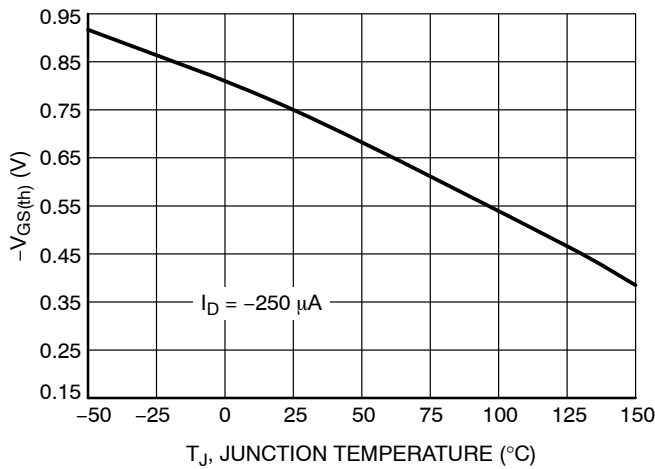


Figure 11. Threshold Voltage

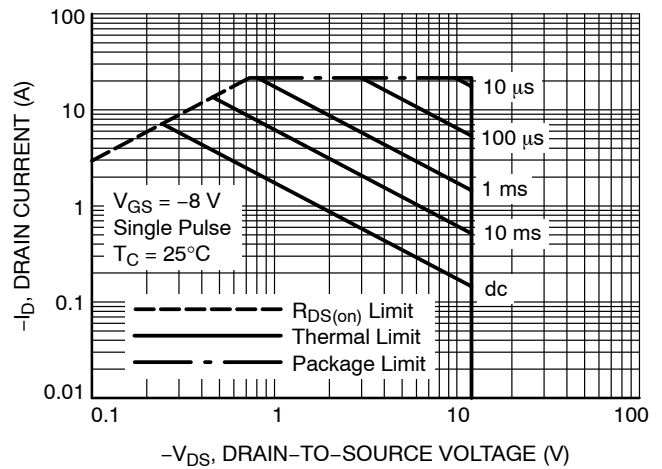


Figure 12. Maximum Rated Forward Biased Safe Operating Area

NTLUS3C18PZ

TYPICAL CHARACTERISTICS

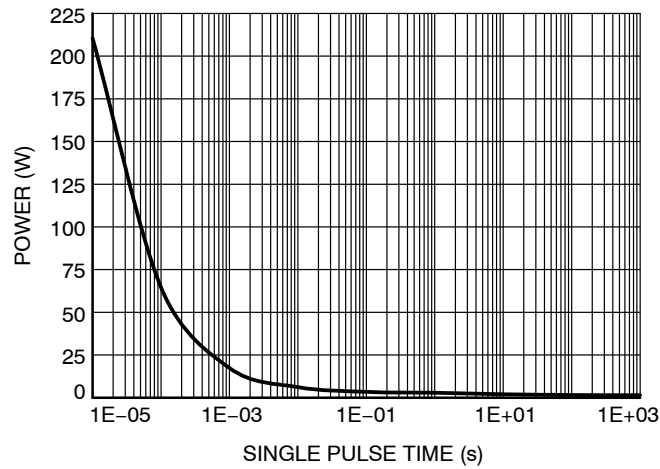


Figure 13. Single Pulse Maximum Power Dissipation

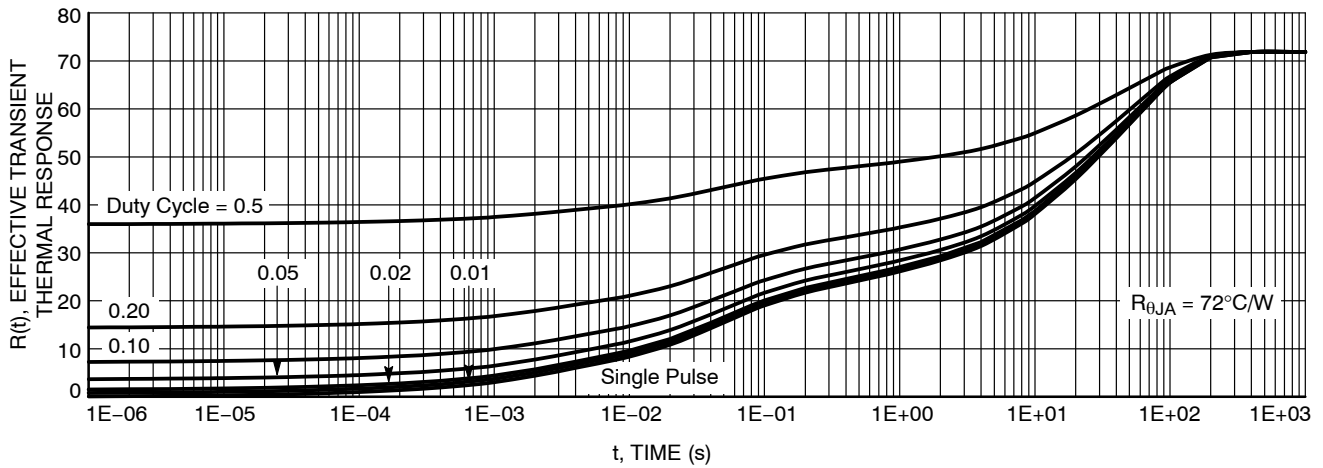


Figure 14. FET Thermal Response

DEVICE ORDERING INFORMATION

| Device | Package | Shipping† |
|----------------|--------------------|--------------------|
| NTLUS3C18PZTAG | UDFN6 (Pb-Free) | 3000 / Tape & Reel |
| NTLUS3C18PZTBG | UDFN6 (Pb-Free) | 3000 / Tape & Reel |

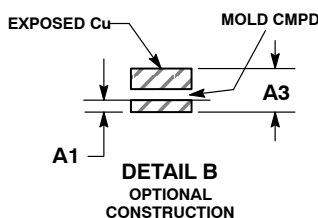
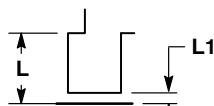
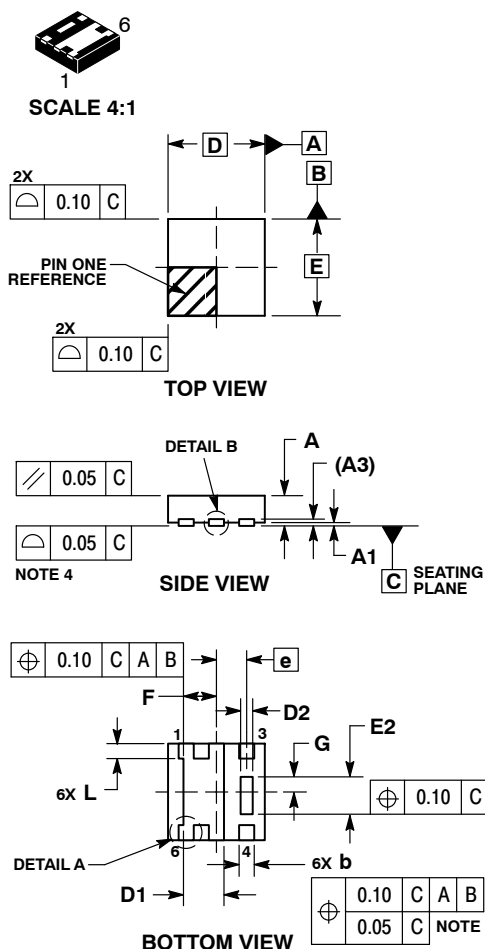
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

UDFN6 1.6x1.6, 0.5P

CASE 517AU-01

ISSUE O

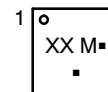
DATE 16 OCT 2008



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM TERMINAL.
 4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

| MILLIMETERS | | |
|-------------|------|------|
| DIM | MIN | MAX |
| A | 0.45 | 0.55 |
| A1 | 0.00 | 0.05 |
| A3 | 0.13 | REF |
| b | 0.20 | 0.30 |
| D | 1.60 | BSC |
| E | 1.60 | BSC |
| e | 0.50 | BSC |
| D1 | 0.62 | 0.72 |
| D2 | 0.15 | 0.25 |
| E2 | 0.57 | 0.67 |
| F | 0.55 | BSC |
| G | 0.25 | BSC |
| L | 0.20 | 0.30 |
| L1 | --- | 0.15 |

GENERIC MARKING DIAGRAM*



XX = Specific Device Code

M = Date Code

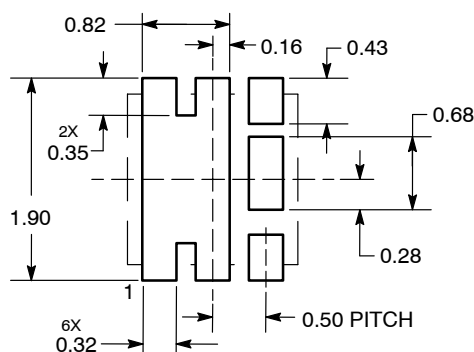
■ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot "■", may or may not be present.

SOLDERMASK DEFINED MOUNTING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

| | | |
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| DESCRIPTION: | UDFN6, 1.6X1.6, 0.5P | PAGE 1 OF 1 |

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