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2N6426



### **NPN Darlington Transistor**

This device is designed for applications requiring extremely high current gain at currents to 1.0 A. Sourced from Process 05. See MPSA14 for characteristics.

#### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	12	V
I <sub>C</sub>	Collector Current - Continuous	1.2	A
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

#### Thermal Characteristics

Thermal Characteristics     TA = 25°C unless otherwise noted					
Symbol	Characteristic	Мах	Units		
		2N6426			
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C		
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	°C/W		
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W		

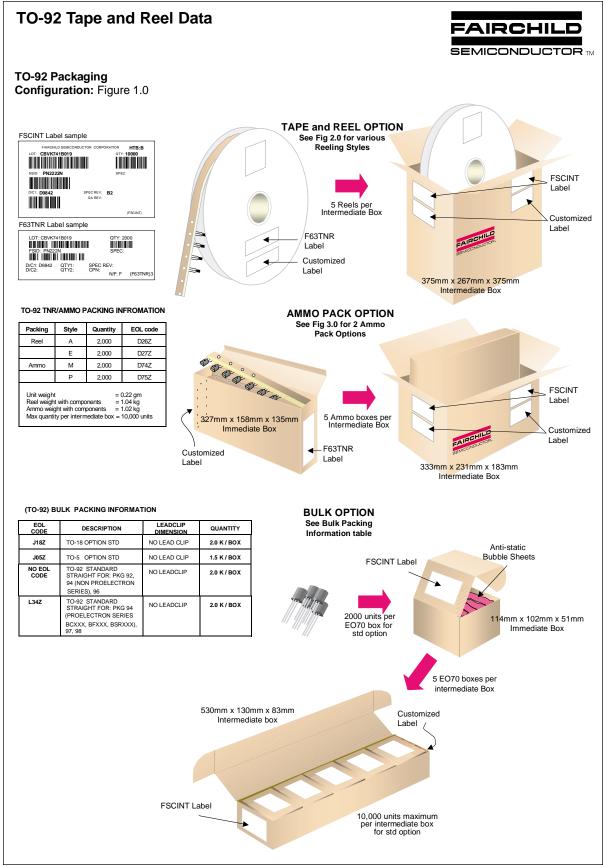
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## NPN Darlington Transistor (continued)

Symbol	Parameter	Test Conditions	Min	Max	Units
-					
OFF CHA	RACTERISTICS				
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	40		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \ \mu {\rm A}, \ I_{\rm E} = 0$	40		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm E} = 10 \ \mu A, I_{\rm C} = 0$	12		V
СВО	Collector Cutoff Current	$V_{CB} = 30 \text{ V}, \text{ I}_{E} = 0$		50	nA
CEO	Collector Cutoff Current	$V_{CE} = 25 V, I_B = 0$		1.0	μA
EBO	Emitter Cutoff Current	$V_{EB} = 10 \text{ V}, \text{ I}_{C} = 0$		50	nA
CE(sat)	Collector-Emitter Saturation Voltage	$I_{\rm C} = 500 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$ $I_{\rm C} = 500 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$		1.2	V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$V_{CE} = 5.0 \text{ V}, I_C = 500 \text{ mA}$ $I_C = 50 \text{ mA}, I_B = 0.5 \text{ mA}$	20,000	200,000 1.2	V
/ <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_{\rm C} = 500 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$		2.0	V
	Base-Emitter Saturation Voltage Base-Emitter On Voltage				
SMALL S	÷	$I_{\rm C} = 500 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$		2.0	V
V <sub>BE(ON)</sub> SMALL S C <sub>ob</sub>	Base-Emitter On Voltage	$I_{c} = 500 \text{ mA}, I_{B} = 0.5 \text{ mA}$ $I_{c} = 50 \text{ mA}, V_{CE} = 5.0 \text{ V}$		2.0 1.75	V V
V <sub>BE(ON)</sub> SMALL S C <sub>ob</sub> C <sub>ib</sub>	Base-Emitter On Voltage	$I_{C} = 500 \text{ mA}, I_{B} = 0.5 \text{ mA}$ $I_{C} = 50 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1.0 \text{ MHz}$	20,000	2.0 1.75 7.0	V V pF
SMALL S Cob Cib	Base-Emitter On Voltage	$\begin{split} I_{C} &= 500 \text{ mA}, I_{B} = 0.5 \text{ mA} \\ I_{C} &= 50 \text{ mA}, V_{CE} = 5.0 \text{ V} \\ \end{split}$ $\begin{split} V_{CB} &= 10 \text{ V}, I_{E} = 0, f = 1.0 \text{ MHz} \\ V_{EB} &= 1.0 \text{ V}, I_{C} = 0, f = 1.0 \text{ MHz} \\ I_{C} &= 10 \text{ mA}, V_{CE} = 5.0 \text{ V}, \end{split}$	20,000	2.0 1.75 7.0	V V pF
V <sub>BE(sat)</sub> V <sub>BE(on)</sub> SMALL S Cob Cib Cib Cib Cib Cib Cib Cib Cib Cib Ci	Base-Emitter On Voltage	$\begin{split} I_{C} &= 500 \text{ mA}, I_{B} = 0.5 \text{ mA} \\ I_{C} &= 50 \text{ mA}, V_{CE} = 5.0 \text{ V} \\ \end{split}$ $\begin{split} V_{CB} &= 10 \text{ V}, I_{E} = 0, f = 1.0 \text{ MHz} \\ V_{EB} &= 1.0 \text{ V}, I_{C} = 0, f = 1.0 \text{ MHz} \\ I_{C} &= 10 \text{ mA}, V_{CE} = 5.0 \text{ V}, \\ f &= 1.0 \text{ kHz} \end{split}$	,	2.0 1.75 7.0 15	V V pF pF

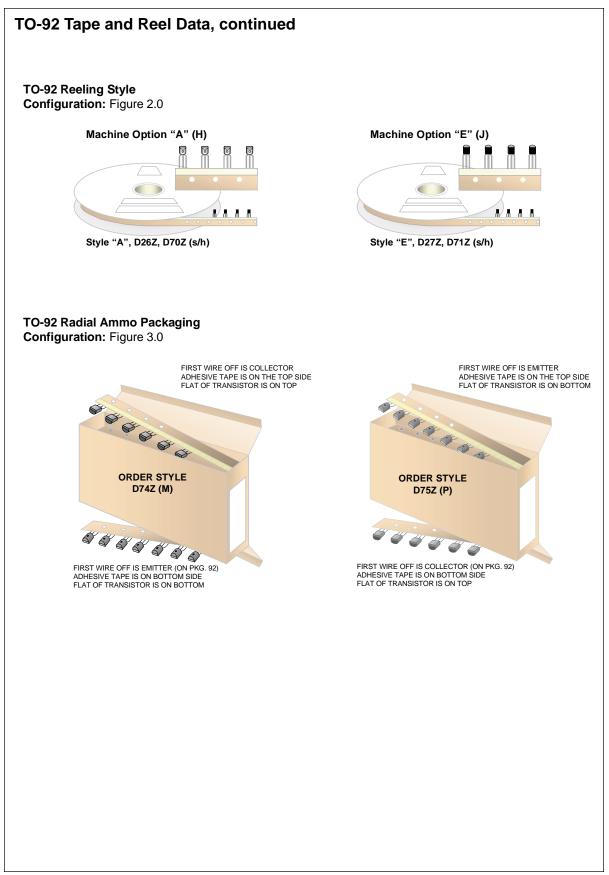
\*Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

2N6426

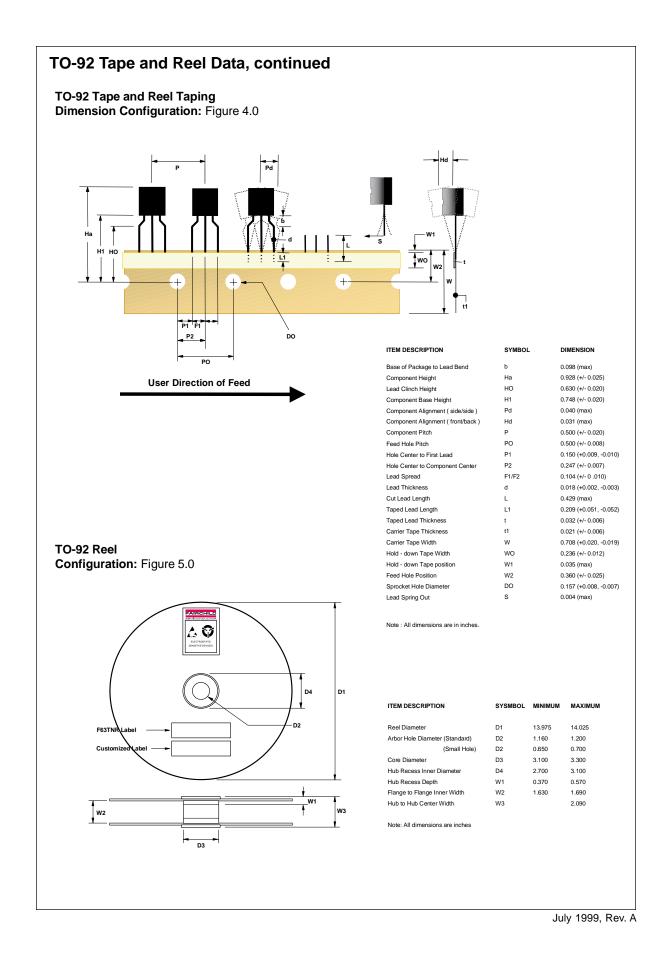


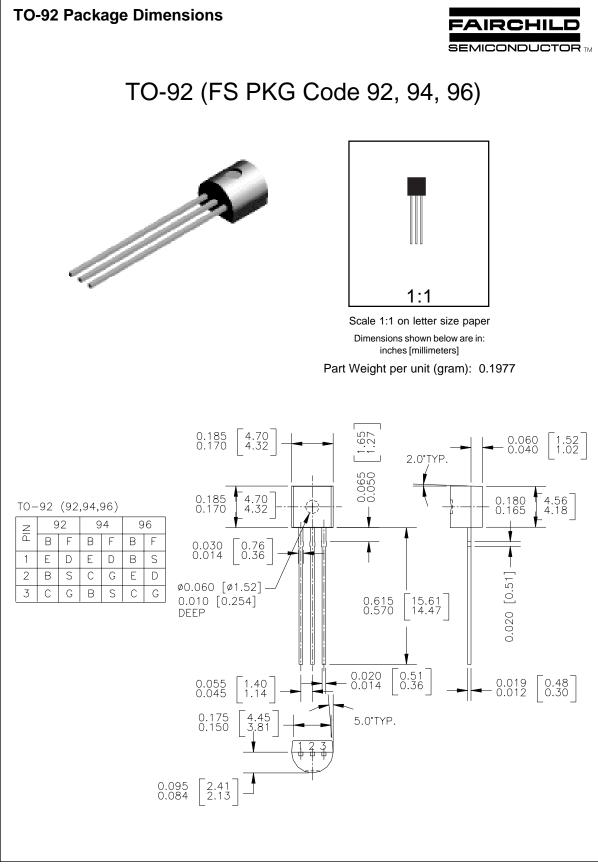
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Product Status	Definition
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