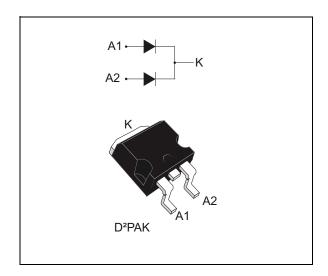


## STPS20H100C-Y

### Automotive power Schottky rectifier

Datasheet - production data



### **Description**

Dual center tap Schottky rectifier designed for high frequency miniature switched mode power supplies such as adaptators and on board DC/DC converters for automotive applications.

Table 1. Device summary

Symbol	Value
I <sub>F(AV)</sub>	2 x 10 A
V <sub>RRM</sub>	100 V
T <sub>j(max)</sub>	175 °C
V <sub>F (Typ)</sub>	0.59 V

#### **Features**

- · Negligible switching losses
- · High junction temperature capability
- Good trade off between leakage current and forward voltage drop
- Low leakage current
- Avalanche rated
- · AEC-Q101 qualified.

This is information on a product in full production.

PPAP capable

Characteristics STPS20H100C-Y

### 1 Characteristics

Table 2. Absolute ratings (limiting values per diode at 25 °C, unless otherwise specified)

Symbol	Parameter			Value	Unit
$V_{RRM}$	Repetitive peak reverse voltage			100	V
I <sub>F(RMS)</sub>	Forward rms current			30	Α
1	Average forward current, $\delta = 0.5$ , $T_c = 160$ °C	S = 0.5 T = 160 °C		10	А
<sup>I</sup> F(AV)	Average forward current, $0 = 0.5$ , $T_c = 100^{\circ}$ C		per device	20	
I <sub>FSM</sub>	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$		250	Α	
I <sub>RSM</sub>	Non repetitive peak reverse current $t_p = 100 \mu s square$		3	Α	
P <sub>ARM</sub> <sup>(1)</sup>	Repetitive peak avalanche power $t_p = 10 \mu s, T_j = 125 \text{ °C}$		780	W	
T <sub>stg</sub>	Storage temperature range			-65 to +175	°C
Tj	Operating junction temperature <sup>(2)</sup>			-40 to +175	°C

<sup>1.</sup> For pulse time duration deratings, please refer to *Figure 3*. More details regarding the avalanche energy measurements and diode validation in the avalanche are provided in the STMicroelectronics Application notes AN1768, "Admissible avalanche power of Schottky diodes" and AN2025, "Converter improvement using Schottky rectifier avalanche specification".

**Table 3. Thermal parameters** 

Symbol	Parameter	Value	Unit	
D	Junction to case per dio	de	1.6	
R <sub>th(j-c)</sub>	per dev	rice	0.9	°C/W
R <sub>th(c)</sub>	coupling		0.15	

When the diodes 1 and 2 are used simultaneously:  $\Delta$ Tj(diode 1) = P(diode1) x R<sub>th(j-c)</sub>(Per diode) + P(diode 2) x R<sub>th(c)</sub>

Table 4. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
I <sub>R</sub> <sup>(1)</sup> Reverse leakage current	Povorco logicado current	T <sub>j</sub> = 25 °C	V - V	-	-	4.5	μΑ
	T <sub>j</sub> = 125 °C	$V_R = V_{RRM}$	-	2	6	mA	
	V <sub>F</sub> <sup>(2)</sup> Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 10 A	-	-	0.77	
V (2)		T <sub>j</sub> = 125 °C		-	0.59	0.64	V
VF.		T <sub>j</sub> = 25 °C	I <sub>F</sub> = 20 A	-	-	0.88	V
	T <sub>j</sub> = 125 °C	11F = 20 A	-	0.67	0.73		

<sup>1.</sup> Pulse test:  $t_p = 5$  ms,  $\delta < 2\%$ 

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<sup>2.</sup>  $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

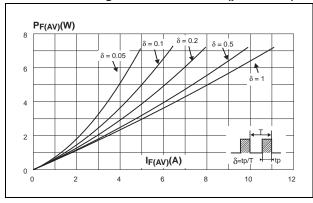
<sup>2.</sup> Pulse test:  $t_p$  = 380  $\mu$ s,  $\delta$  < 2%

STPS20H100C-Y Characteristics

To evaluate the conduction losses use the following equation:  $P = 0.55 \times I_{F(AV)} + 0.009 \times I_{F}^{2}_{(RMS)}$ 

Figure 1. Average forward power dissipation versus average forward current (per diode)

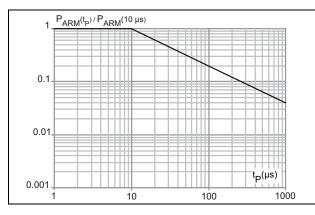
Figure 2. Average forward current versus ambient temperature ( $\delta = 0.5$ , per diode)



12 I<sub>F(AV)</sub>(A)
10 R<sub>th(j-a)</sub> = R<sub>th(j-c)</sub>
10 R<sub>th(j-a)</sub> = R<sub>th(j-c)</sub>
10 T<sub>amb</sub>(°C)
10 25 50 75 100 125 150 175

Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Relative variation of thermal impedance junction to case versus pulse duration (per diode)



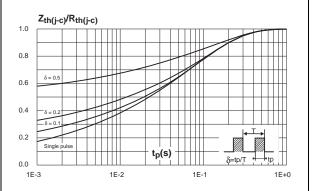
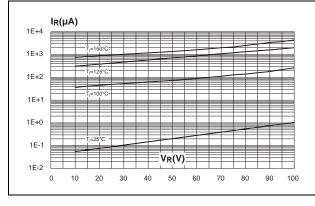
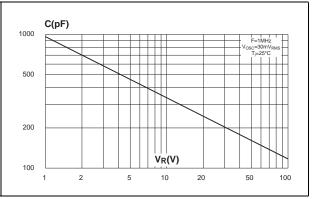


Figure 5. Reverse leakage current versus reverse voltage applied (typical values, per diode)

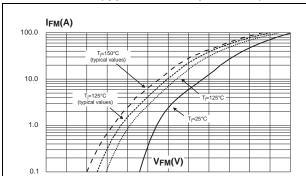
Figure 6. Junction capacitance vs. reverse voltage applied (typical values, per diode)





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Figure 7. Forward voltage drop vs. forward current (typical values, per diode)



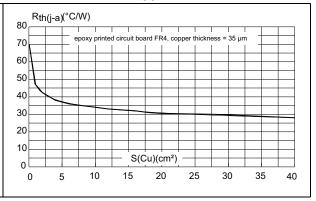
0.6

0.8

1.0

0.4

Figure 8. Thermal resistance junction to ambient versus copper surface under tab



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## 2 Package information

- Epoxy meets UL94, V0
- Lead-free package
- Cooling method: by conduction (C)

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Figure 9. D<sup>2</sup>PAK dimension definitions Α C2 Ε L2 D L3 R B2 **A2** G V2 \* Flat zone no less than 2 mm

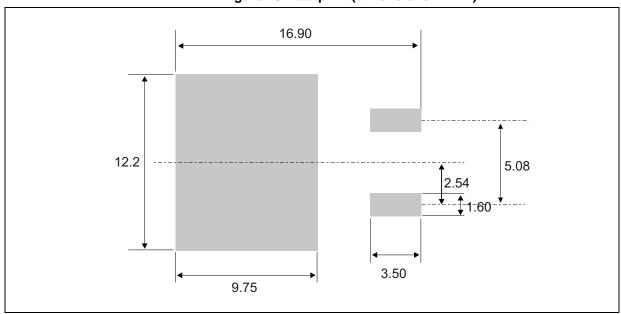


Package information STPS20H100C-Y

Table 5. D<sup>2</sup>PAK dimension values

	Dimensions					
Ref.		Millimeters		Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	4.40		4.60	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.70		0.93	0.027		0.037
B2	1.14		1.70	0.045		0.067
С	0.45		0.60	0.017		0.024
C2	1.23		1.36	0.048		0.054
D	8.95		9.35	0.352		0.368
Е	10.00		10.40	0.393		0.409
G	4.88		5.28	0.192		0.208
L	15.00		15.85	0.590		0.624
L2	1.27		1.40	0.050		0.055
L3	1.30		1.75	0.051		0.069
М	2.29		2.79	0.090		0.110
R		0.40 typ.		0.016 typ.		
V2	0°		8°	0°		8°

Figure 10. Footprint (dimensions in mm)



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# **3** Ordering information

**Table 6. Ordering information** 

Order code	Marking	Package	Weight	Base qty	Delivery mode	
STPS20H100CGY-TR	STPS20H100CGY	D²PAK	1.48 g	1000	Tape and reel	

## 4 Revision history

Table 7. Revision history

Date	Revision	Changes
19-Nov-2014	1	First issue



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