ON Semiconductor

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Driver Transistor

NPN Silicon

Features

- Moisture Sensitivity Level: 1
- ESD Rating:
 - ♦ Human Body Model 4 kV
 - ◆ Machine Model 400 V
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	80	Vdc
Collector-Base Voltage	V_{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current – Continuous	Ic	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board T _A = 25°C	P _D	460	mW
Thermal Resistance, Junction to Ambient (Note 1)	$R_{\theta JA}$	272	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

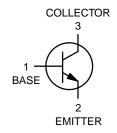


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SC-70 CASE 419 STYLE 3



MARKING DIAGRAM



GM = Specific Device Code
M = Date Code
• Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBTA06WT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel
SMMBTA06WT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel
SMMBTA06WT3G	SC-70 (Pb-Free)	10,000 / Tape & Reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

September, 2018 - Rev. 5

^{1.} FR-4 Board, 1 oz. Cu, 100 mm².

^{*}Date Code orientation may vary depending upon manufacturing location.

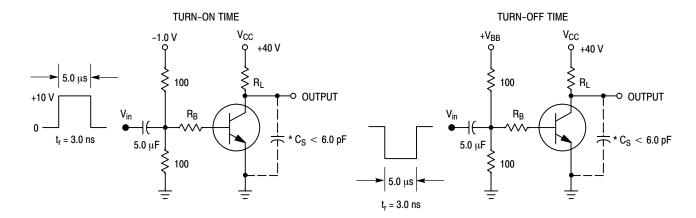
^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (Note 1) $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	V _(BR) CEO	80	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = 100 \mu Adc, I_C = 0$)	V _{(BR)EBO}	4.0	-	Vdc
Collector Cutoff Current $(V_{CE} = 60 \text{ Vdc}, I_B = 0)$	I _{CES}	_	0.1	μAdc
Collector Cutoff Current $(V_{CB} = 80 \text{ Vdc}, I_E = 0)$	I _{CBO}	_	0.1	μAdc
ON CHARACTERISTICS				
DC Current Gain $ (I_C = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}) $ $ (I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}) $	h _{FE}	100 100	- -	_
Collector-Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	-	0.25	Vdc
Base-Emitter On Voltage (I _C = 100 mAdc, V _{CE} = 1.0 Vdc)	V _{BE(on)}	-	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS	<u>, </u>		•	•
Current-Gain - Bandwidth Product (Note 2) (I _C = 10 mA, V _{CE} = 2.0 V, f = 100 MHz)	f _T	100	_	MHz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.
 f_T is defined as the frequency at which |h_{fe}| extrapolates to unity.



*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

Figure 1. Switching Time Test Circuits

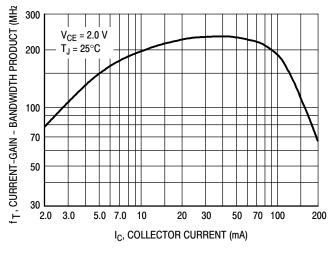


Figure 2. Current-Gain — Bandwidth Product

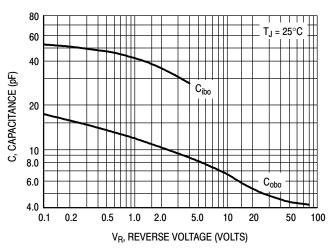


Figure 3. Capacitance

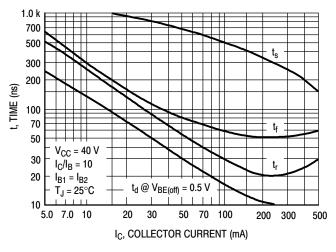


Figure 4. Switching Time

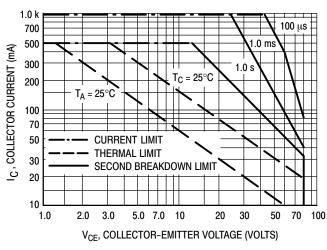


Figure 5. Active-Region Safe Operating Area

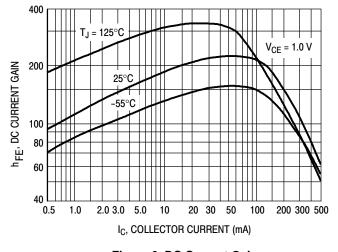


Figure 6. DC Current Gain

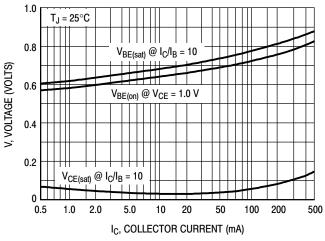
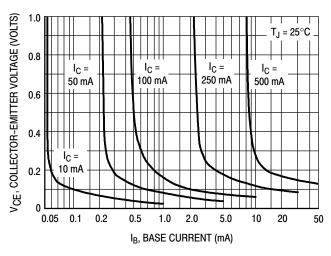


Figure 7. "ON" Voltages



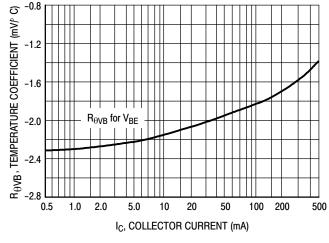


Figure 8. Collector Saturation Region

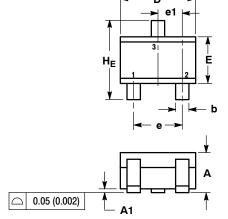
Figure 9. Base–Emitter Temperature Coefficient

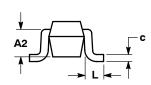


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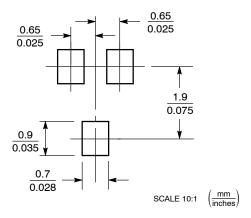




- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	MOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM



XX = Specific Device Code Μ = Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
3. COLLECTOR	3. COLLECTOR	3. DRAIN	3. CATHODE-ANODE	3. ANODE-CATHODE	3. CATHODE

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