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KSD1589

Low Frequency Power Amplifier Low Speed Switching Industrial Use

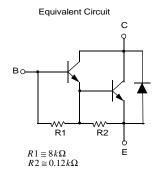
• Complement to KSB1098



NPN Silicon Darlington Transistor

Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	150	V	
V _{CEO}	Collector-Emitter Voltage	100	V	
V _{EBO}	Emitter-Base Voltage	7	V	
I _C	Collector Current (DC)	5	Α	
I _{CP}	*Collector Current (Pulse)	8	Α	
I _B	Base Current	0.5	Α	
P _C	Collector Dissipation (T _a =25°C)	1.5	W	
	Collector Dissipation (T _C =25°C)	20	W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 55 ~ 150	°C	



Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = 100V, I_{E} = 0$			1	μΑ
h _{FE1}	*DC Current Gain	$V_{CE} = 2V, I_{C} = 3A$	2K	6K	15K	
h _{FE2}		$V_{CE} = 2V, I_{C} = 5A$	500			
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_C = 3A$, $I_B = 3mA$		0.9	1.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 3A$, $I_B = 3mA$		1.6	2	V
t _{ON}	Turn On Time	$V_{CC} = 50V, I_{C} = 3A$		1		μs
t _{stg}	Storage Time	$I_{B1} = -I_{B2} = 3mA$		3.5		μs
t _f	Fall Time	$R_L = 16.7\Omega$		1.2		μs

^{*} Pulse Test: PW≤350μs, Duty Cycle≤2% Pulsed

h_{FE} Classification

Classification	R	0	Y
h _{FE1}	2000 ~ 5000	3000 ~ 7000	5000 ~ 15000

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^{*} PW≤10ms, Duty Cycle≤50%

Typical Characteristics

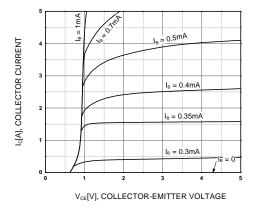


Figure 1. Static Characteristic

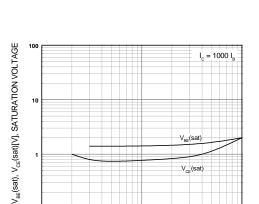


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

I_C[A], COLLECTOR CURRENT

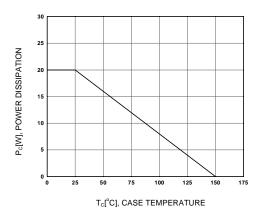


Figure 5. Power Derating

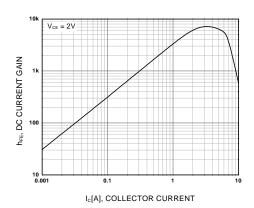


Figure 2. DC current Gain

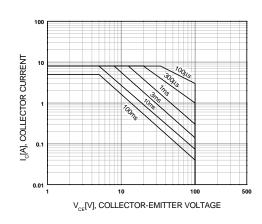
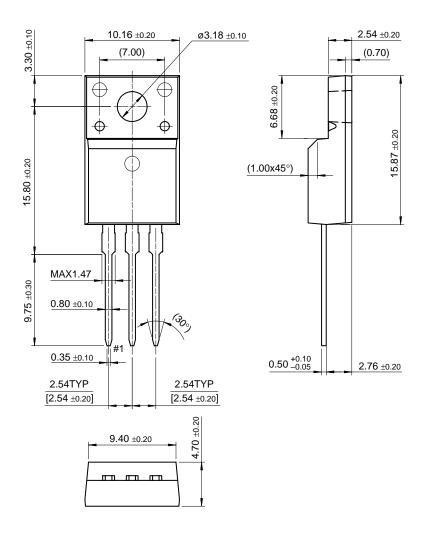


Figure 4. Safe Operating Area

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Package Dimensions

TO-220F



Dimensions in Millimeters

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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