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KSD401

TV Vertical Deflection Output

- Collector-Base Voltage: V_{CBO}=200V
 Collector Current: I_C=2A
- Collector Dissipation : P_C=25W(T_C=25°C)
- Complement to KSB546



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	200	V
V _{CEO}	Collector-Emitter Voltage	150	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current	2	Α
P _C	Collector Dissipation (T _C =25°C)	25	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

$\textbf{Electrical Characteristics} \ \, \textbf{T}_{\text{C}} = 25^{\circ} \text{C unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 500uA, I_E = 0$	200			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 10 \text{mA}, I_B = 0$	150			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -500uA, I_C = 0$	5			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 150V, I_{E} = 0$			50	μΑ
h _{FE}	DC Current Gain	$V_{CE} = 10V, I_{C} = 0.4A$	120		400	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$			1	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 0.4A$		5		MHz

h_{FE} Classification

Classification	Υ	G
h _{FE}	120 ~ 240	200 ~ 400

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Typical Characteristics

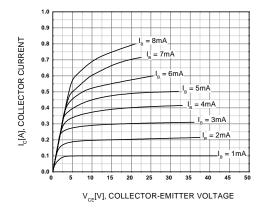


Figure 1. Static Characteristic

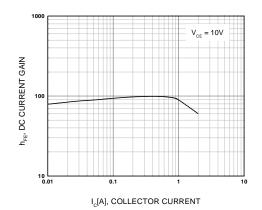


Figure 2. DC current Gain

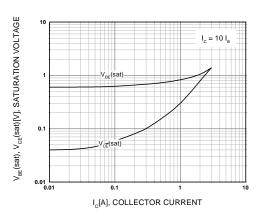


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

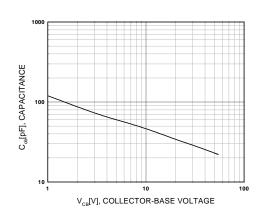


Figure 4. Collector Output Capacitance

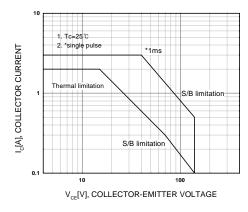


Figure 5. Safe Operating Area

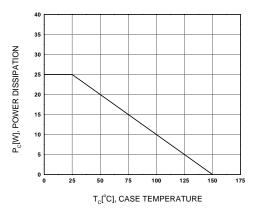
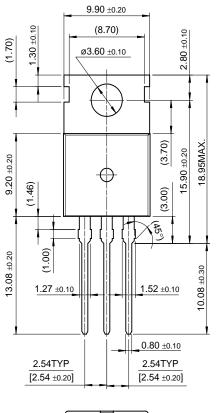


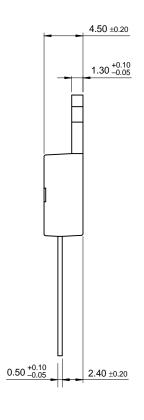
Figure 6. Power Derating

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Package Dimensions

TO-220







Dimensions in Millimeters

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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