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August 2009

KSA473 PNP Epitaxial Silicon Transistor

Features

- · Low Frequency Power Amplifier, Power Regulator
- Collector Current : I_C= -3A
- Collector Dissipation : $P_C = 10W (T_C=25^{\circ}C)$
- Complement to KSC1173



Absolute Maximum Ratings * T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	- 30	V
V _{CEO}	Collector-Emitter Voltage	- 30	V
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current	- 3	А
P _C	Collector Dissipation (T _C =25°C)	10	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 to + 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

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Electrical Characteristics $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -500 \mu A, I_E = 0$	- 30			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = - 10mA, I _B = 0	- 30			٧
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -1 \text{mA}, I_C = 0$	- 5			٧
I _{CBO}	Collector Cut-off Current	$V_{CB} = -20V, I_{E} = 0$			- 1.0	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 1.0	μΑ
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = -2V, I_{C} = -0.5A$ $V_{CE} = -2V, I_{C} = -2.5A$	70 25		240	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 2A, I _B = - 0.2A		- 0.3	- 0.8	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -2V, I_{C} = -0.5A$		- 0.75	- 1.0	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -2V, I_{C} = -0.5A$		100		MHz
C _{ob}	Output Capacitance	$V_{CB} = -10V, I_{E} = 0,$ f = 1MHz		40		pF

hFE Classification

Classification	0	Υ
h _{FE1}	70 ~ 140	120 ~ 240

Typical Performance Characteristics

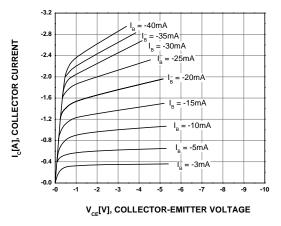


Figure 1. Static Characteristic

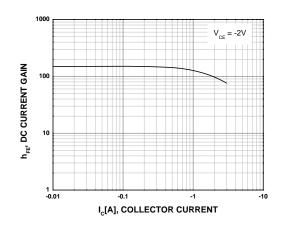


Figure 2. DC current Gain

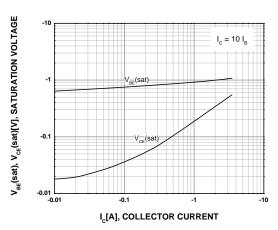


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

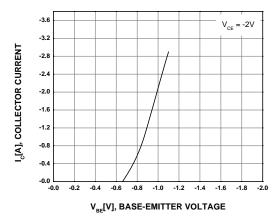


Figure 4. Base-Emitter On Voltage

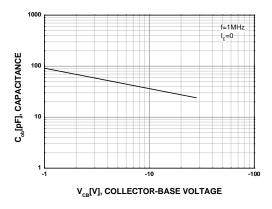
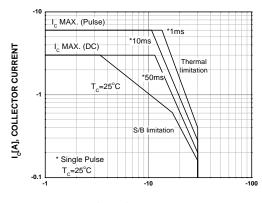


Figure 5. Collector Output Capacitance



 $\mathbf{V}_{\text{CE}}[\mathbf{V}],\,\mathbf{COLLECTOR\text{-}EMITTER}\,\,\mathbf{VOLTAGE}$

Figure 6. Safe Operating Area

Typical Performance Characteristics

(Continued)

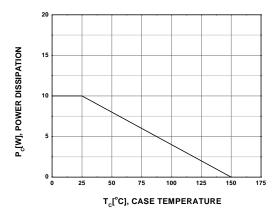
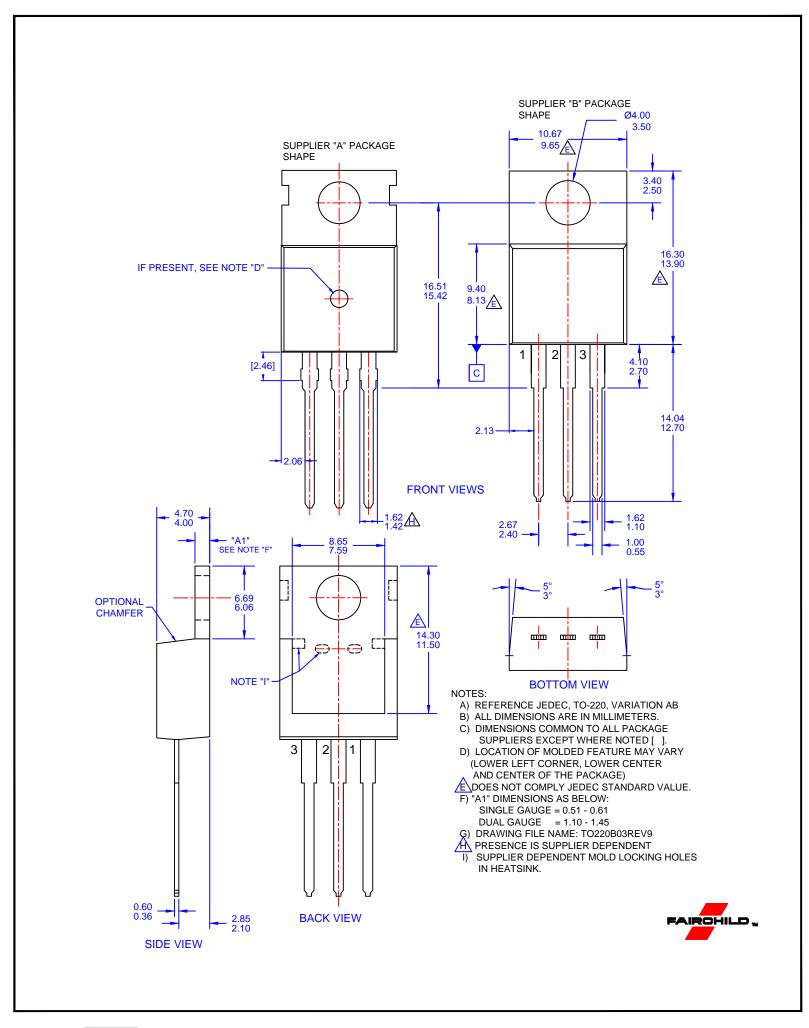


Figure 7. Power Derating



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