ON Semiconductor

Is Now



To learn more about onsemi[™], please visit our website at www.onsemi.com

onsemi and Onsemi. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,

Amplifier Transistor

PNP Silicon

Features

• These are Pb-Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-60	Vdc
Collector-Base Voltage	V _{CBO}	-60	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current - Continuous	I _C	-600	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

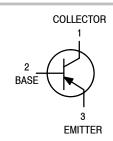
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/W

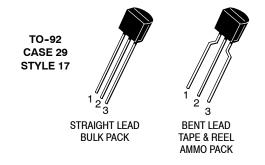
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week

= Pb-Free Package
 (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
P2N2907AG	TO-92 (Pb-Free)	5000 Units / Bulk
P2N2907ARL1G	TO-92 (Pb-Free)	2000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS		-		1	
Collector-Emitter Breakdown Voltage (Note $(I_C = -10 \text{ mAdc}, I_B = 0)$	1)	V _{(BR)CEO}	-60	-	Vdc
Collector-Base Breakdown Voltage (I _C = -10 μAdc, I _E = 0)		V _{(BR)CBO}	-60	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = -10 \mu Adc, I_C = 0$)		V _{(BR)EBO}	-5.0	-	Vdc
Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)		I _{CEX}	-	-50	nAdc
Collector Cutoff Current $(V_{CB} = -50 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -50 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C})$		I _{CBO}	-	-0.01 -10	μAdc
Emitter Cutoff Current (V _{EB} = -3.0 Vdc)		I _{EBO}	-	-10	nAdc
Collector Cutoff Current (V _{CE} = -10 V)		I _{CEO}	-	-10	nAdc
Base Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)		I _{BEX}	-	-50	nAdc
ON CHARACTERISTICS		•		-1	
$\begin{array}{l} \text{DC Current Gain} \\ & (I_C = -0.1 \text{ mAdc, } V_{CE} = -10 \text{ Vdc}) \\ & (I_C = -1.0 \text{ mAdc, } V_{CE} = -10 \text{ Vdc}) \\ & (I_C = -10 \text{ mAdc, } V_{CE} = -10 \text{ Vdc}) \\ & (I_C = -150 \text{ mAdc, } V_{CE} = -10 \text{ Vdc}) \\ & (I_C = -500 \text{ mAdc, } V_{CE} = -10 \text{ Vdc}) \end{array} (Note of the example of the exam$		h _{FE}	75 100 100 100 50	- - 300 -	-
Collector-Emitter Saturation Voltage (Note 1 ($I_C = -150 \text{ mAdc}$, $I_B = -15 \text{ mAdc}$) ($I_C = -500 \text{ mAdc}$, $I_B = -50 \text{ mAdc}$))	V _{CE(sat)}	-	-0.4 -1.6	Vdc
Base-Emitter Saturation Voltage (Note 1) (I _C = -150 mAdc, I _B = -15 mAdc) (I _C = -500 mAdc, I _B = -50 mAdc)		V _{BE(sat)}		-1.3 -2.6	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain - Bandwidth Product (Notes 1 $(I_C = -50 \text{ mAdc}, V_{CE} = -20 \text{ Vdc}, f = 100)$		f _T	200	-	MHz
Output Capacitance (V _{CB} = -10 Vdc, I _E = 0, f = 1.0 MHz)			-	8.0	pF
Input Capacitance $(V_{EB} = -2.0 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$		C _{ibo}	-	30	pF
SWITCHING CHARACTERISTICS					
Turn-On Time		t _{on}	-	50	ns
Delay Time	$(V_{CC} = -30 \text{ Vdc}, I_C = -150 \text{ mAdc},$ $I_{B1} = -15 \text{ mAdc})$ (Figures 1 and 5)	t _d	-	10	ns
Rise Time	. _{B1} = 10 117 (do) (1 igures 1 and 3)	t _r	-	40	ns
Turn-Off Time		t _{off}	-	110	ns
Storage Time	$(V_{CC} = -6.0 \text{ Vdc}, I_C = -150 \text{ mAdc}, I_{B_1} = I_{B_2} = -15 \text{ mAdc})$ (Figure 2)	ts	-	80	ns
Fall Time	101 - 102 - 10 mmae) (1 igaie 2)	t _f	-	30	ns

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%. 2. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.

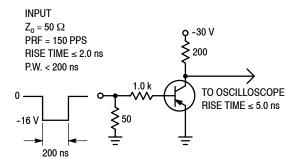


Figure 1. Delay and Rise Time Test Circuit

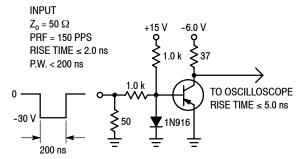


Figure 2. Storage and Fall Time Test Circuit

TYPICAL CHARACTERISTICS

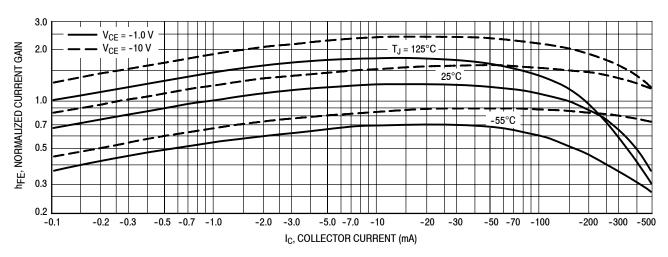


Figure 3. DC Current Gain

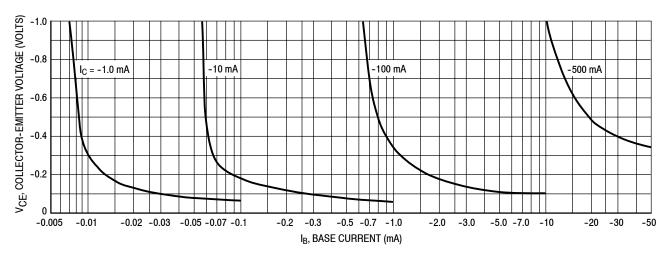


Figure 4. Collector Saturation Region

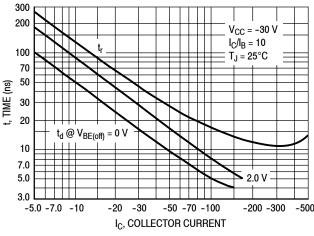


Figure 5. Turn-On Time

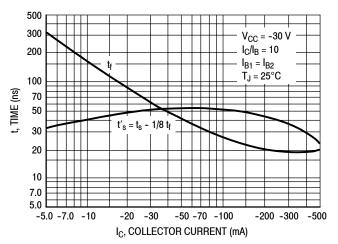


Figure 6. Turn-Off Time

TYPICAL SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

 V_{CE} = 10 Vdc, T_A = 25°C

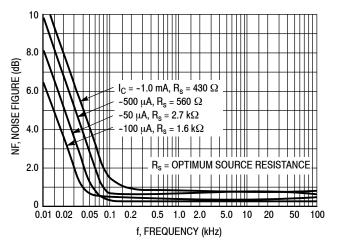


Figure 7. Frequency Effects

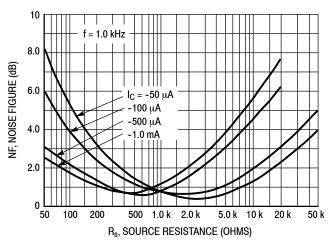


Figure 8. Source Resistance Effects

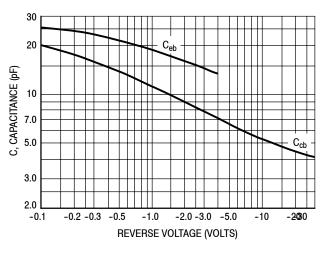


Figure 9. Capacitances

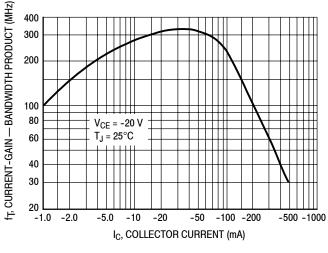


Figure 10. Current-Gain - Bandwidth Product

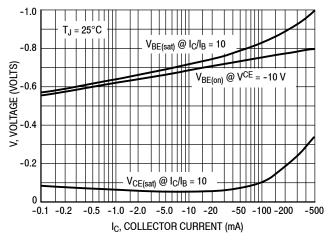


Figure 11. "On" Voltage

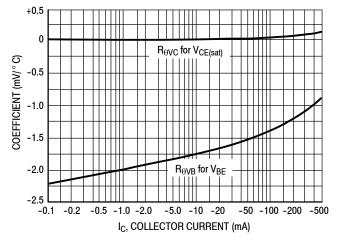
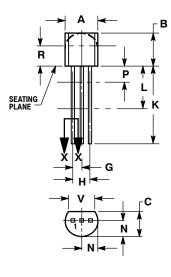


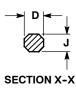
Figure 12. Temperature Coefficients

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



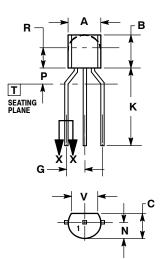
STRAIGHT LEAD **BULK PACK**



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
- IS UNCONTROLLED.
 LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
V	0 135		3 43	



BENT LEAD TAPE & REEL AMMO PACK



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
 CONTOUR OF PACKAGE BEYOND
 DIMENSION R IS UNCONTROLLED.
 LEAD DIMENSION IS UNCONTROLLED IN P
- AND BEYOND DIMENSION K MINIMUM

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.45	5.20	
В	4.32	5.33	
С	3.18	4.19	
D	0.40	0.54	
G	2.40	2.80	
7	0.39	0.50	
K	12.70		
N	2.04	2.66	
P	1.50	4.00	
R	2.93		
٧	3.43		

STYLE 17:

PIN 1. COLLECTOR

BASE

EMITTER

ON Semiconductor and 👊 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and are registered traderlanks of semiconduction Components industries, LC (SCILLC) scillct Cestives are injult of make changes without further holice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA **Phone**: 303-675-2175 or 800-344-3860 Toll Free USA/Canada **Fax**: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center

Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative