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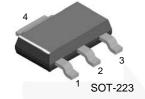
January 2014



# NZT6729 PNP General-Purpose Amplifier

# Description

This device is designed for general-purpose mediumpower amplifiers and switches for collector currents to 800 mA. Sourced from process 79.



1. Base 2,4. Collector 3. Emitter

# **Ordering Information**

Part Number	Part Number Marking		Packing Method	
NZT6729	6729	SOT-223 4L	Tape and Reel	

## Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-80	V
V <sub>CBO</sub>	Collector-Base Voltage	-80	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
۱ <sub>C</sub>	Collector Current - Continuous	-1	Α
T <sub>J</sub> , T <sub>STG</sub>	3 Operating and Storage Junction Temperature Range -55 to -		°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady state limits. Fairchild Semiconductor should be consulted on application involving pulsed or low-duty cycle operation.

# Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
P <sub>D</sub>	Total Device Dissipation	1.0	W
	Derate Above 25°C	8.0	mW/°C
R <sub>0JA</sub>	Thermal Resistance, Junction to Ambient		°C/W

#### Note:

3. PCB size: FR-4 76 x 114 x 1.57 mm<sup>3</sup> (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

# **Electrical Characteristics**

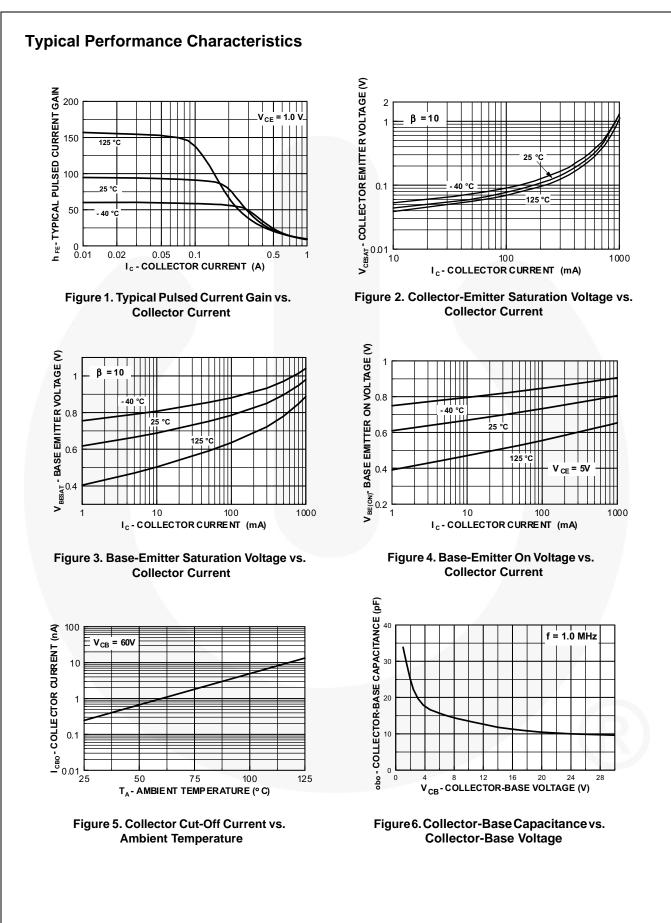
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1.0 mA, I <sub>B</sub> = 0	-80		V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -100 μA, I <sub>E</sub> = 0	-80		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -1.0 mA, I <sub>C</sub> = 0	-5.0		V
I <sub>CBO</sub>	Collector-Base Cut-Off Current	$V_{CB} = -60 \text{ V}, \text{ I}_{E} = 0$		-0.1	μA
I <sub>EBO</sub>	Emitter-Base Cut-Off Current	$V_{EB} = -5.0 \text{ V}, I_{C} = 0$		-10	μA
h <sub>FE</sub> D	DC Current Gain <sup>(4)</sup>	$I_{\rm C}$ = -50 mA, $V_{\rm CE}$ = -1.0 V	80		
		$I_{\rm C}$ = -250 mA, $V_{\rm CE}$ = -1.0 V	50	250	
		$I_{\rm C}$ = -500 mA, $V_{\rm CE}$ = -1.0 V	20		
	Collector-Emitter Saturation Voltage <sup>(4)</sup>	$I_{\rm C}$ = -250 mA, $I_{\rm B}$ = -10 mV		-0.50	V
		I <sub>C</sub> = -250 mA, I <sub>B</sub> = -25 mV		-0.35	v
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(4)</sup>	I <sub>C</sub> = -250 mA, V <sub>CE</sub> = -1.0 V		-1.2	V
h <sub>fe</sub>	Small-Signal Current Gain	I <sub>C</sub> = -200 mA, V <sub>CE</sub> = -5.0 V, f = 20 MHz	2.5	25	
C <sub>cb</sub>	Collector-Base Capacitance	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1.0 \text{ MHz}$		30	pF

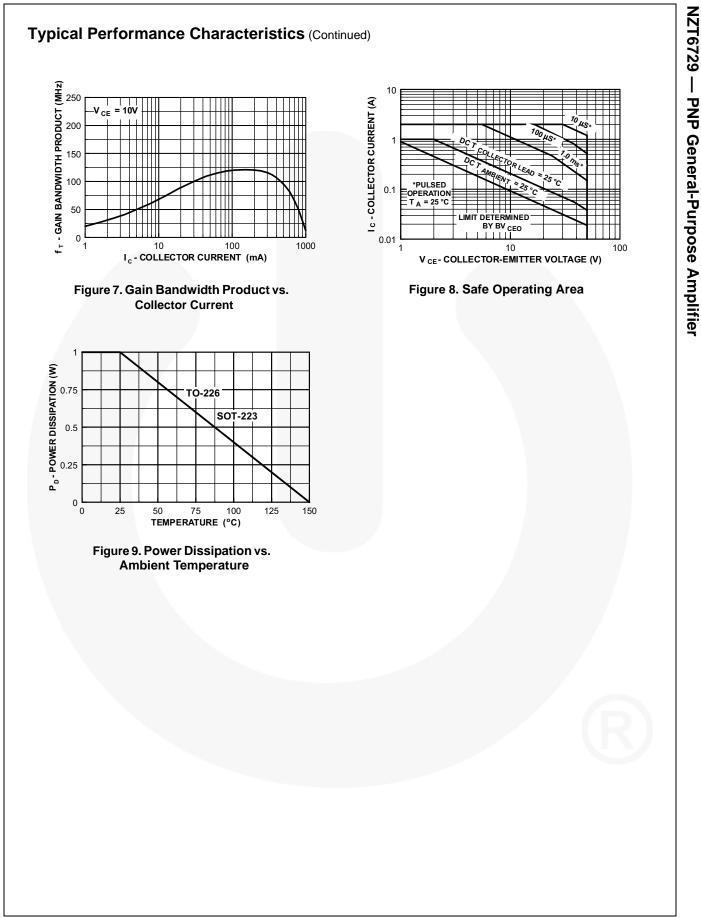
#### Note:

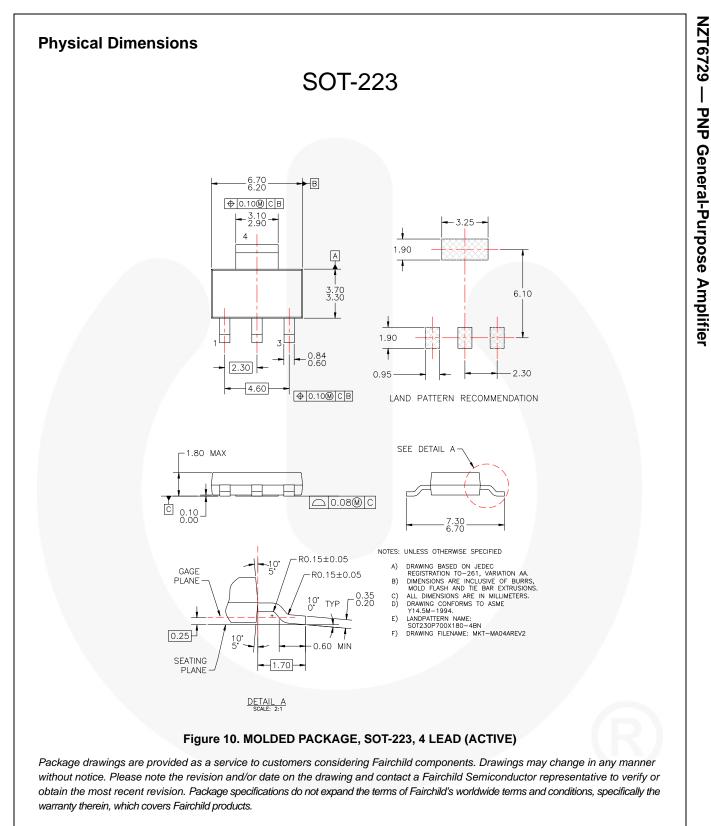
4. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%.





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Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: <u>http://www.fairchildsemi.com/dwg/MA/MA04A.pdf</u>.

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
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