Preferred Device

Power MOSFET 170 mAmps, 100 Volts

N-Channel SOT-23

Features

• Pb-Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	100	Vdc
Gate–Source Voltage – Continuous – Non–repetitive (t _p ≤ 50 μs)	V _{GS} V _{GSM}	±20 ±40	Vdc Vpk
Drain Current - Continuous (Note 1) - Pulsed (Note 2)	I _D	0.17 0.68	Adc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 3) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

- The Power Dissipation of the package may result in a lower continuous drain current.
- 2. Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.
- 3. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

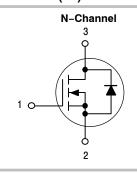


ON Semiconductor®

http://onsemi.com

170 mAMPS 100 VOLTS

 $R_{DS(on)} = 6 \Omega$



MARKING DIAGRAM

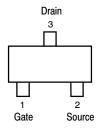


SOT-23 CASE 318 STYLE 21



SA M Device CodeDate Code

PIN ASSIGNMENT



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS							
Drain–Source Breakdown Voltage (V _{GS} = 0, I _D = 250 μAdc)		V _{(BR)DSS}	100	_	_	Vdc	
Zero Gate Voltage Drain Current $ (V_{GS} = 0, V_{DS} = 100 \text{ Vdc}) \qquad T_J = 25^{\circ}\text{C} $ $ T_J = 125^{\circ}\text{C} $		I _{DSS}	- -	- -	15 60	μAdc	
Gate-Body Leakage Current (V _{GS} = 20 Vdc, V _{DS} = 0)			-	-	50	nAdc	
ON CHARACTERISTICS (Note 4)		·				•	
Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 1.0 mAdc)		V _{GS(th)}	0.8	-	2.8	Vdc	
Static Drain–Source On–Resistance $(V_{GS} = 10 \text{ Vdc}, I_D = 100 \text{ mAdc})$		r _{DS(on)}	-	5.0	6.0	Ω	
Forward Transconductance (V _{DS} = 25 Vdc, I _D = 100 mAdc)			80	-	_	mmhos	
DYNAMIC CHARACTERISTICS		•			•	•	
Input Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{iss}	-	20	_	pF	
Output Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{oss}	-	9.0	_	pF	
Reverse Transfer Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{rss}	-	4.0	_	pF	
SWITCHING CHARACTERISTICS ⁽⁴⁾							
Turn-On Delay Time	(V _{CC} = 30 Vdc, I _C = 0.28 Adc,	t _{d(on)}	-	20	-	ns	
Turn-Off Delay Time	V_{GS} = 10 Vdc, R_{GS} = 50 Ω)	t _{d(off)}	_	40	_	ns	
REVERSE DIODE							
Diode Forward On-Voltage (I _D = 0.34 Adc, V _{GS} = 0 Vdc)		V _{SD}	-	-	1.3	V	

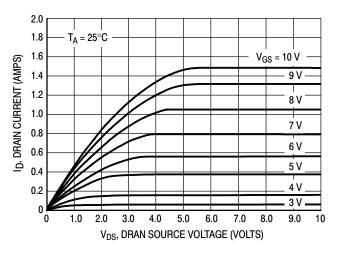
^{4.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Device	Package	Shipping [†]
BSS123LT1	SOT-23	3,000 Tape & Reel
BSS123LT1G	SOT-23 (Pb-Free)	3,000 Tape & Reel
BSS123LT3	SOT-23	10,000 Tape & Reel
BSS123LT3G	SOT-23 (Pb-Free)	10,000 Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

TYPICAL ELECTRICAL CHARACTERISTICS



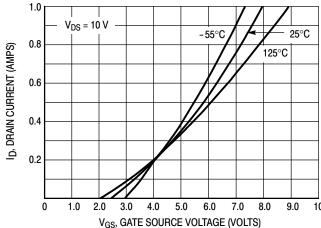
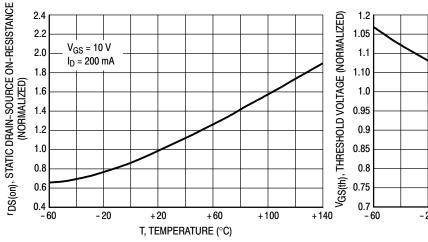


Figure 1. Ohmic Region

Figure 2. Transfer Characteristics



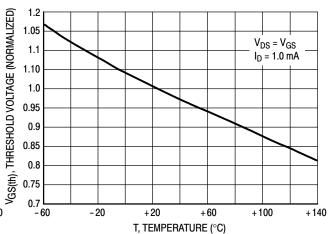
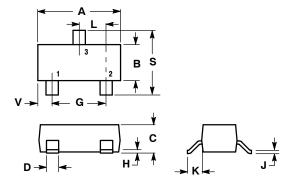


Figure 3. Temperature versus Static Drain-Source On-Resistance

Figure 4. Temperature versus Gate Threshold Voltage

PACKAGE DIMENSIONS

SOT-23 (TO - 236)CASE 318-08 **ISSUE AK**



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- T14.3M, 1962.
 CONTROLLING DIMENSION: INCH.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD
 FINISH THICKNESS. MINIMUM LEAD
 THICKNESS IS THE MINIMUM THICKNESS OF
- BASE MATERIAL. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

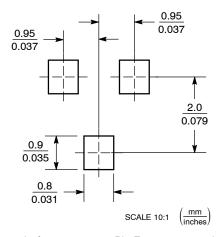
	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
Н	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

STYLE 21:

PIN 1. GATE

- 2. SOURCE
- DRAIN

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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