MMBD914L

Switching Diode, High Speed, 100 V

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	100	V
Forward Current	I _F	200	mA
Non-Repetitive Peak Forward Surge Current t = 1 sec t = 1 μsec	I _{FSM}	1.0 2.0	A
Peak Forward Surge Current	I _{FM(surge)}	500	mA

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C	P _D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) $T_{\Delta} = 25^{\circ}C$	P _D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	417	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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SOT-23 CASE 318 STYLE 8



MARKING DIAGRAM



5D = Device Code

M = Date Code*

Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBD914LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SMMBD914LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MMBD914LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
SMMBD914LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

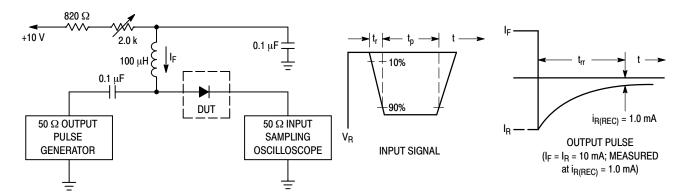
MMBD914L

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	•			
Reverse Breakdown Voltage ($I_R = 100 \mu Adc$)	V _(BR)	100	-	Vdc
Reverse Voltage Leakage Current (V _R = 20 Vdc) (V _R = 75 Vdc)	I _R	- -	25 5.0	nAdc μAdc
Diode Capacitance (V _R = 0, f = 1.0 MHz)	C _T	-	4.0	pF
Forward Voltage (I _F = 10 mAdc)	V _F	-	1.0	Vdc
Reverse Recovery Time (I _F = I _R = 10 mAdc) (Figure 1)	t _{rr}	-	4.0	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in. 2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

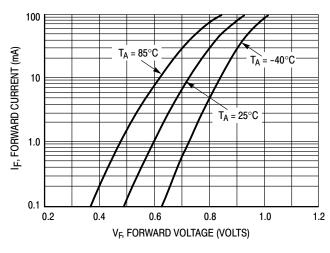


Notes: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

MMBD914L



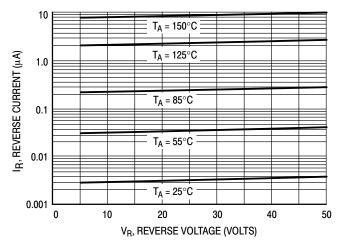


Figure 2. Forward Voltage

Figure 3. Leakage Current

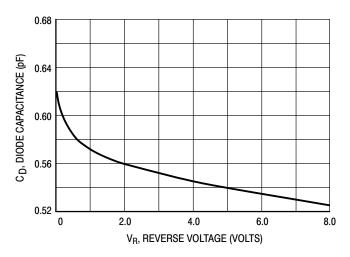


Figure 4. Capacitance



SOT-23 (TO-236) CASE 318-08 **ISSUE AS**

DATE 30 JAN 2018

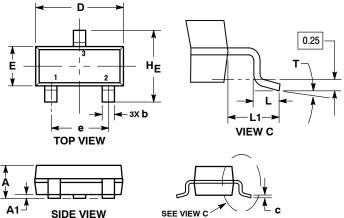
SCALE 4:1

CTVLE 4 TUDULE.

DRAIN

3. CATHODE

STYLE 27: PIN 1. CATHODE 2. CATHODE



END VIEW

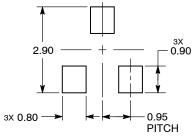
CTVLE 7

NOTES:

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

RECOMMENDED SOLDERING FOOTPRINT



CTVLE 6.

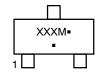
DIMENSIONS: MILLIMETERS

3. INPUT

3. ANODE

STYLE 28: PIN 1. ANODE 2. ANODE

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5:	STYLE 6:	STYLE 7:	STYLE 8:		
CANCELLED	PIN 1. BASE	PIN 1. EMITTER	PIN 1. ANODE		
	EMITTER	2. BASE	NO CONNECTION		
	3. COLLECTOR	3. COLLECTOR	3. CATHODE		
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. SOURCE	PIN 1. CATHODE
ANODE	2. SOURCE	CATHODE	CATHODE	2. DRAIN	2. GATE
3. CATHODE	3. GATE	CATHODE-ANODE	3. ANODE	3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION		PIN 1. CATHODE
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE	2. ANODE
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANOD	
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
2. SOUNCE	2. 001F01	2. ANODE	2. DITAIN	2. OATTODE	2. ANODE

CATHODE

CTVI F O

3. SOURCE

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