ON Semiconductor

Is Now

Onsemi

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Switch Mode Power Rectifier

These state-of-the-art devices are designed for use in negative switching power supplies, inverters and as free wheeling diodes. Also, used in conjunction with common cathode dual Ultrafast Rectifiers, makes a single phase full-wave bridge.

Features

- Common Anode Dual Rectifier (8.0 A per Leg or 16 A per Package)
- Ultrafast 35 Nanosecond Reverse Recovery Times
- Exhibits Soft Recovery Characteristics
- High Temperature Glass Passivated Junction
- Low Leakage Specified @ 150°C Case Temperature
- Current Derating @ Both Case and Ambient Temperatures
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Complement to MUR1620CT and MURB1620CT Common Cathode Device
- ESD Ratings:
 - Machine Model = C (> 400 V)
 - Human Body Model = 3B (> 16,000 V)
- NRVU Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These are Pb–Free Devices*

Mechanical Characteristics:

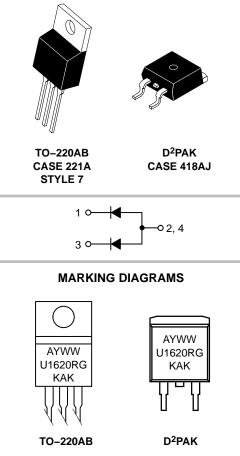
- Case: Epoxy, Molded
- Weight: MUR1620CTR: 1.9 Grams (Approximately) MURB1620CTR: 1.7 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds



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ULTRAFAST RECTIFIER 16 AMPERES, 200 VOLTS



U1620R	= Device Code
KAK	= Diode Polarity
А	= Assembly Location
Y	= Year
WW	= Work Week
G	= Pb–Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	V
Average Rectified Forward Current (Rated V_R , $T_C = 160^{\circ}C$) Per Leg Per Total Device	I _{F(AV)}	8.0 16	A
Peak Repetitive Surge Current (Rated V _R , Square Wave, 20 kHz, T _C = 140°C) Per Diode	I _{FM}	16	A
Non–Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	100	A
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-65 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	2.0	°C/W
Thermal Resistance, Junction-to-Ambient (D ² PAK)	$R_{ hetaJA}$	45	°C/W

ELECTRICAL CHARACTERISTICS (Per Leg)

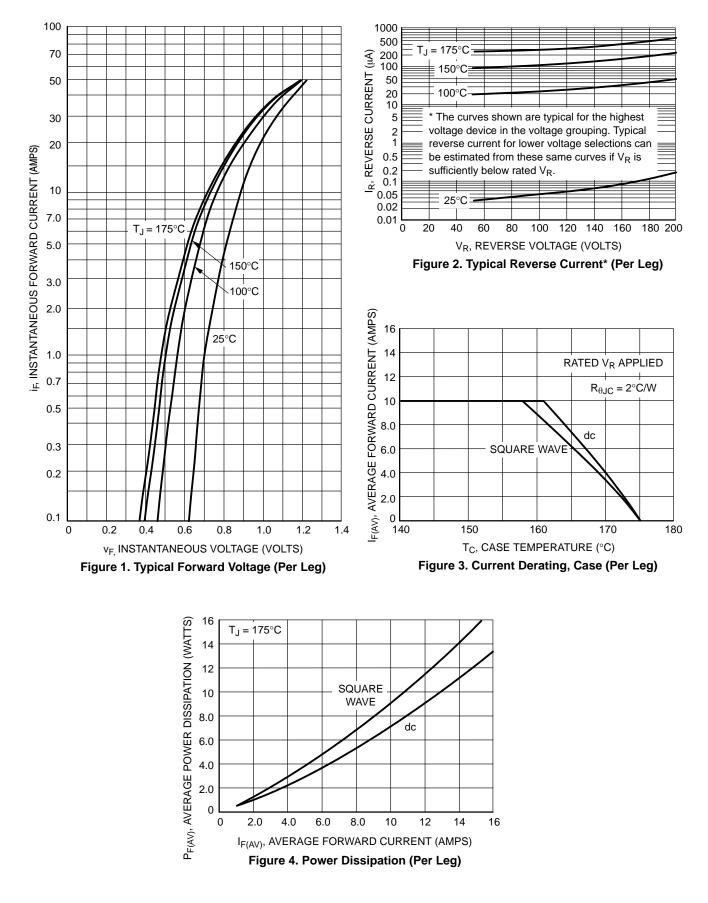
Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 8.0 \text{ Amps}, T_C = 25^{\circ}C$) ($i_F = 8.0 \text{ Amps}, T_C = 150^{\circ}C$)	VF	1.2 1.1	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_C = 25^{\circ}C$) (Rated dc Voltage, $T_C = 150^{\circ}C$)	i _R	5.0 500	μΑ
Maximum Reverse Recovery Time (I _F = 1.0 Amp, di/dt = 50 Amps/μs)	t _{rr}	85	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Pulse Test: Pulse Width = 5.0 ms, Duty Cycle $\leq 10\%$.

ORDERING INFORMATION

Device	Package	Shipping [†]
MUR1620CTRG	TO-220 (Pb-Free)	50 Units / Rail
MURB1620CTRG	D ² PAK–3 (Pb–Free)	50 Units / Rail
MURB1620CTRT4G	D ² PAK–3 (Pb–Free)	800 / Tape & Reel
NRVUB1620CTRT4G	D ² PAK–3 (Pb–Free)	800 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



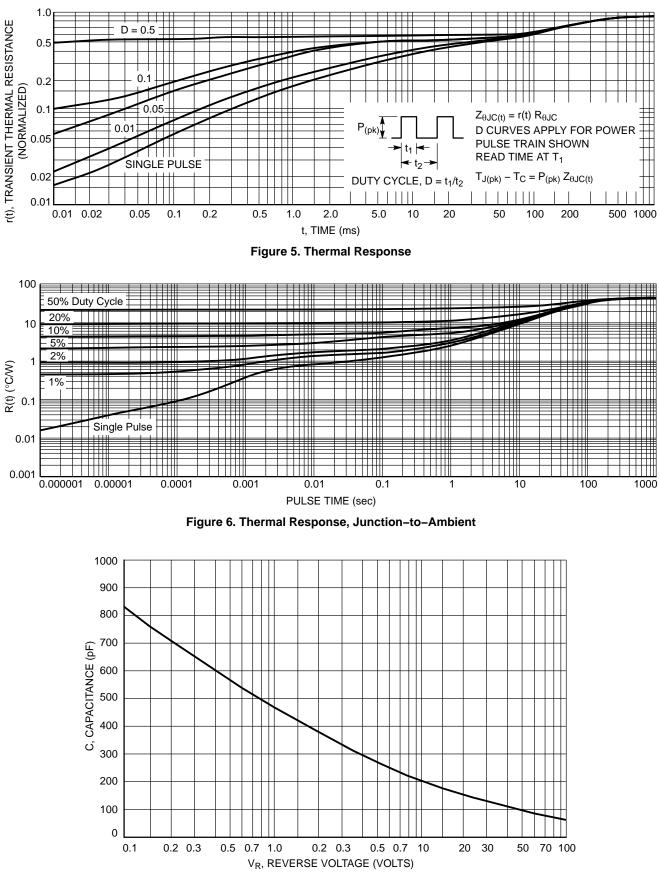


Figure 7. Typical Capacitance (Per Leg)

PACKAGE DIMENSIONS

MILLIMETERS

MIN MAX

4.83

0.25

0.99

0.74

1.65

9.65

10.67

15.88

2.79

1.68

1.78

8

4.06

0.51

0.30

1.14

8.38

6.60

9.65

6.22

14.60

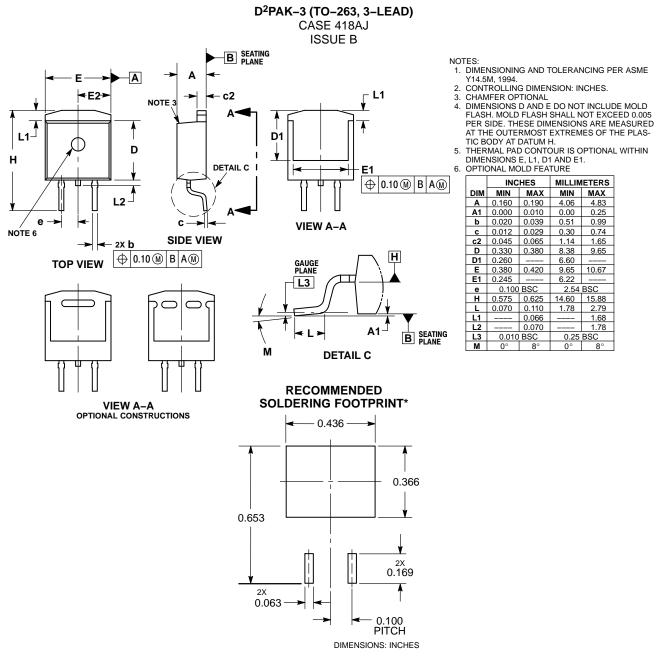
0.066

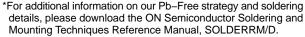
0.070

1.78

2.54 BSC

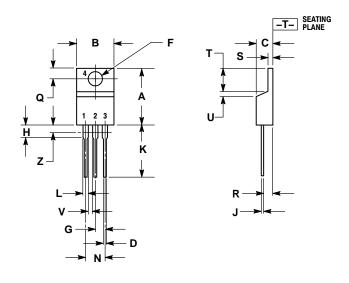
0.25 BSC 0°





PACKAGE DIMENSIONS

TO-220 CASE 221A-09 ISSUE AH



DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED. MILLIMETERS INCHES MIN MAX 0.570 0.620 DIM MIN MAX 14.48 15.75 Α В 0.380 0.415 10.53 9.66 C D 0.160 0.190 4.07 4.83 0.025 0.038 0.64 0.96 F 0.142 0.161 3.61 4.09 G 0.095 0.105 2.42 2.66 Н 0.110 0.161 2.80 4.10 J 0.014 0.024 0.36 0.61 Κ 0.500 0.562 12.70 14.27 0.060 LN 0.045 1.15 1.52 0.190 0.210 4.83 5.33 Q 0.100 0.120 2.54 3.04 R 0.080 0.110 2.04 2.79 S T 0.045 0.055 1.15 1.39 0.235 0.255 5.97 6.47 U 0.000 0.050 0.00 1.27 ۷ 0.045 1.15 Z 0.080 2.04 STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE ANODE 4.

DIMENSIONING AND TOLERANCING PER ANSI

Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

NOTES

1.

3.

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