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March 2015

FDD8782/FDU8782 N-Channel PowerTrench® MOSFET 25V, 35A, $11m\Omega$

General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $r_{\text{DS}(\text{on})}$ and fast switching speed.

Application

- Vcore DC-DC for Desktop Computers and Servers
- VRM for Intermediate Bus Architecture

Features

- Max $r_{DS(on)} = 11.0 m\Omega$ at $V_{GS} = 10 V$, $I_D = 35 A$
- Max $r_{DS(on)}$ = 14.0m Ω at V_{GS} = 4.5V, I_D = 35A
- Low gate charge: $Q_{g(10)} = 18nC(Typ)$, $V_{GS} = 10V$
- Low gate resistance
- Avalanche rated and 100% tested
- RoHS Compliant





MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		Ratings	Units
V_{DS}	Drain to Source Voltage		25	V
V_{GS}	Gate to Source Voltage		±20	V
	Drain Current -Continuous (Package Limited)		35	
I _D	-Continuous (Die Limited)		54	Α
	-Pulsed	(Note 1)	321	
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	72	mJ
P_{D}	Power Dissipation		50	W
T_J , T_{STG}	Operating and Storage Temperature		-55 to 175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case TO-252,TO-251	3.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient TO-252,TO-251	100	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient TO-252,1in ² copper pad area	52	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD8782	FDD8782	TO-252AA	13"	16mm	2500 units
FDU8782	FDU8782	TO-251AA	N/A(Tube)	N/A	75 units
FDU8782	FDU8782_F071	TO-251AA	N/A(Tube)	N/A	75 units

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Electrical Characteristics T _J = 25°C unless otherwise not

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	cteristics					
B _{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	25			V
$\frac{\Delta B_{VDSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25°C		14.3		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20V,$ $V_{GS} = 0V$ $T_{J} = 150^{\circ}C$			1 250	μА
I_{GSS}	Gate to Source Leakage Current	V _{GS} = ±20V			±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	1.2	1.7	2.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25°C		-6.5		mV/°C
r _{DS(on)}	Drain to Source On Resistance	V _{GS} = 10V, I _D = 35A		8.5	11.0	
		V _{GS} = 4.5V, I _D = 35A		11.0	14.0	mΩ
	Drain to course on registance	V_{GS} = 10V, I_D = 35A T_J = 175°C		12.1	18.0	11152

Dynamic Characteristics

C _{iss}	Input Capacitance	V - 42V V - 0V	920	1220	pF
Coss	Output Capacitance	V _{DS} = 13V, V _{GS} = 0V, f = 1MHz	230	310	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1101112	160	240	pF
R _g	Gate Resistance	f = 1MHz	1.4		Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		7	14	ns
t _r	Rise Time	$V_{DD} = 13V, I_{D} = 35A$ $V_{GS} = 10V, R_{GS} = 9\Omega$	9	18	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10V, R _{GS} = 9Ω	22	36	ns
t _f	Fall Time		14	25	ns
Q_g	Total Gate Charge	V _{GS} = 0V to 10V	18	25	nC
Qg	Total Gate Charge	$V_{GS} = 0V \text{ to } 5V$ $V_{DD} = 13V$ $I_{D} = 35A$	9.4	13	nC
Q_{gs}	Gate to Source Gate Charge	$I_{\rm D} = 35A$ $I_{\rm a} = 1.0 {\rm mA}$	3.1		nC
Q _{gd}	Gate to Drain "Miller" Charge		4.0		nC

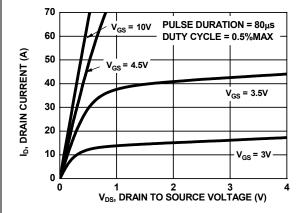
Drain-Source Diode Characteristics

V _{SD}		V _{GS} = 0V, I _S = 35A	0.96	1.25	V
		V _{GS} = 0V, I _S = 15A	0.86	1.2	v
t _{rr}	Reverse Recovery Time	I _F = 35A, di/dt = 100A/μs	25	38	ns
Q _{rr}	Reverse Recovery Charge	I _F = 35A, di/dt = 100A/μs	17	26	nC

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Notes:
1: Pulse time < 300us, Duty cycle = 2%.
2: Starting T_J = 25°C, L = 1.0mH, I_{AS} = 12A, V_{DD} = 23V, V_{GS} = 10V.





PULSE DURATION = 80μs
DUTY CYCLE = 0.5%MAX

V_{GS} = 3V

V_{GS} = 3.5V

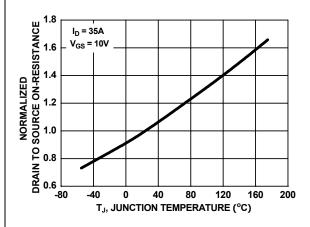
V_{GS} = 4.5V

V_{GS} = 10V

I_D, DRAIN CURRENT(A)

Figure 1. On Region Characteristics

Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage



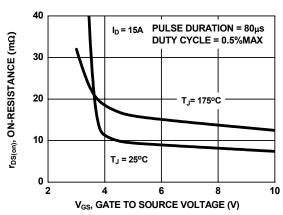
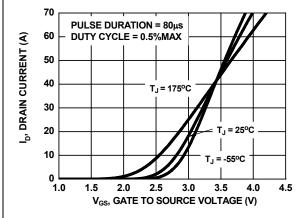


Figure 3. Normalized On Resistance vs Junction Temperature

Figure 4. On-Resistance vs Gate to Source Voltage



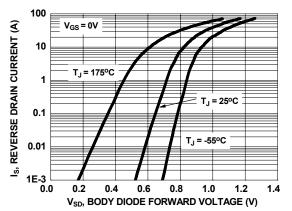
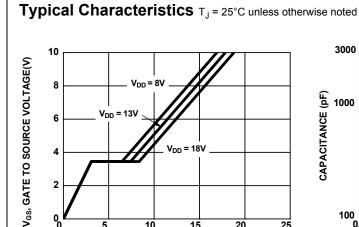


Figure 5. Transfer Characteristics

Figure 6. Source to Drain Diode Forward Voltage vs Source Current

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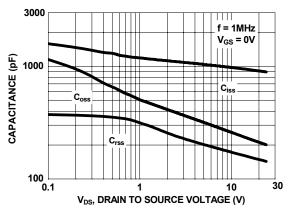
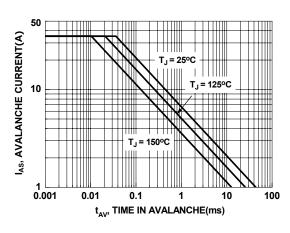


Figure 7. Gate Charge Characteristics

Qg, GATE CHARGE(nC)

20

Figure 8. Capacitance vs Drain to Source Voltage



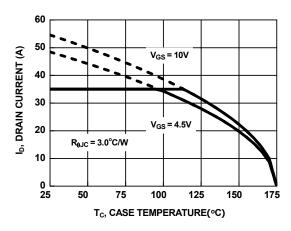
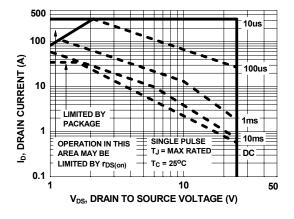


Figure 9. Unclamped Inductive Switching Capability

Figure 10. Maximum Continuous Drain Current vs **Case Temperature**



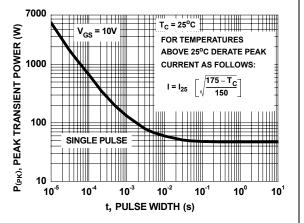


Figure 11. Forward Bias Safe Operating Area

Figure 12. Single Pulse Maximum Power Dissipation

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10¹

10°

10⁻¹

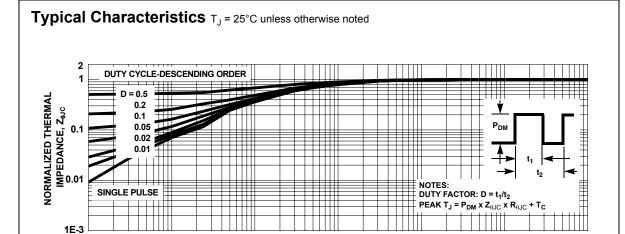
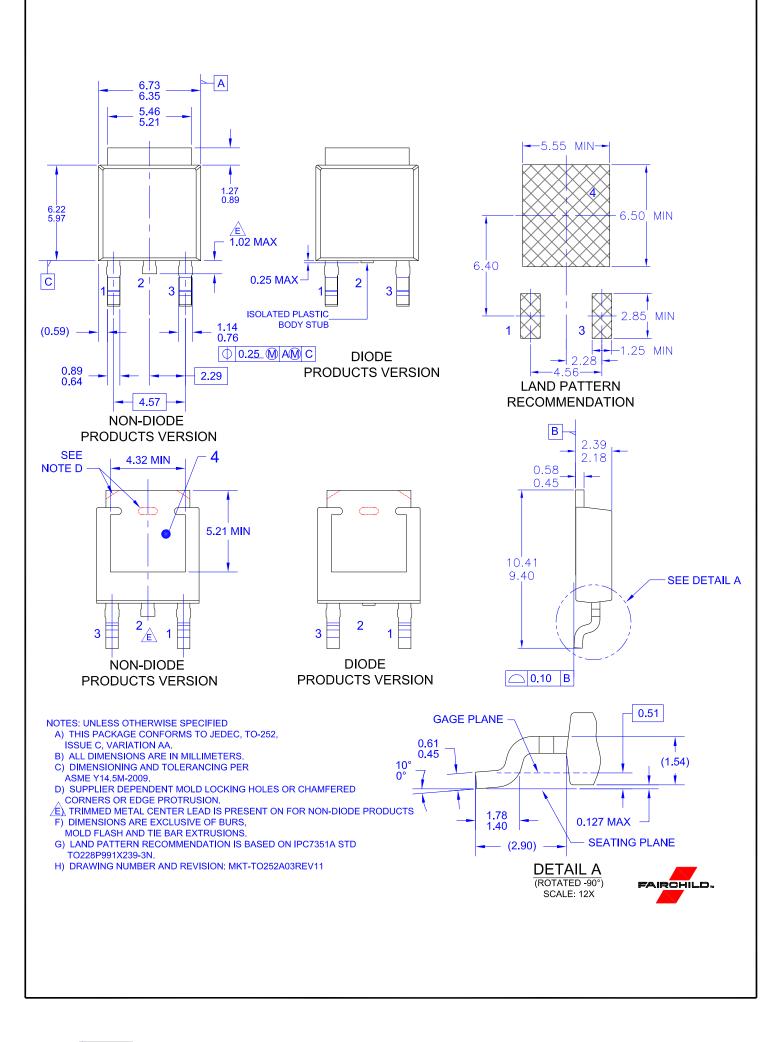


Figure 13. Transient Thermal Response Curve

10³ 10² t, RECTANGULAR PULSE DURATION(s)

10⁻⁵

10⁴



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