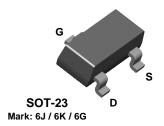


PN4391 PN4392 **PN4393** MMBF4391 MMBF4392 **MMBF4393**





NOTE: Source & Drain are interchangeable

N-Channel Switch

This device is designed for low level analog switching, sample and hold circuits and chopper stabalized amplifiers. Sourced from Process 51. See J111 for characteristics.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	30	V
V _{GS}	Gate-Source Voltage	- 30	V
I _{GF}	Forward Gate Current	50	mA
T _J ,T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		PN4391-4393	*MMBF4391-4393	
P_D	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

^{*}Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

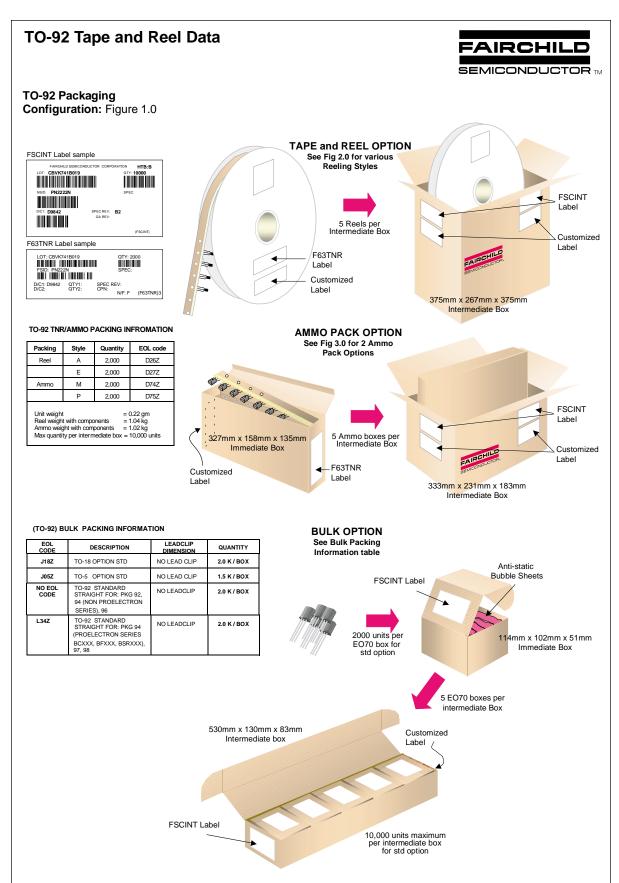
©1997 Fairchild Semiconductor Corporation

N-Channel Switch

(continued)

Symbol	Parameter	i	Min	Max	Units	
OFF CHAF	RACTERISTICS					
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$		- 30		V
I _{GSS}	Gate Reverse Current	V _{GS} = - 15 V, V _{DS} = 0			- 1.0	nA
		$V_{GS} = -15 \text{ V}, V_{DS} = 0, T_A =$	150°C		- 0.2	μΑ
V _{GS(off)}	Gate-Source Cutoff Voltage	$V_{DS} = 20 \text{ V}, I_{D} = 1.0 \text{ nA}$	4391	- 4.0	- 10	V
			4392 4393	- 2.0 - 0.5	- 5.0 - 3.0	V
V _{GS(f)}	Gate-Source Forward Voltage	I _G = 1.0 mA, V _{DS} = 0	4000		1.0	V
I _{D(off)}	Drain Cutoff Leakage Current	V _{DS} = 20 V, V _{GS} = - 12 V	4391		0.1	nA
, ,	_	$V_{DS} = 20 \text{ V}, V_{GS} = -7.0 \text{ V}$	4392		0.1	nA
		$V_{DS} = 20 \text{ V}, V_{GS} = -5.0 \text{ V}$	4393		0.1	nA
		V_{DS} = 20 V, V_{GS} = - 12 V, T_A = 150°C	4391		0.2	μА
		V _{DS} = 20 V, V _{GS} = - 7.0 V,				•
		T _A = 150°C	4392		0.2	μΑ
		$V_{DS} = 20 \text{ V}, V_{GS} = -5.0 \text{ V},$	4393		0.2	μА
		T _A = 150°C	4393			μιτ
ON CHARA	ACTERISTICS					
I _{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 20 \text{ V}, V_{GS} = 0$	4391	50	150	mA
			4392	25	75	mA
	D : 0 0 1/4		4393	5.0	30	mA
$V_{DS(on)}$	Drain-Source On Voltage	$I_D = 12 \text{ mA}, V_{GS} = 0$ $I_D = 6.0 \text{ mA}, V_{GS} = 0$	4391 4392		0.4 0.4	V V
		$I_D = 3.0 \text{ mA}, V_{GS} = 0$ $I_D = 3.0 \text{ mA}, V_{GS} = 0$	4393		0.4	v
r _{DS(on)}	Drain-Source On Resistance	$I_D = 1.0 \text{ mA}, V_{GS} = 0$	4391		30	Ω
			4392		60	Ω
			4393		100	Ω
SMALL-SI	GNAL CHARACTERISTICS					
r _{ds(on)}	Drain-Source On Resistance	$V_{DS} = V_{GS} = 0$, f= 1.0 kHz	4391		30	Ω
			4392		60	Ω
C:	Input Capacitance	V _{DS} = 20, V _{GS} = 0, f = 1.0 M	4393		100 14	Ω pF
Ciss	Reverse Transfer Capacitance	V _{GS} = - 12 V, f = 1.0 MHz	4391		3.5	рF
Crss	Reverse Transfer Capacitance	$V_{GS} = -7.0 \text{ V}, f = 1.0 \text{ MHz}$	4392		3.5	pF
		V _{GS} = - 5.0 V, f = 1.0 MHz	4393		3.5	pF
0.4/1=0.111	. IO OLIA DA OTERIOTICO					
	NG CHARACTERISTICS	10 55 4	1001		F 0	
t _r	Rise Time	$I_{D(on)} = 12 \text{ mA}$ $I_{D(on)} = 6.0 \text{ mA}$	4391 4392		5.0 5.0	ns ns
		$I_{D(on)} = 6.0 \text{ mA}$ $I_{D(on)} = 3.0 \text{ mA}$	4392		5.0	ns
t _f	Fall Time	$V_{GS(Off)} = 12 \text{ V}$	4391		15	ns
		$V_{GS(off)} = 6.0 \text{ V}$	4392		20	ns
		$V_{GS(off)} = 3.0 \text{ V}$	4393		30	ns
ton	Turn-On Time	$I_{D(on)} = 12 \text{ mA}$	4391		15	ns
ton	į.	$I_{D(on)} = 6.0 \text{ mA}$	4392		15 15	ns ns
t _{on}		1 00 4				
	Turn-Off Time	$I_{D(on)} = 3.0 \text{ mA}$	4393			
t _{off}	Turn-Off Time	$I_{D(on)} = 3.0 \text{ mA}$ $V_{GS(off)} = 12 \text{ V}$ $V_{GS(off)} = 6.0 \text{ V}$	4393 4391 4392		20 35	ns ns

*Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1.0%

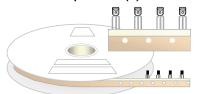


©2001 Fairchild Semiconductor Corporation

TO-92 Tape and Reel Data, continued

TO-92 Reeling Style Configuration: Figure 2.0

Machine Option "A" (H)



Style "A", D26Z, D70Z (s/h)

Machine Option "E" (J)

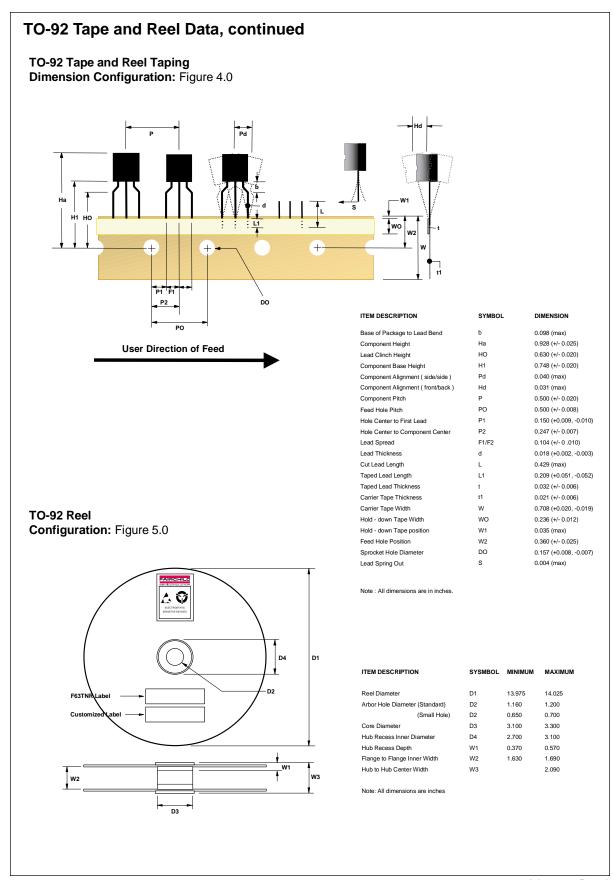
Style "E", D27Z, D71Z (s/h)

TO-92 Radial Ammo Packaging Configuration: Figure 3.0





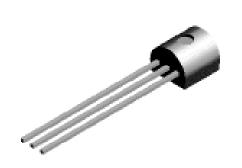
FIRST WIRE OFF IS COLLECTOR (ON PKG. 92) ADHESIVE TAPE IS ON BOTTOM SIDE FLAT OF TRANSISTOR IS ON TOP

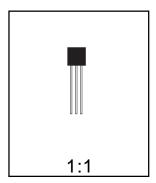


TO-92 Package Dimensions



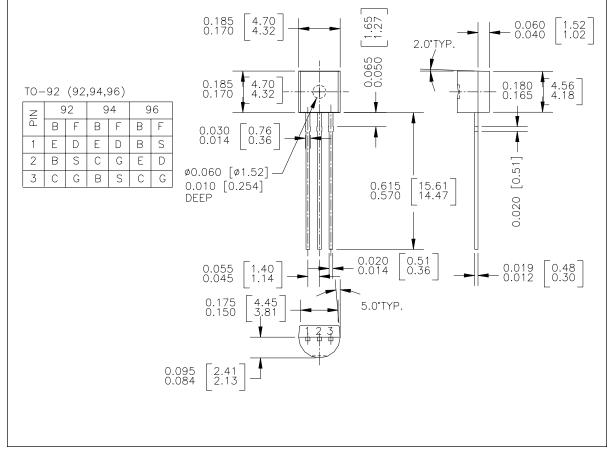
TO-92 (FS PKG Code 92, 94, 96)





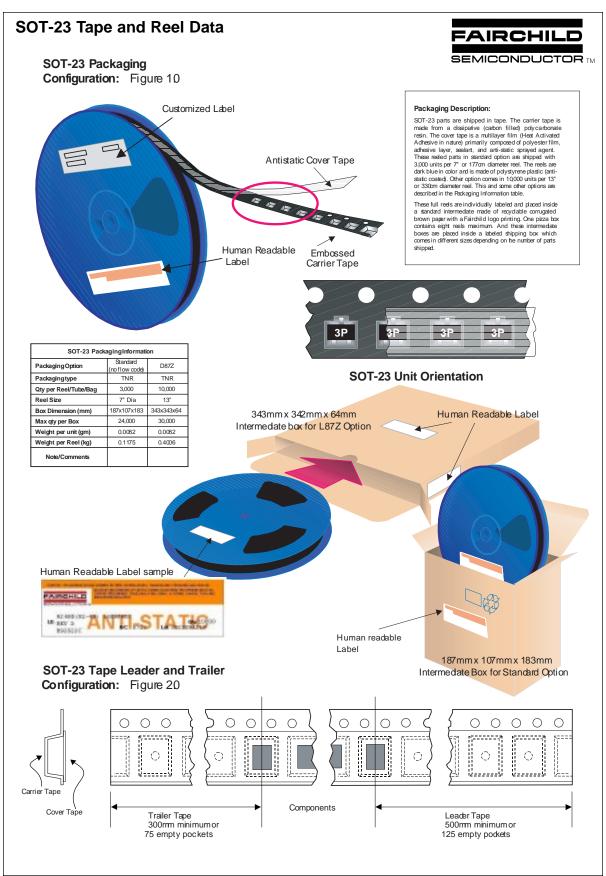
Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.1977



©2000 Fairchild Semiconductor International

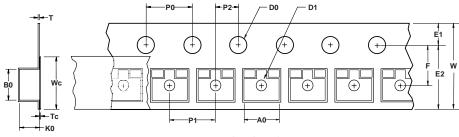
January 2000, Rev. B



SOT-23 Tape and Reel Data, continued

SOT-23 Embossed Carrier Tape

Configuration: Figure 3.0



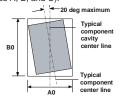
User Direction of Feed	

	Dimensions are in millimeter													
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	Т	Wc	Тс
SOT-23 (8mm)	3.15 +/-0.10	2.77 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.30 +/-0.10	0.228 +/-0.013	5.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



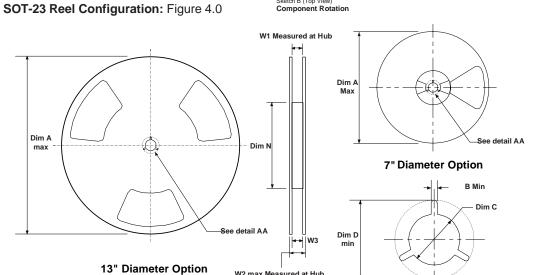
Sketch B (Top View)
Component Rotation



Sketch C (Top View)

Component lateral movement

DETAIL AA



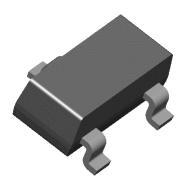
	Dimensions are in inches and millimeters								
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9

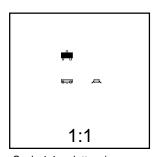
W2 max Measured at Hub

SOT-23 Package Dimensions



SOT-23 (FS PKG Code 49)

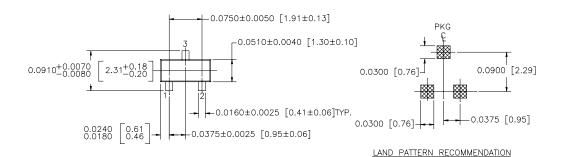


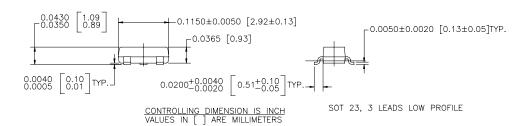


Scale 1:1 on letter size paper

Dimensions shown below are in: inches [millimeters]

Part Weight per unit (gram): 0.0082





NOTE: UNLESS OTHERWISE SPECIFIED

- 1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42
- 2. REFERENCE JEDEC REGISTRATION TO -236, VARIATION AB, ISSUE G, DATED JUL 1993

©2000 Fairchild Semiconductor International

September 1998, Rev. A1

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

 $ACEx^{TM}$ FASTr™ PowerTrench® SyncFET™ Bottomless™ QFET™ TinyLogic™ GlobalOptoisolator™ QSTM UHC™ CoolFET™ GTO™ **VCX**TM $CROSSVOLT^{TM}$ QT Optoelectronics™ HiSeC™ DOME™

Quiet Series™ ISOPLANAR™ E²CMOSTM SILENT SWITCHER® MICROWIRE™ EnSigna™ OPTOLOGIC™ SMART START™ FACT™ OPTOPLANAR™ SuperSOT™-3 FACT Quiet Series™ PACMAN™ SuperSOT™-6 **POPTM** SuperSOT™-8 FAST®

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. G