MBRS3100T3G, NRVBS3100T3G

Schottky Power Rectifier, Surface Mount, 3.0 A, 100 V, SMC Package

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes, in surface mount applications where compact size and weight are critical to the system.

Features

- Small Compact Surface Mountable Package with J-Bend Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Excellent Ability to Withstand Reverse Avalanche Energy Transients
- Guard-Ring for Stress Protection
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable*
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 217 mg (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Polarity Band on Plastic Body Indicates Cathode Lead
- ESD Ratings:
 - Machine Model = C
 - Human Body Model = 3B



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SCHOTTKY BARRIER RECTIFIERS 3.0 AMPERES, 100 VOLTS



SMC 2-LEAD CASE 403AC

MARKING DIAGRAM



| B310 | = Specific Device Code |
|------|------------------------|
| А | = Assembly Location** |
| Y | = Year |
| WW | = Work Week |

= Pb-Free Package

(Note: Microdot may be in either location)

** The Assembly Location code (A) is front side optional. In cases where the Assembly Location is stamped in the package, the front side assembly code may be blank.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|---------------|-------------------------|------------------------|
| MBRS3100T3G | SMC 2–Lead (Pb–Free) | 2,500 / Tape & Reel |
| NRVBS3100T3G* | SMC 2-Lead (Pb-Free) | 2,500 / Tape & Reel |

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|--|-------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 100 | V |
| Average Rectified Forward Current (At Rated V_R , $T_L = 100^{\circ}C$) | I _{F(AV)} | 3.0 | A |
| Non-repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz) | I _{FSM} | 130 | A |
| Operating Junction Temperature Range (Note 1) | TJ | –65 to +175 | °C |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
|--------------------------------------|---------------------|-------|------|
| Thermal Resistance, Junction-to-Lead | $R_{	ext{	heta}JL}$ | 11 | °C/W |

ELECTRICAL CHARACTERISTICS

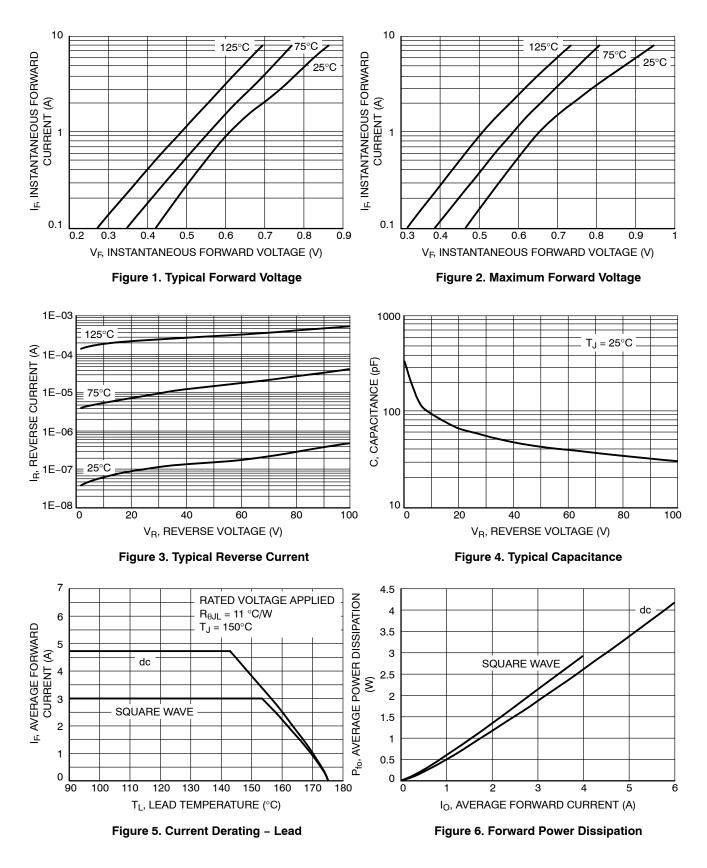
| Characteristic | Symbol | Value | Unit |
|---|----------------|------------------------------|------|
| | V _F | 0.79 0.90 0.62 0.70 | V |
| Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 125^{\circ}C$) | i _R | 0.05 5.0 | mA |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

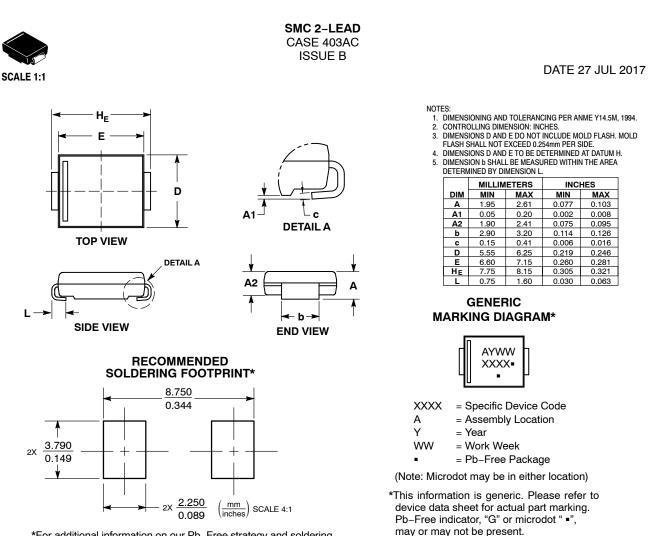
2. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

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TYPICAL CHARACTERISTICS







*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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|---|-------------|---|-------------|--|
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