

SANYO Semiconductors

DATA SHEET

N-Channel Silicon MOSFET FW813 — General-Purpose Switching Device **Applications**

Features

- ON-resistance $R_{DS}(on)1=39m\Omega$ (typ.)
- 4V drive
- Nch + Nch MOSFET

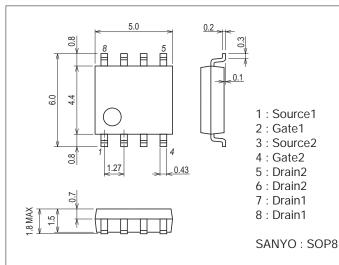
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		5	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	52	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (2000mm ² x0.8mm) 1unit, PW≤10s	2.3	W
Total Dissipation	PT	When mounted on ceramic substrate (2000mm ² ×0.8mm), PW≤10s	2.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7005A-003



Product & Package Information

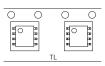
- Package
- JEITA, JEDEC
 - : SC-87, SOT96

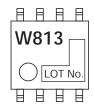
Marking

: SOP8

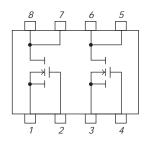
• Minimum Packing Quantity : 1,000 pcs./reel

Packing Type : TL





Electrical Connection

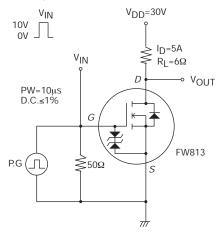


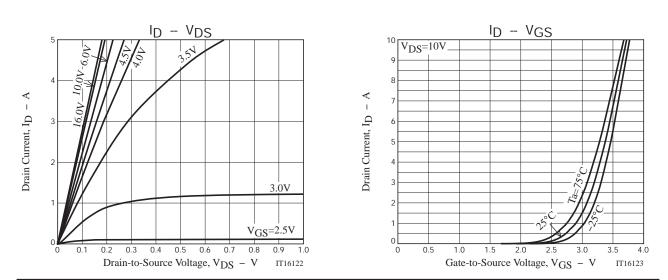
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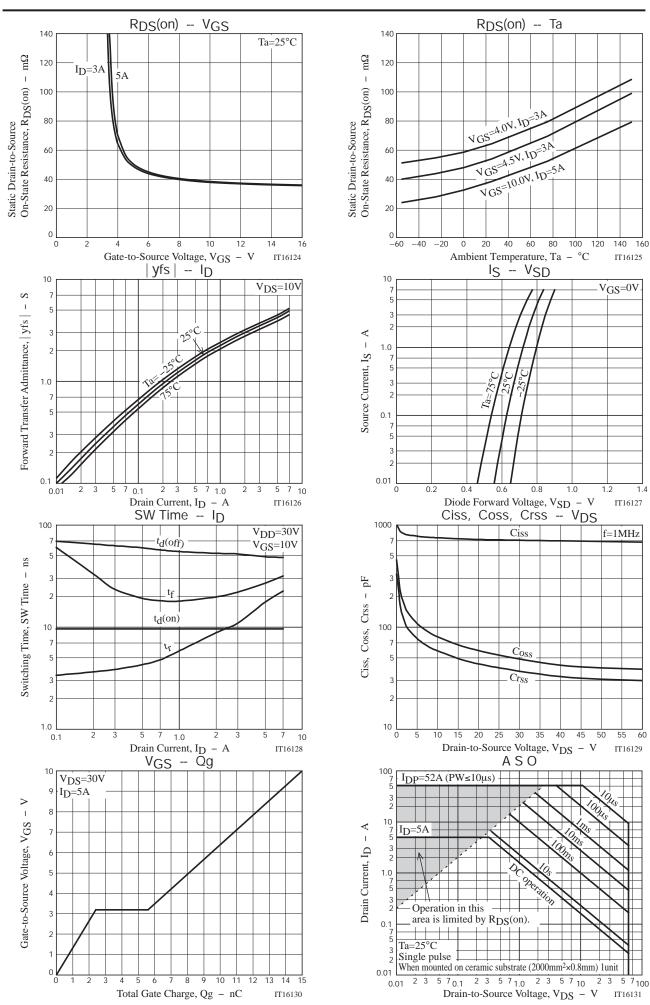
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			1.114
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0V			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =5A		4.2		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	ID=5A, VGS=10V		39	49	mΩ
	R _{DS} (on)2	ID=3A, VGS=4.5V		54	76	mΩ
	R _{DS} (on)3	ID=3A, VGS=4V		64	90	mΩ
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		725		рF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		60		рF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		45		рF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		9.6		ns
Rise Time	tr	See specified Test Circuit.		18		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		49		ns
Fall Time	tf	See specified Test Circuit.		27		ns
Total Gate Charge	Qg	V _{DS} =30V, V _{GS} =10V, I _D =5A		15		nC
Gate-to-Source Charge	Qgs	V _{DS} =30V, V _{GS} =10V, I _D =5A		2.4		nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=30V, VGS=10V, ID=5A		3.2		nC
Diode Forward Voltage	VSD	IS=5A, VGS=0V		0.81	1.2	V

Switching Time Test Circuit

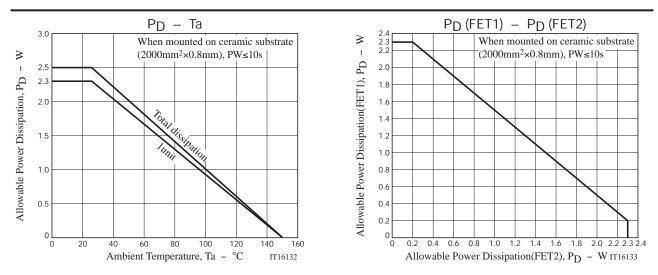






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Note on usage : Since the FW813 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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