Preferred Devices

Surface Mount Ultrafast Power Rectifiers

This series employs the state–of–the–art epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for high voltage, high frequency rectification, or as free wheeling and protection diodes, in surface mount applications where compact size and weight are critical to the system.

Features

- Small Compact Surface Mountable Package with J-Bend Leads
- Rectangular Package for Automated Handling
- High Temperature Glass Passivated Junction
- Low Forward Voltage Drop (0.71 to 1.05 Volts Max @ 3.0 A, T_J = 150°C)
- Pb–Free Packages are Available

Mechanical Characteristics

- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 217 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in 16 mm Tape and Reel, 2500 units per reel
- Polarity: Notch in Plastic Body Indicates Cathode Lead
- Device Meets MSL1 Requirements
- ESD Ratings: Machine Model, C (> 400 V) Human Body Model, 3B (> 8000 V)



ON Semiconductor®

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ULTRAFAST RECTIFIERS 3.0 AMPERES 200–600 VOLTS



SMC CASE 403 PLASTIC

MARKING DIAGRAM



U3 = Specific Device Code

- = G (340T3)
- = J (360T3)
- A = Assembly Location
- Y = Year WW= Work Week

ORDERING INFORMATION

Device	Package	Shipping [†]
MURS320T3	SMC	2500/Tape & Reel
MURS320T3G	SMC (Pb-Free)	2500/Tape & Reel
MURS340T3	SMC	2500/Tape & Reel
MURS340T3G	SMC (Pb-Free)	2500/Tape & Reel
MURS360T3	SMC	2500/Tape & Reel
MURS360T3G	SMC (Pb-Free)	2500/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS

Rating	Symbol	MURS320T3	MURS340T3	MURS360T3	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	400	600	V
Average Rectified Forward Current	I _{F(AV)}	3.0 @ T _L = 140°C 4.0 @ T _L = 130°C	3.0 @ T _L = 130°C 4.0 @ T _L = 115°C	3.0 @ T _L = 130°C 4.0 @ T _L = 115°C	A
Non–Repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I _{FSM}	75			A
Operating Junction Temperature	TJ	- 65 to +175			°C

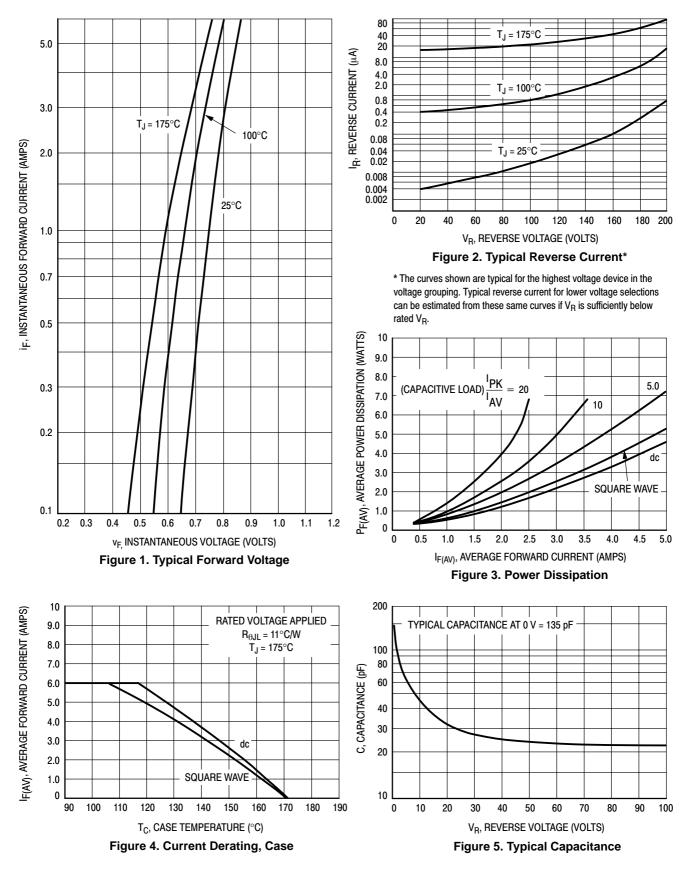
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

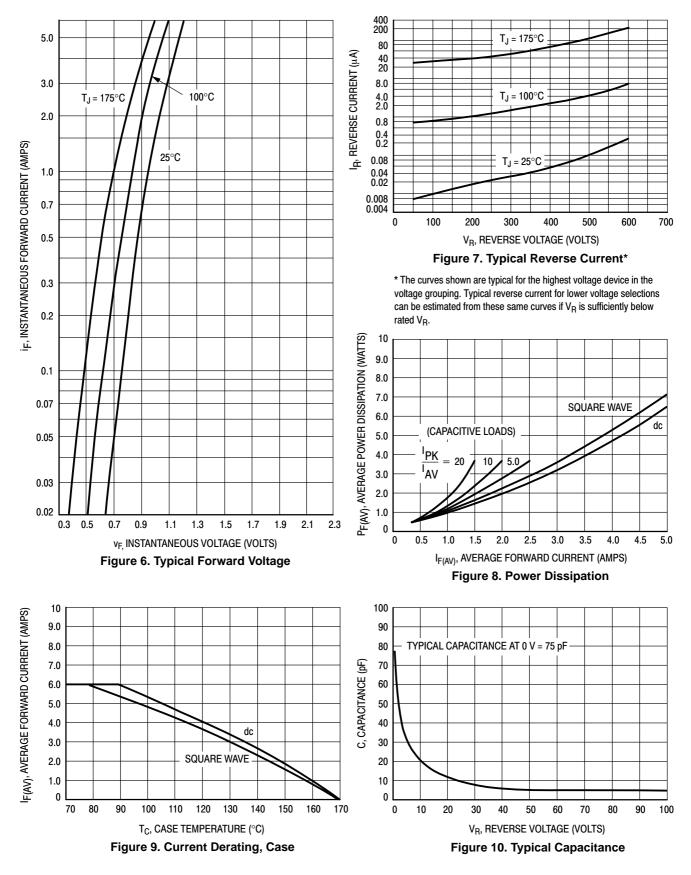
Thermal Resistance, Junction-to-Lead	R_{\thetaJL}	11			°C/W	
ELECTRICAL CHARACTERISTICS						
$\begin{array}{l} \mbox{Maximum Instantaneous Forward Voltage (Note 1)} \\ (i_F = 3.0 \mbox{ A}, \mbox{ T}_J = 25^{\circ}\mbox{C}) \\ (i_F = 4.0 \mbox{ A}, \mbox{ T}_J = 25^{\circ}\mbox{C}) \\ (i_F = 3.0 \mbox{ A}, \mbox{ T}_J = 150^{\circ}\mbox{C}) \end{array}$	VF	0.875 0.89 0.71	1.25 1.28 1.05	1.25 1.28 1.05	V	
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 150^{\circ}C$)	i _R	5.0 150	10 250	10 250	μΑ	
$\begin{array}{l} \text{Maximum Reverse Recovery Time} \\ (i_F = 1.0 \text{ A, di/dt} = 50 \text{ A/}\mu\text{s}) \\ (i_F = 0.5 \text{ A, }i_R = 1.0 \text{ A, }I_{REC} \text{ to } 0.25 \text{ A}) \end{array}$	t _{rr}	35 25	75 50	75 50	ns	
Maximum Forward Recovery Time (i _F = 1.0 A, di/dt = 100 A/µs, Recovery to 1.0 V)	t _{fr}	25	50	50	ns	

1. Pulse Test: Pulse Width = 300 $\mu s,$ Duty Cycle \leq 2.0%.

MURS320T3

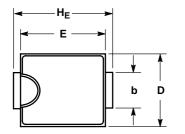


MURS340T3, MURS360T3



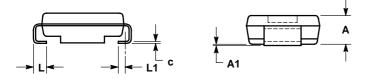
PACKAGE DIMENSIONS

SMC PLASTIC PACKAGE CASE 403-03 ISSUE E

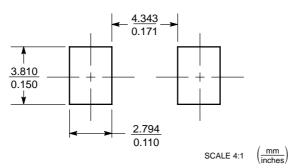


NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P. 4. 403-01 THRU -02 OBSOLETE, NEW STANDARD 403-03.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.90	2.13	2.41	0.075	0.084	0.095
A1	0.05	0.10	0.15	0.002	0.004	0.006
b	2.92	3.00	3.07	0.115	0.118	0.121
С	0.15	0.23	0.30	0.006	0.009	0.012
D	5.59	5.84	6.10	0.220	0.230	0.240
E	6.60	6.86	7.11	0.260	0.270	0.280
HE	7.75	7.94	8.13	0.305	0.313	0.320
L	0.76	1.02	1.27	0.030	0.040	0.050
L1		0.51 REF			0.020 REF	



SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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