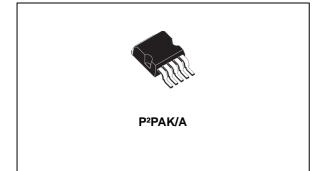


LD29300

3 A, very low drop voltage regulators

Datasheet - production data



Features	
i catules	

- Very low dropout voltage (typ. 0.4 at 3 A)
- Guaranteed output current up to 3 A
- Fixed voltage with ± 1% tolerance at 25 °C
- Internal current and thermal limit
- Logic controlled electronic shutdown available in P²PAK/A

Description

The LD29300 is a high current, high accuracy, low-dropout voltage regulator series. These regulators feature 400 mV dropout voltage and very low ground current. Designed for high current loads, these devices are also used in lower current, extremely low dropout-critical systems, where their tiny dropout voltage and ground current values are important attributes. Typical applications are in power supply switching post regulation, series power supply for monitors, series power supply for VCRs and TVs, computer systems and battery powered systems.

Table 1. Device summarv

Order codes	Output voltages
LD29300P2M33R	3.3 V
LD29300P2MTR	ADJ

This is information on a product in full production.

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2	Pin configuration
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1 Diagram

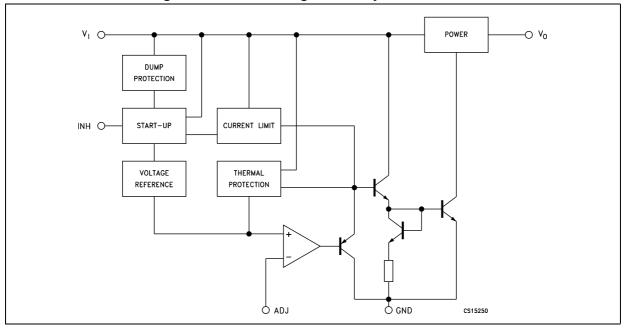
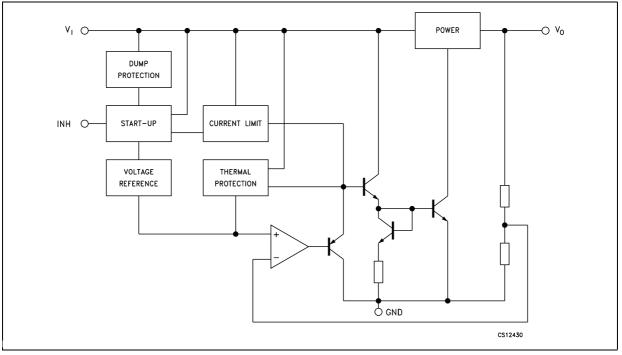


Figure 1. Schematic diagram for adjustable version

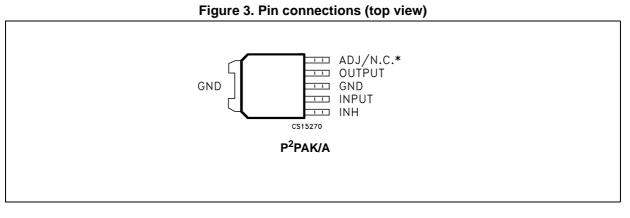






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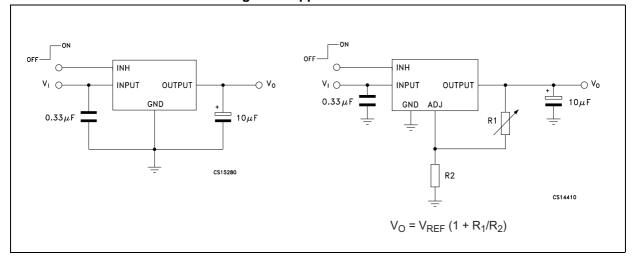
2 Pin configuration



* Not connected for fixed version.



3 Typical application







4 Maximum ratings

Symbol	Parameter	Value	Unit
VI	DC input voltage	30 ⁽¹⁾	V
۱ ₀	Output current	Internally limited	mA
PD	Power dissipation	Internally limited	mW
T _{STG}	Storage temperature range	- 55 to 150	°C
T _{OP}	Operating junction temperature range	- 40 to 125	°C

Table 2. Absolute maximum ratings

1. Above 14 V the device is automatically in shut-down.

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied.

Symbol	Symbol Parameter		Unit
R _{thJA}	Thermal resistance junction-ambient	60	°C/W
R _{thJC}	Thermal resistance junction-case	3	°C/W

Table 3. Thermal data



5 Electrical characteristics

 I_O = 10 mA, T_J = 25 °C, V_I = 5.3 V, V_{INH} = 2 V, C_I = 330 nF, C_O = 10 μF , unless otherwise specified.

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V	Output voltage	$I_0 = 10$ mA to 3A, $V_1 = 4.3$ to 8.8V	3.267	3.3	3.333	V
V _O	Output voltage	$T_{\rm J} = -40$ to 125°C	3.234		3.366	v
ΔV_{O}	Load regulation	$I_{O} = 10$ mA to 3A		0.2	1.0	%
ΔV_{O}	Line regulation	V _I = 4.3 to 13V		0.06	0.5	%
SVR	Supply voltage rejection	f = 120 Hz, V_I = 5.3 ±1V, I_O = 1.5A ⁽¹⁾	52	67		dB
		$I_{\rm O}$ = 500mA, $T_{\rm J}$ = -40 to 125°C ⁽²⁾		0.1		
V _{DROP}	Dropout voltage	I_{O} = 1.5A, T_{J} = -40 to 125°C ⁽²⁾		0.2		V
		$I_{\rm O} = 3$ A, $T_{\rm J} = -40$ to 125°C ⁽²⁾		0.4	0.7	
		$I_{O} = 1.5A, T_{J} = -40 \text{ to } 125^{\circ}C$		20	50	mA
۱ _q	Quiescent current	$I_{O} = 3A, T_{J} = -40$ to $125^{\circ}C$		45	100	ША
		$V_I = 13V$, $V_{INH} = GND$, $T_J = -40$ to $125^{\circ}C$		130	180	μA
I _{sc}	Short circuit current	V _I - V _O = 5.5V		4.5		А
V_{IL}	Control input logic low	OFF MODE ⁽¹⁾ , $T_J = -40$ to 125°C			0.8	V
V _{IH}	Control input logic high	ON MODE ⁽¹⁾ , $T_J = -40$ to 125°C 2				V
I _{INH}	Control input current	$T_{J} = -40$ to 125°C, $V_{INH} = 13V$		5	10	μA
eN	Output noise voltage	$B_{\rm P} = 10$ Hz to 100kHz, $I_{\rm O} = 100$ mA ⁽¹⁾		132		μV_{RMS}

1. Guaranteed by design.

2. Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99% of its nominal value with V_0 + 1 V applied to V_1 .



 I_O = 10 mA, T_J = 25 °C, V_I = 3.23 V, V_{INH} = 2 V, C_I = 330 nF, C_O = 10 μF adjust pin tied to output pin.

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
VI	Minimum operating input voltage	I_{O} = 10mA to 3A, T_{J} = -40 to 125°C	2.5			V
ΔV_O	Load regulation	I _O = 10mA to 3A		0.2	1.0	%
ΔV_{O}	Line regulation	V _I = 2.5 V to 13V		0.06	0.5	%
V	Reference voltage	$I_{O} = 10$ mA to 3A, $V_{I} = 2.5$ to 4.5V	-1%	1.23	+1%	V
V _{REF}	Reference voltage	$T_{\rm J} = -40$ to 125°C ⁽¹⁾	-2%		+2%	v
SVR	Supply voltage rejection	f = 120 Hz, V_1 = 3.23 ±1V, I_0 = 1.5A ⁽²⁾	65	75		dB
		$I_{O} = 1.5A, T_{J} = -40 \text{ to } 125^{\circ}\text{C}$		20	50	mA
۱ _q	Quiescent current	$I_{O} = 3A, T_{J} = -40 \text{ to } 125^{\circ}\text{C}$		45	100	ma
		$V_I = 13V$, $V_{INH} = GND$, $T_J = -40$ to $125^{\circ}C$		130	180	μA
I _{ADJ}	Adjust pin current	T _J = -40 to 125°C			1	μA
I _{sc}	Short circuit current	V _I - V _O = 5.5V		4.5		А
V _{IL}	Control input logic low	OFF MODE $^{(1)}$,T _J = -40 to 125°C			0.8	V
V _{IH}	Control input logic high	ON MODE ⁽¹⁾ , $T_J = -40$ to $125^{\circ}C$	2			V
I _{INH}	Control input current	$T_{J} = -40$ to 125°C, $V_{INH} = 13V$		5	10	μA
eN	Output noise voltage	$B_{P} = 10$ Hz to 100kHz, $I_{O} = 100$ mA ⁽²⁾		50		μV_{RMS}

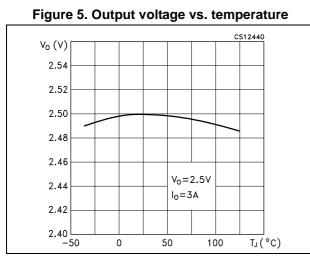
Table 5. Electrica	al characteristics of	f LD29300#ADJ
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1. Reference voltage is measured between output and GND pin, with ADJ PIN tied to $V_{\mbox{OUT}}$

2. Guaranteed by design.



6 Typical characteristics



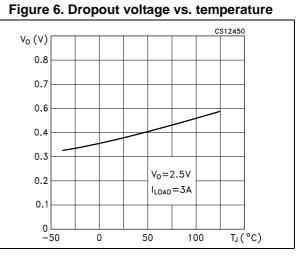
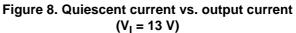


Figure 7. Dropout voltage vs. output current



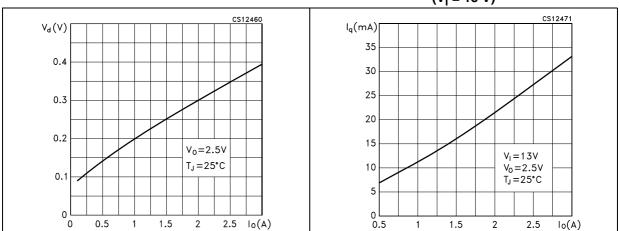




Figure 9. Quiescent current vs. output current Figure 10. Quiescent current vs. supply voltage $(V_1 = 4.5 \text{ V})$

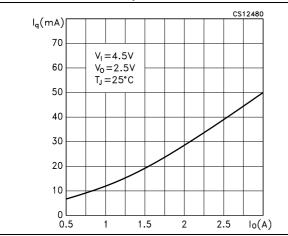


Figure 11. Quiescent current vs. temperature (I_O = 100 mA)

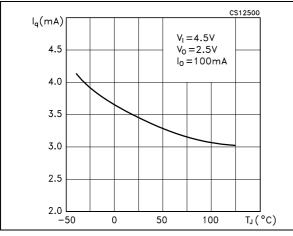
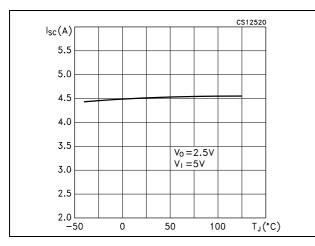


Figure 13. Short circuit current vs. temperature



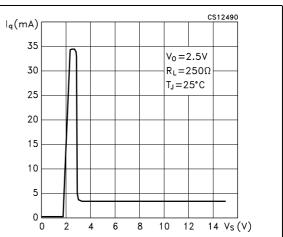


Figure 12. Quiescent current vs. temperature $(I_0 = 3 A)$

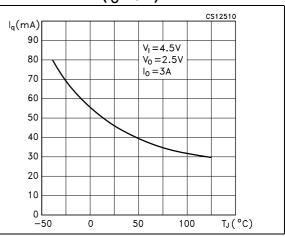
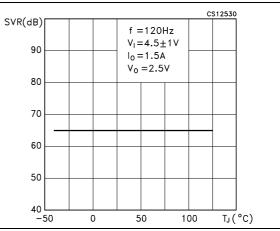


Figure 14. Supply voltage rejection vs. temperature



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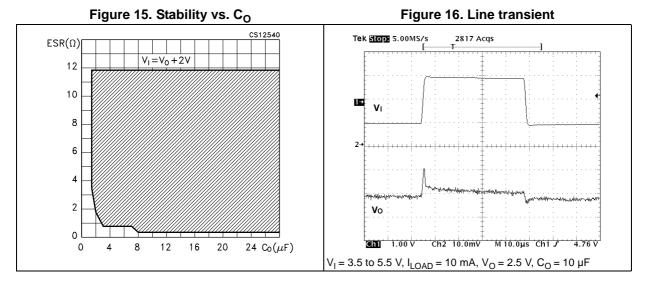
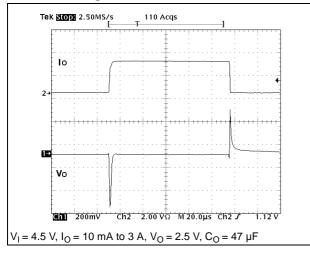


Figure 17. Load transient



7 Package mechanical data

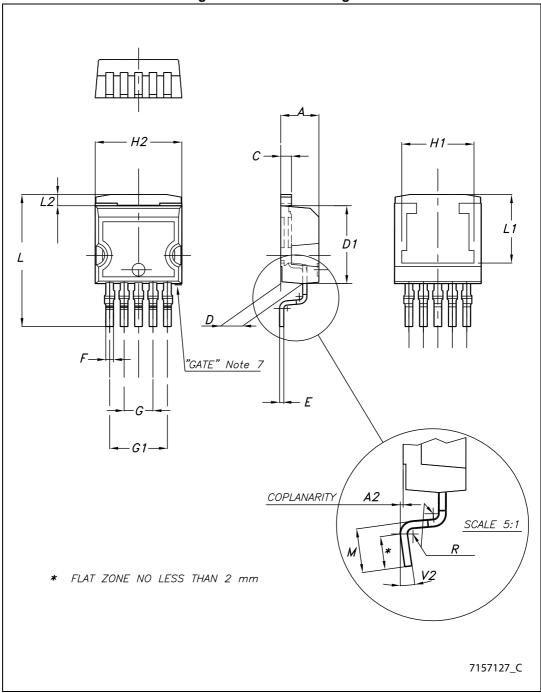
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

Dim.		mm		
Dini.	Min.	Тур.	Max.	
А	4.30		4.80	
A2	0.03		0.23	
С	1.17		1.37	
D	2.40		2.80	
D1	8.95		9.35	
E	0.45		0.60	
F	0.80		1.05	
G	3.20		3.60	
G1	6.60		7.00	
H1		8.5		
H2	10.00		10.40	
L	15		15.85	
L1		8		
L2	1.27		1.40	
М	2.4		3.2	
R		0.40		
V2	0°		8°	

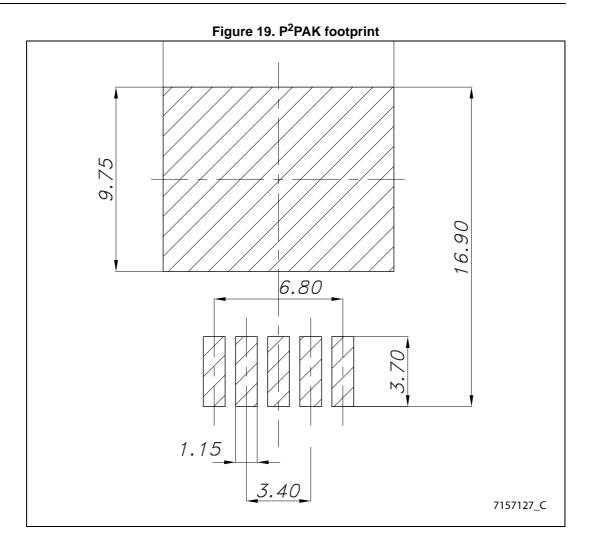
	-2		
Table 6	. P ² PAK	mechanical	data



Figure 18. P²PAK drawings









8 Packaging mechanical data

D .		mm		
Dim.	Min.	Тур.	Max.	
А			180	
С	12.8	13	13.2	
D	20.2			
Ν	60			
Т			14.4	
Ao	10.50	10.6	10.70	
Во	15.70	15.80	15.90	
Ko	4.80	4.90	5.00	
Po	3.9	4.0	4.1	
Р	11.9	12.0	12.1	

Table 7. P ² PAK ta	pe and reel	mechanical data
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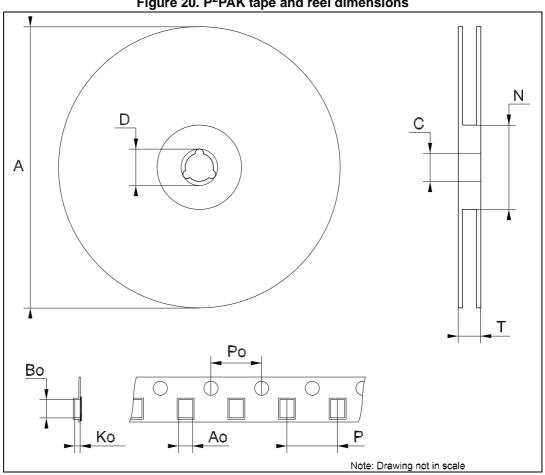


Figure 20. P²PAK tape and reel dimensions



9 Revision history

Date	Revision	Changes
21-Oct-2005	7	Order codes updated.
10-Apr-2007	8	Order codes updated.
11-May-2007	9	Order codes updated.
08-Jun-2007	10	Order codes updated.
03-Apr-2008	11	Modified: Table 1 on page 1.
11-Jul-2008	12	Modified: Table 1 on page 1.
13-Sep-2012	13	Updated: Table 1 on page 1.
18-Nov-2013	14	Part numbers LD29300XX, LD29300XX18 and LD29300XX33 have been changed to LD29300. Updated the Description in cover page and <i>Table 1: Device</i> <i>summary</i> . Updated <i>Table 3: Thermal data</i> , Section 5: Electrical characteristics and Section 7: Package mechanical data. Added Section 8: Packaging mechanical data. Minor text changes.

Table 8.	Document	revision	history
	Document	1013011	matory



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