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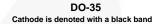
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February 2016

# FDH300 / FDH300A / FDLL300A / FDH333 / FDLL333 High Conductance Low Leakage Diode







LL-34 (SOD-80)

THE PLACEMENT OF THE EXPANSION GAP HAS NO RELATIONSHIP TO THE LOCATION OF THE CATHODE TERMINAL

#### LL-34 COLOR BAND MARKING

DEVICE 1ST BAND

FDLL300A WHITE FDLL333 WHITE

 -1st band denotes cathode terminal and has wider width

#### **Ordering Information**

Part Number	Top Mark	Package	Packing Method	
FDH300TR	H300	DO-204AH (DO-35)	Tape and Reel	
FDH300A	H300A	DO-204AH (DO-35)	Bulk	
FDH300ATR	H300A	DO-204AH (DO-35)	Tape and Reel	
FDH333	H333	DO-204AH (DO-35)	Bulk	
FDH333TR	H333	DO-204AH (DO-35)	Tape and Reel	
FDLL300A	WHITE	SOD-80 2L	Tape and Reel	
FDLL333	WHITE	SOD-80 2L	Tape and Reel	

#### **Absolute Maximum Ratings**(1), (2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter		Value	Unit
W <sub>IV</sub>	Working Inverse Voltage		125	V
Io	Average Rectified Forward Current		200	mA
I <sub>F</sub>	DC Forward Current		500	mA
i <sub>f</sub>	Recurrent Peak Forward Current		600	mA
I <sub>FSM</sub> Non-Repetitive Peak For Surge Current	Non-Repetitive Peak Forward	Pulse Width = 1.0 s	1.0	A
	Surge Current	Pulse Width = 1.0 μs	4.0	
T <sub>STG</sub>	Storage Temperature Range		-65 to +200	°C
TJ	Operating Junction Temperature		175	°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 175°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

© 1997 Fairchild Semiconductor Corporation FDH300 / FDH300A / FDLL300A / FDH333 / FDLL333 Rev. 3.4

#### **Thermal Characteristics**

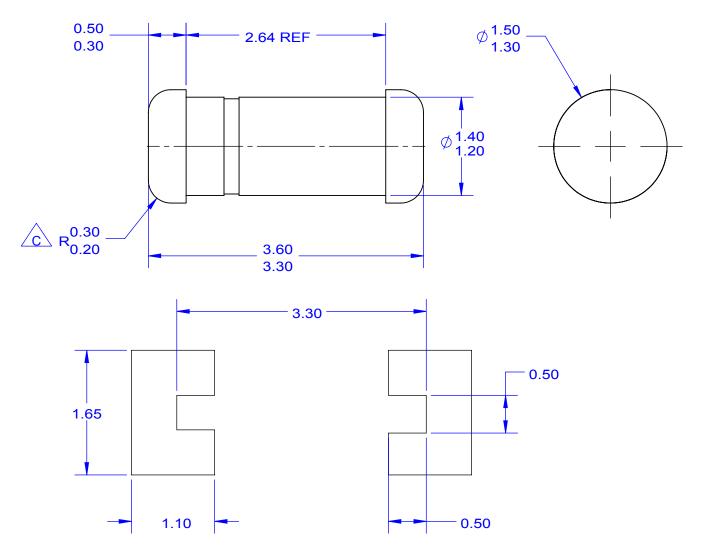
Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
P <sub>D</sub>	Total Device Dissipation	500	mW
	Derate Above 25°C	3.33	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	300	°C/W

#### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol		Parameter	Conditions	Min.	Max.	Unit
$V_{R}$	Breakdown Voltag	e	I <sub>R</sub> = 100 μA	150		V
		FDH300 / FDH300A / FDLL300A	I <sub>F</sub> = 1.0 mA		680	mV
		FDH300	I <sub>F</sub> = 5.0 mA		750	mV
	Forward Voltage	FDH300A / FDLL300A	I <sub>F</sub> = 5.0 mA		760	mV
		FDH300 / FDH300A / FDLL300A	I <sub>F</sub> = 10 mA		800	mV
		FDH300	I <sub>F</sub> = 50 mA		880	mV
		FDH300A / FDLL300A	I <sub>F</sub> = 50 mA		890	mV
$V_{F}$		FDH300 / FDH300A / FDLL300A	I <sub>F</sub> = 100 mA		920	mV
٧F		FDH300 / FDH300A / FDLL300A	I <sub>F</sub> = 200 mA		1.0	V
		FDH333 / FDLL333	I <sub>F</sub> = 50 mA	800	890	mV
			I <sub>F</sub> = 100 mA	830	940	mV
			I <sub>F</sub> = 150 mA	860	970	mV
			I <sub>F</sub> = 200 mA	0.87	1.05	V
			I <sub>F</sub> = 250 mA	0.88	1.08	V
			I <sub>F</sub> = 300 mA	0.90	1.15	٧
	Reverse Current	FDH300 / FDH300A / FDLL300A	V <sub>R</sub> = 125 V		1.0	nA
1-		T DI ISOO / T DI ISOOA / T DEESOOA	$V_R = 125 \text{ V}, T_A = 150^{\circ}\text{C}$		3.0	μΑ
I <sub>R</sub>		FDH333 / FDLL333	V <sub>R</sub> = 125 V		3.0	nA
			V <sub>R</sub> = 125 V, T <sub>A</sub> = 100°C		500	nA
Co	Diode Capacitano	e	$V_R = 0$ , $f = 1.0 \text{ MHz}$		6.0	pF

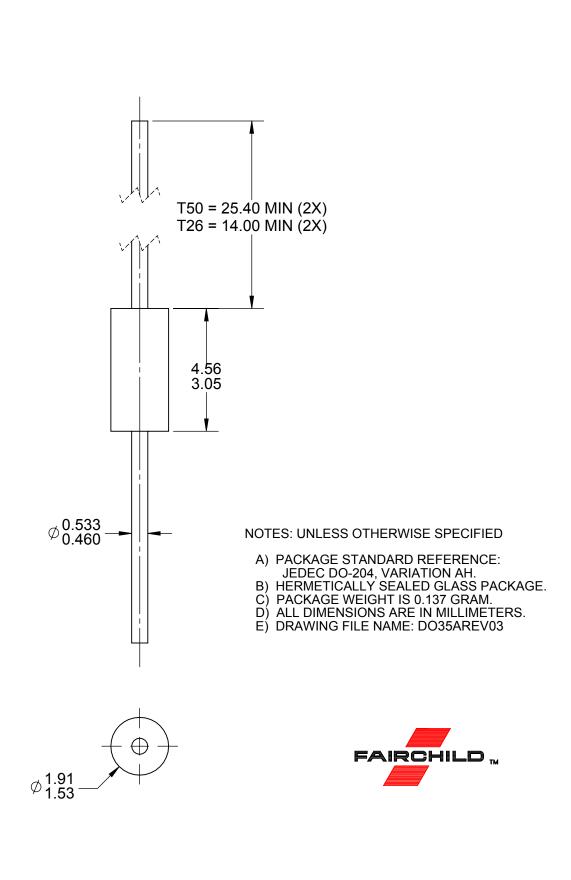


LAND PATTERN RECOMMENDATION

NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE: JEDEC DO-213, VARIATION AC.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C CORNER RADIUS IS OPTIONAL.
- D) LAND PATTERN RECOMMENDATION PER IPC DIOMELF3414N
- E) DRAWING FILE NAME: SOD80A REV3





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