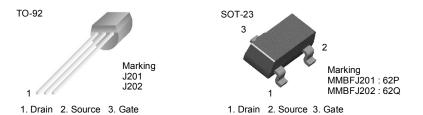


January 2008

# J201 - J202 / MMBFJ201 - MMBFJ203 **N-Channel General Purpose Amplifier**

- · This device is designed primarily for low level audio and general purpose applications with high impedance signal sources.
- Sourced from Process 52.



## Absolute Maximum Ratings \* Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	-40	V
I <sub>GF</sub>	Forward Gate Current	50	mA
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. NOTES:

### Thermal Characteristics\* T.=25°C unless otherwise noted

Symbol	Parameter	Va	Units	
		J201 - J202	MMBFJ201 - MMBFJ203	Offics
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	W mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

<sup>\*</sup> Device mounted on FR-4 PCB 1.6" × 1.6" × 0.06"

© 2007 Fairchild Semiconductor Corporation J201 - J202 / MMBFJ201 - MMBFJ203 Rev. 1.0.0 www.fairchildsemi.com

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150°C.

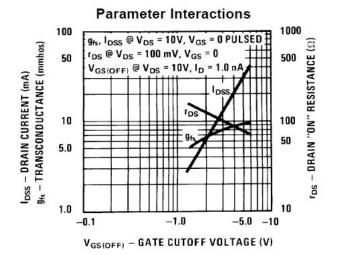
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

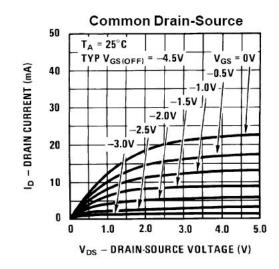
# Electrical Characteristics \* $T_C = 25$ °C unless otherwise noted

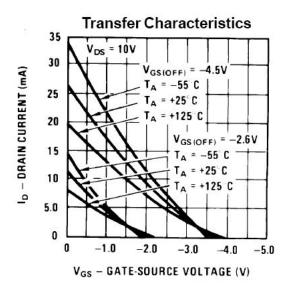
Symbol	Parameter	Conditions		Min.	Max	Units
Off Charact	Off Characteristics					
$V_{(BR)GSS}$	Gate-Source Breakdwon Voltage	$I_{G} = -1\mu A, V_{DS} = 0$		-40		V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = -20V, V <sub>DS</sub> = 0			-100	pA
V <sub>GS</sub> (off)	Gate-Source Cutoff Voltage	V <sub>DS</sub> = 20V, I <sub>D</sub> = 10nA	201 202 203	-0.3 -0.8 -2	-1.5 -4 -10	V
On Charact	On Characteristics					
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current *	V <sub>DS</sub> = 20V, I <sub>GS</sub> = 0	201 202 203	0.2 0.9 4	1.0 4.5 20	mA
Small Signal Characteristics						
y <sub>FS</sub>	Forward Transfer Admittance	V <sub>DS</sub> = 20V, f = 1.0kHz	201 202 203	500 1000 1500		μmhos

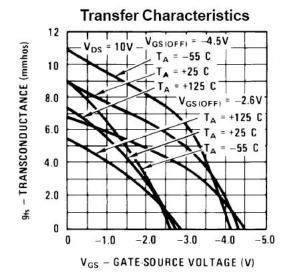
<sup>\*</sup> Pulse Test: Pulse Width  $\leq$  300ms, Duty Cycle  $\leq$  2.0%

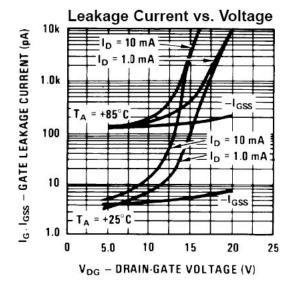
## **Typical Characteristics**

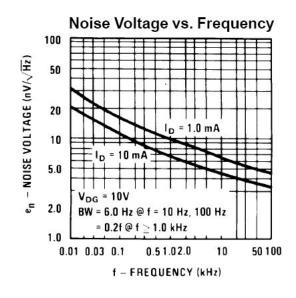




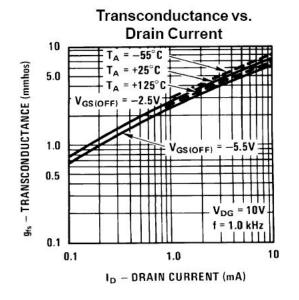


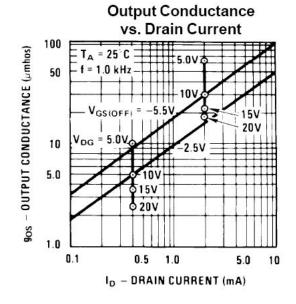


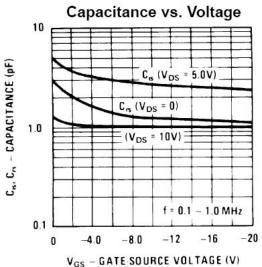


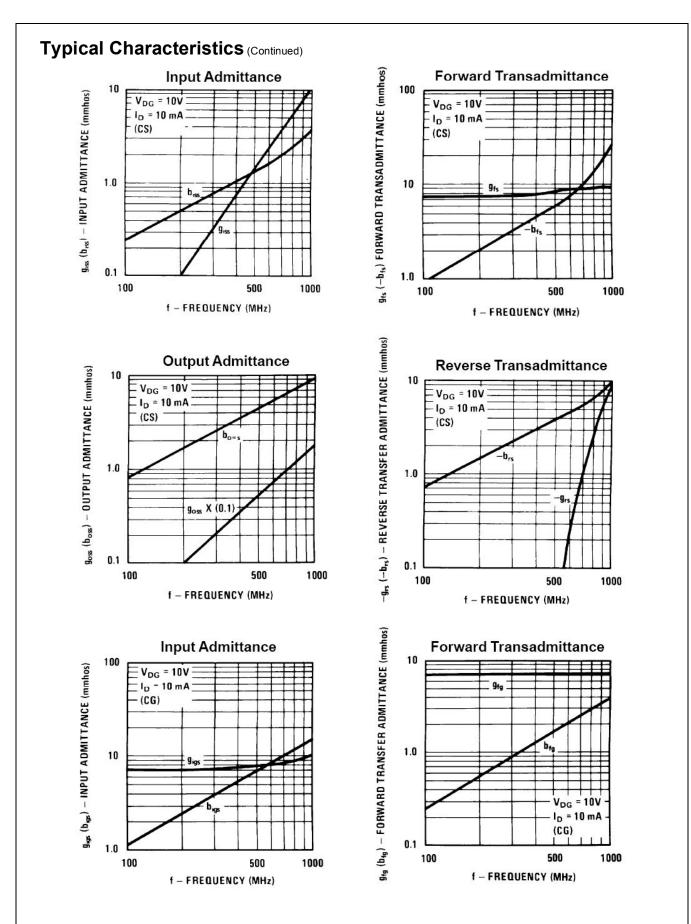


# Typical Characteristics (Continued)



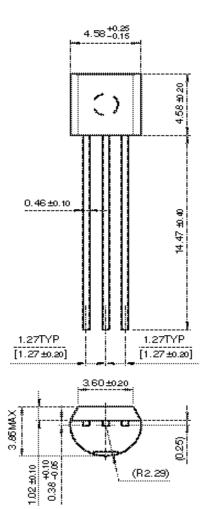


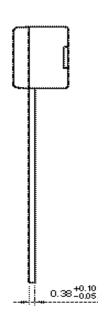




## **Mechanical Dimensions**

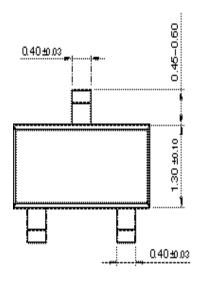
TO-92

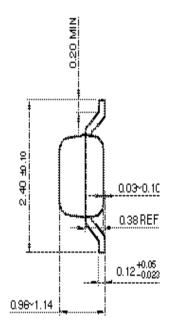


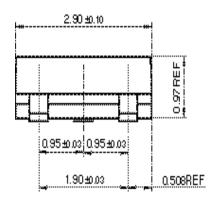


## **Mechanical Dimensions**

# SOT-23







Dimensions in Millimeters





### **TRADEMARKS**

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

Power247® **ACEx®** Green FPS™ SuperSOT™-8 POWEREDGE® SyncFET™ Build it Now™ Green FPS™ e-Series™ CorePLUS™ GTO™ Power-SPM™ The Power Franchise® CROSSVOLT™ i-Lo™ PowerTrench® p wer CTL™ IntelliMAX™ Programmable Active Droop™ QFET® Current Transfer Logic™ **ISOPLANAR™** TinyBoost™ QS™ EcoSPARK® TinyBuck™ MegaBuck™ TinyLogic<sup>®</sup> MICROCOUPLER™ QT Optoelectronics™ Fairchild® MicroFET™ Quiet Series™ TINYOPTO™ RapidConfigure™ Fairchild Semiconductor® MicroPak™ TinyPower™ TinyPWM™ FACT Quiet Series™ MillerDrive™ SMART START™ FACT<sup>®</sup> Motion-SPM™ SPM<sup>®</sup> TinyWire™  $\mathsf{FAST}^{\mathbb{R}}$ OPTOLOGIC® STEALTH™ µSerDes™ FastvCore™ OPTOPLANAR® UHC® SuperFET™ FPS™ UniFET™ SuperSOT™-3 FRFET® PDP-SPM™ SuperSOT™-6 VCX™ Power220® Global Power Resource<sup>SM</sup>

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

### As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### PRODUCT STATUS DEFINITIONS

### **Definition of Terms**

Datasheet Identification	Product Status	Definition		
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.		

Rev. I31

www.fairchildsemi.com