Switch-mode Schottky Power Rectifier 250 V, 40 A

MBR40250G, MBR40250TG, MBRF40250TG, MBRB40250TG

Features

- 250 V Blocking Voltage
- Low Forward Voltage Drop, $V_F = 0.86 V$
- Soft Recovery Characteristic, $T_{RR} < 35$ ns
- Stable Switching Performance Over Temperature
- These Devices are Pb-Free and are RoHS Compliant

Benefits

- Reduces or Eliminates Reverse Recovery Oscillations
- Minimizes Need for EMI Filtering
- Reduces Switching Losses
- Improved Efficiency

Applications

- Power Supply
- Power Management
- Automotive
- Instrumentation

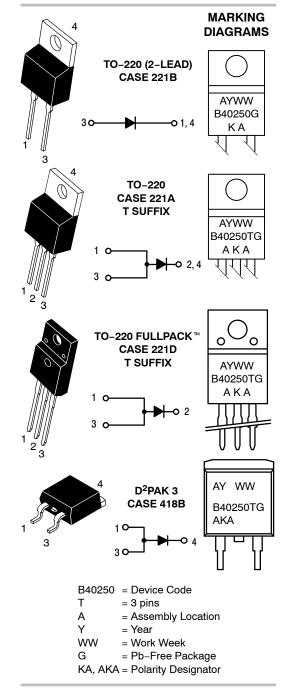
Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 1.9 grams (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Epoxy Meets UL 94 V-0 at 0.125 in



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ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

MBR40250G, MBR40250TG, MBRF40250TG, MBRB40250TG

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	250	V
Average Rectified Forward Current (Rated V_R) T _C = 82°C MBR40250, MBR40250T, MBRB40250T (Rated V_R) T _C = 46°C MBRF40250T	I _{F(AV)}	40	A
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz) T _C = 82°C MBR40250, MBR40250T, MBRB40250T (Rated V _R , Square Wave, 20 kHz) T _C = 46°C MBRF40250T	I _{FRM}	80	A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I _{FSM}	150	А
Storage Temperature	T _{stg}	- 65 to +175	°C
Operating Junction Temperature	TJ	- 65 to +150	°C
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/μs

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance			°C/W
Junction-to-Case	$R_{\theta JC}$		
MBR40250(T) and MBRB40250T		2.0	
MBRF40250		3.0	
Junction-to-Ambient	$R_{\theta JA}$		
MBR40250(T)		60	
MBRF40250		50	
MBRB40250T		50	

ELECTRICAL CHARACTERISTICS

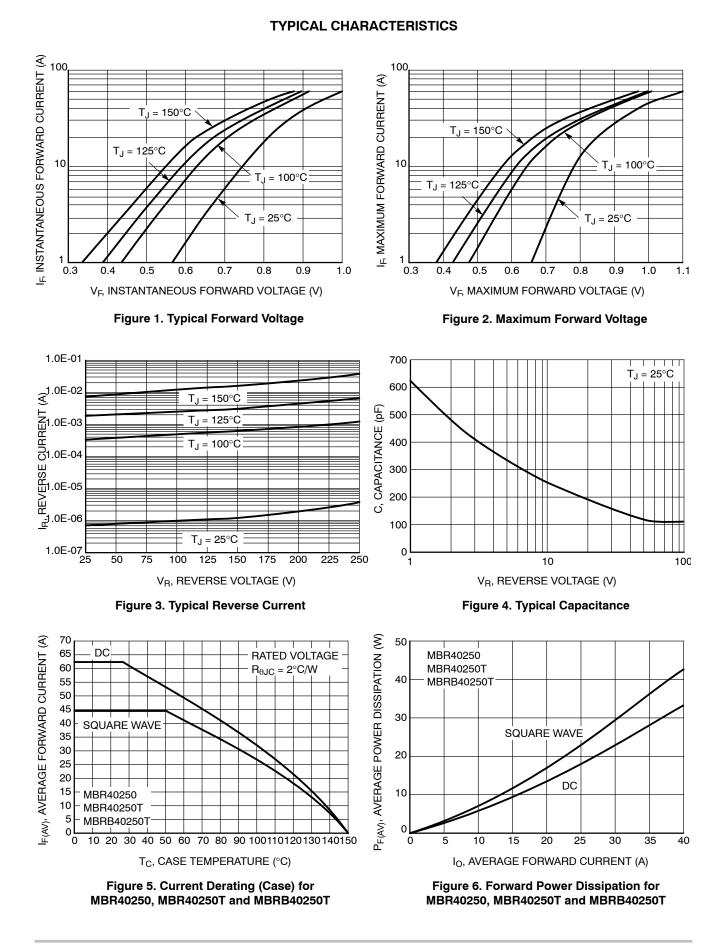
		1
V _F	0.86 0.71 0.97 0.86	V
I _R	0.25 30	mA
t _{rr}	35	ns
-	IR	0.86 0.71 0.97 0.86 I _R 0.25 30 t _{rr}

Capacitance	CT		pF
\dot{V}_{R} = -5.0 V, T _C = 25°C, Frequency = 1.0 MHz		500	

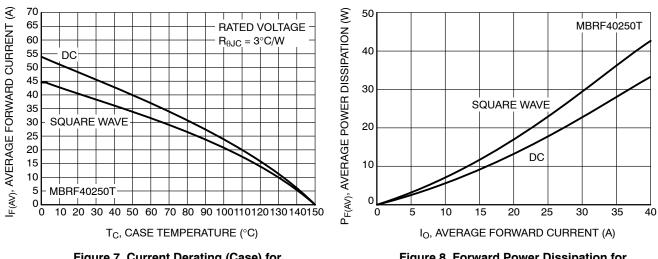
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

MBR40250G, MBR40250TG, MBRF40250TG, MBRB40250TG



MBR40250G, MBR40250TG, MBRF40250TG, MBRB40250TG



TYPICAL CHARACTERISTICS

Figure 7. Current Derating (Case) for MBRF40250T



ORDERING INFORMATION

Device	Package	Shipping [†]
MBR40250G	TO-220 (2-LEAD) (Pb-Free)	50 Units / Rail
MBR40250TG	TO-220 (Pb-Free)	50 Units / Rail
MBRF40250TG	TO-220 FULLPACK (Pb-Free)	50 Units / Rail
MBRB40250TG	D ² PAK 3 (Pb–Free)	50 Units / Rail
MBRB40250TT4G	D ² PAK 3 (Pb–Free)	800 Units / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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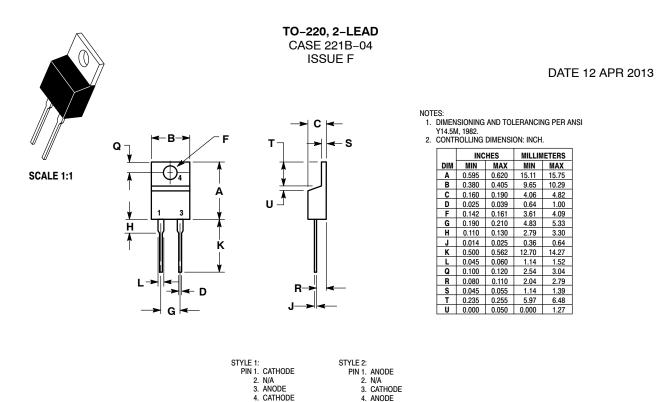
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				4. MAX V	VIDTH FOR	F102 DEVICE	= 1.3510101		
	I	Γ I			INC	HES	MILLIM	ETERS	
				DIM	MIN.	MAX.	MIN.	MAX.	
	2 3			А	0.570	0.620	14.48	15.75	
<u> </u>	┟┰┟┟╌┙──┼			В	0.380	0.415	9.66	10.53	
⊢ + +++++++	₩+₩++			с	0.160	0.190	4.07	4.83	
	í lí	f I		D	0.025	0.038	0.64	0.96	
' z –	I I K			F	0.142	0.161	3.60	4.09	
li li	î î			G	0.095	0.105	2.42	2.66	
				н	0.110	0.161	2.80	4.10	
	¥ ₩	ü l		J	0.014	0.024	0.36	0.61	
V —	R —			K	0.500	0.562	12.70 1.15	14.27	
G	J-	╼║╼		N	0.190	0.060	4.83	1.52 5.33	
Ŭ,	' → → D			Q	0.190	0.210	2.54	3.04	
_	N -			R	0.100	0.120	2.54	2.79	
				s	0.045	0.055	1.15	1.41	
				т	0.235	0.255	5.97	6.47	
				U I	0.000	0.050	0.00	1.27	
				v	0.045		1.15		
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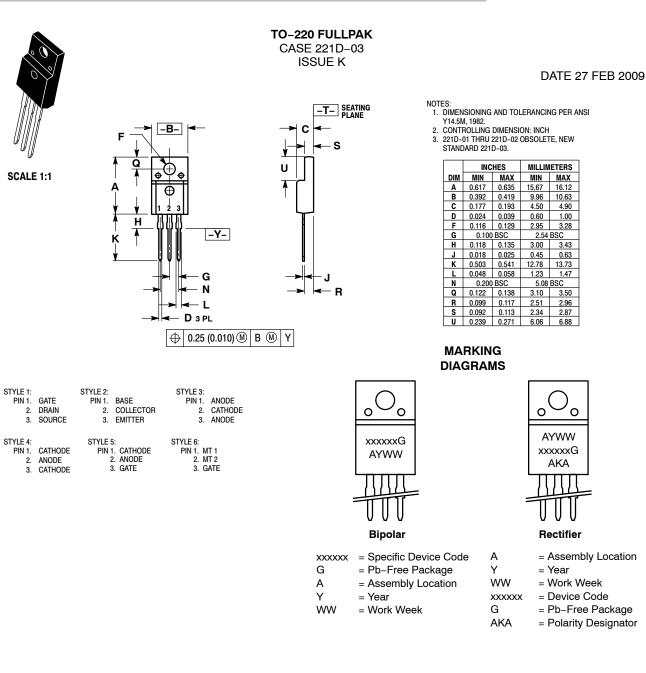
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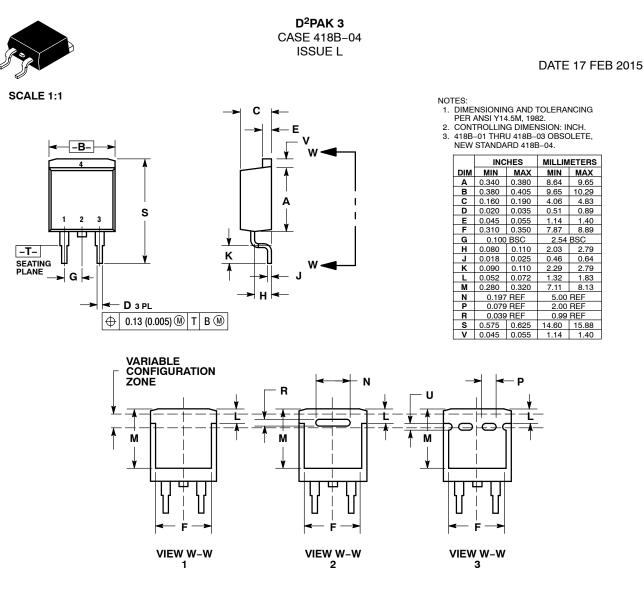
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STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:	STYLE 6:
PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. GATE	PIN 1. CATHODE	PIN 1. NO CONNECT
2. COLLECTOR	2. DRAIN	2. CATHODE	2. COLLECTOR	2. ANODE	2. CATHODE
3. EMITTER	SOURCE	ANODE	3. EMITTER	3. CATHODE	3. ANODE
4. COLLECTOR	4. DRAIN	CATHODE	4. COLLECTOR	4. ANODE	4. CATHODE

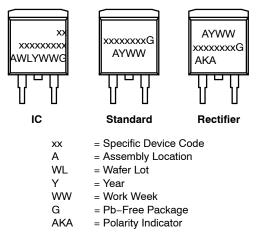
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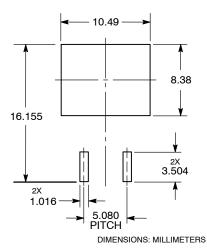
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GENERIC MARKING DIAGRAM*



*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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