

MCH6342

Power MOSFET –30V, 73mΩ, –4.5A, Single P-Channel

This Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

Features

- Low On-Resistance
- 1.8V drive
- High Speed Switching
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

- DC/DC Converter

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V _{DSS}	–30	V
Gate to Source Voltage	V _{GSS}	±10	V
Drain Current (DC)	I _D	–4.5	A
Drain Current (Pulse) PW ≤ 10μs, duty cycle ≤ 1%	I _{DP}	–18	A
Power Dissipation When mounted on ceramic substrate (1500mm ² × 0.8mm)	P _D	1.5	W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	–55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (1500mm ² × 0.8mm)	R _{θJA}	83.3	°C/W

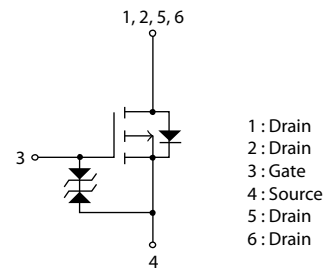


ON Semiconductor[®]

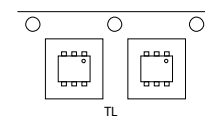
www.onsemi.com

V _{DSS}	R _{DS(on)} Max	I _D Max
–30V	73mΩ@ –4.5V	–4.5A
	99mΩ@ –2.5V	
	155mΩ@ –1.8V	

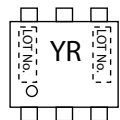
ELECTRICAL CONNECTION P-Channel



PACKING TYPE : TL



MARKING



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

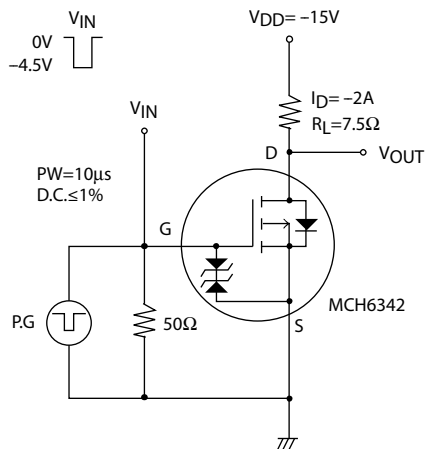
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ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

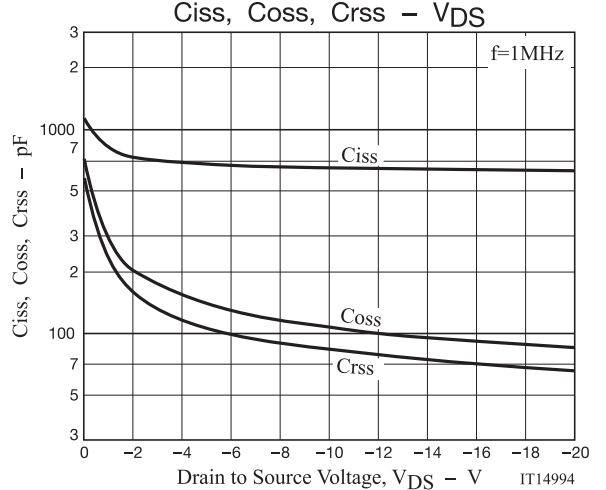
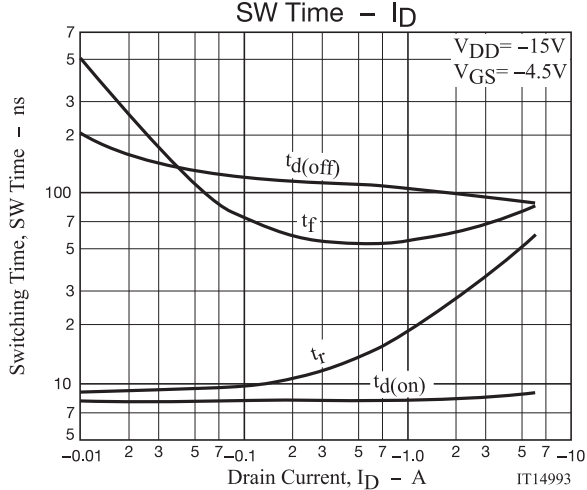
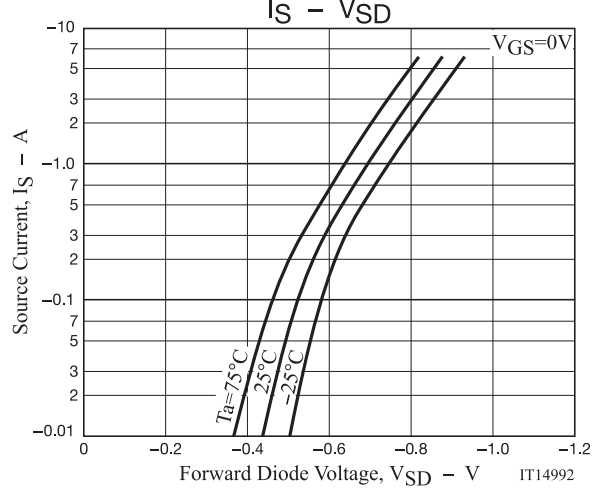
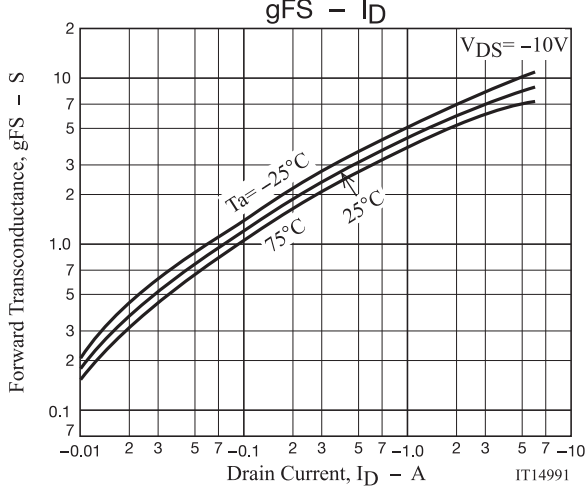
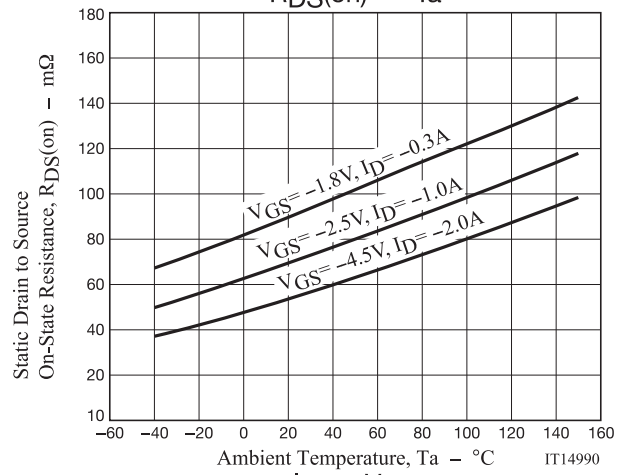
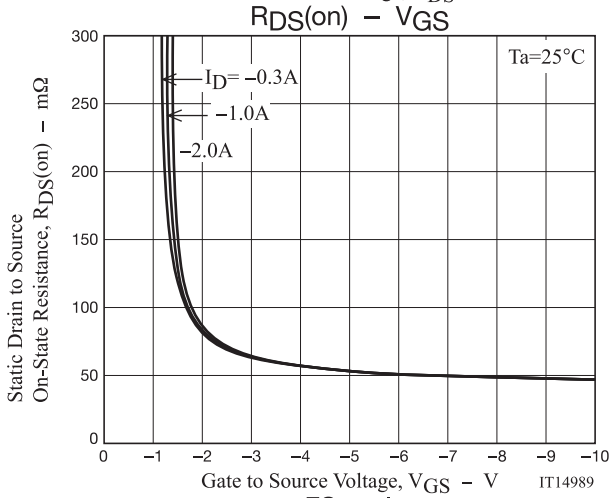
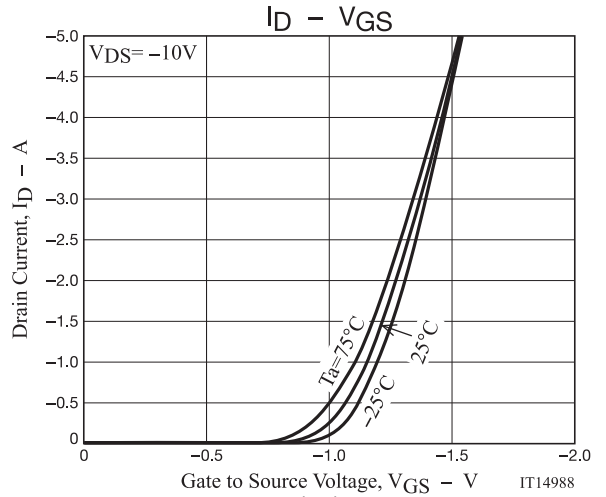
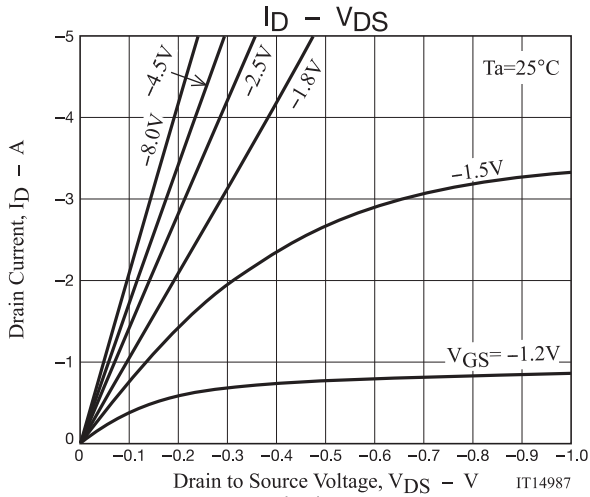
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _{GS} =0V	-30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V			-1	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V			±10	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =-10V, I _D =-1mA	-0.4		-1.3	V
Forward Transconductance	g _{FS}	V _{DS} =-10V, I _D =-2A	3.4	5.8		S
Static Drain to Source On-State Resistance	R _{DS(on)1}	I _D =-2A, V _{GS} =-4.5V		56	73	mΩ
	R _{DS(on)2}	I _D =-1A, V _{GS} =-2.5V		71	99	mΩ
	R _{DS(on)3}	I _D =-0.3A, V _{GS} =-1.8V		95	155	mΩ
Input Capacitance	C _{iss}	V _{DS} =-10V, f=1MHz		650		pF
Output Capacitance	C _{oss}			105		pF
Reverse Transfer Capacitance	C _{rss}			83		pF
Turn-ON Delay Time	t _{d(on)}		See specified Test Circuit		8.2	
Rise Time	t _r			28		ns
Turn-OFF Delay Time	t _{d(off)}			100		ns
Fall Time	t _f			60		ns
Total Gate Charge	Q _g	V _{DS} =-15V, V _{GS} =-4.5V, I _D =-4.5A			8.6	
Gate to Source Charge	Q _{gs}			1.3		nC
Gate to Drain "Miller" Charge	Q _{gd}			2.4		nC
Forward Diode Voltage	V _{SD}	I _S =-4.5A, V _{GS} =0V		-0.83	-1.2	V

Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

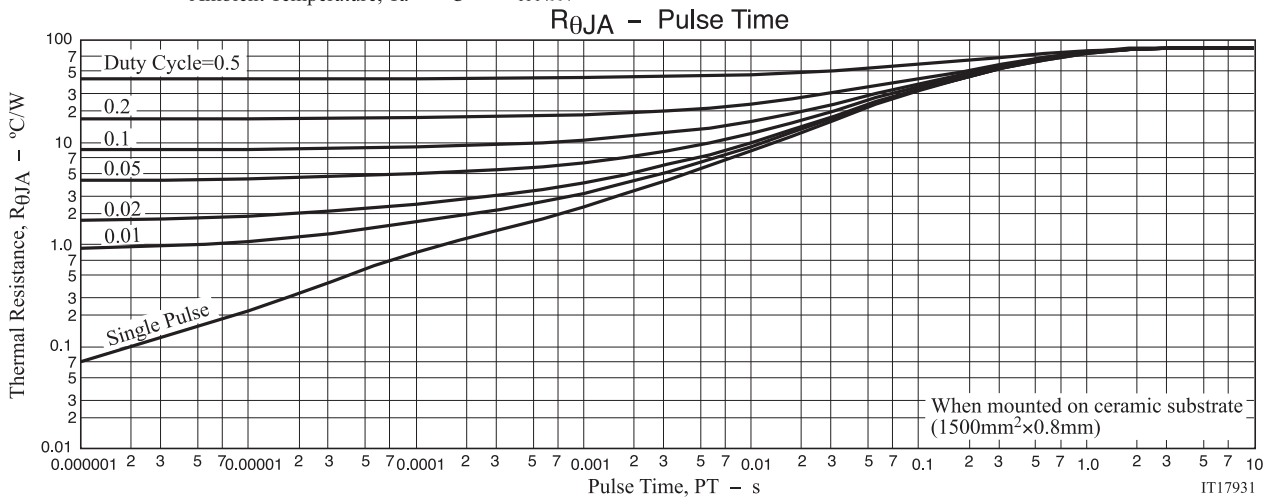
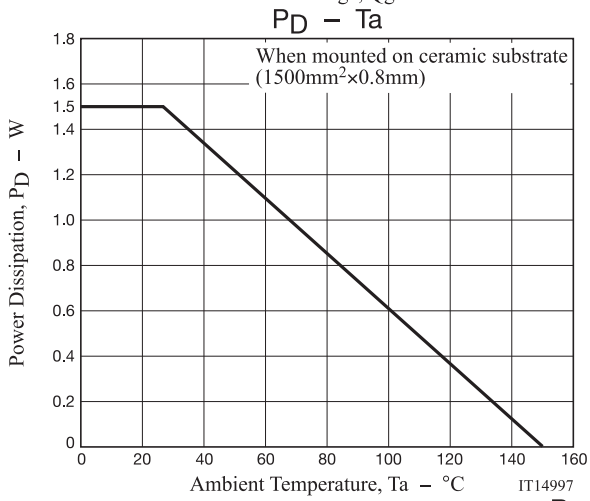
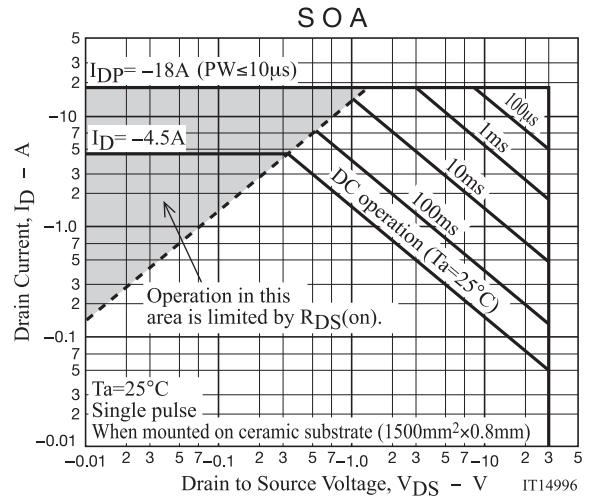
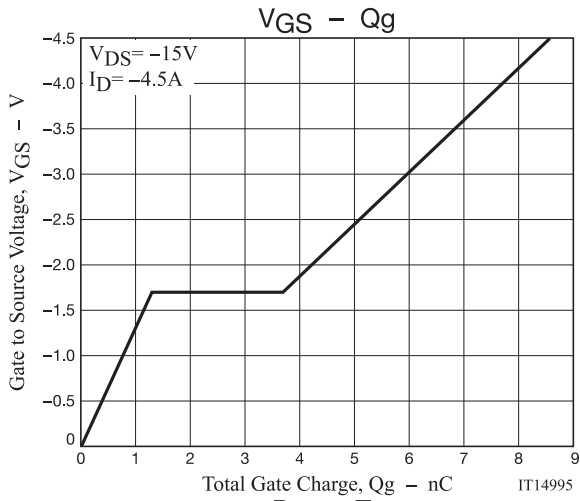
Switching Time Test Circuit



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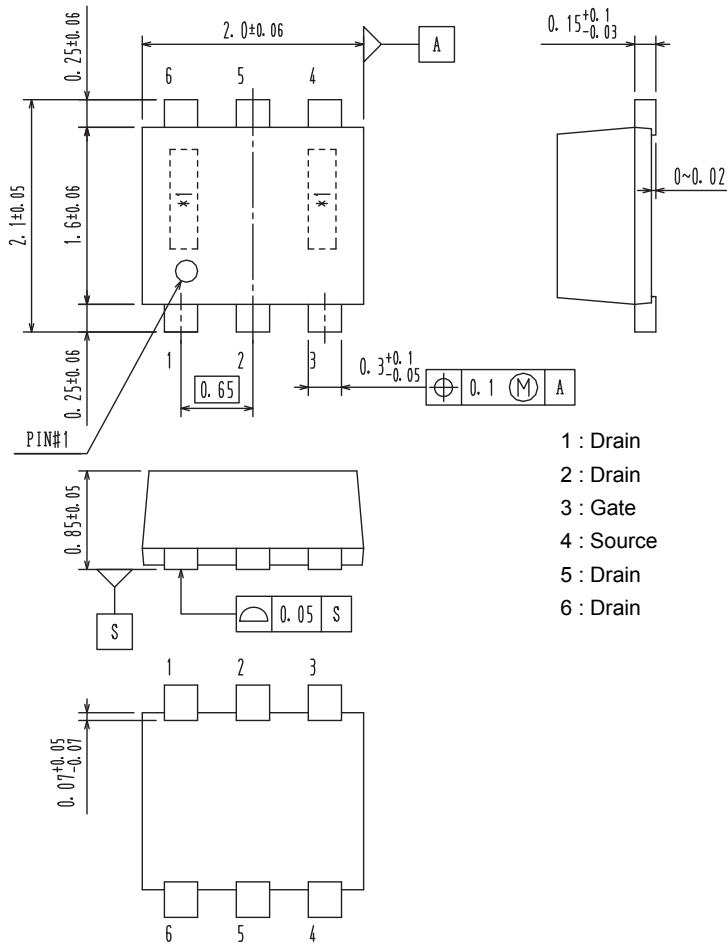


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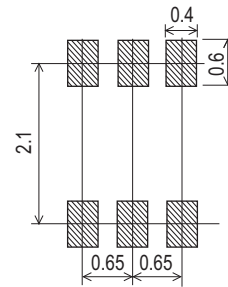
PACKAGE DIMENSIONS

unit : mm

SC-88FL / MCPH6
CASE 419AS
ISSUE O



Recommended Soldering Footprint



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
MCH6342-TL-H	YR	SC-88FL / MCPH6 (Pb-Free / Halogen Free)	3,000 / Tape & Reel
MCH6342-TL-W			

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the MCH6342 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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