

LM137 LM337

Three-terminal adjustable negative voltage regulators

Features

- Output voltage adjustable down to V_{REF}
- 1.5 A guaranteed output current
- 0.3%/V typical load regulation
- 0.01%/V typical line regulation
- Current limit constant with temperature
- Ripple rejection: 77 dB
- Standard 3-lead transistor packages
- Excellent thermal regulation: 0.002%/V
- 50 ppm/°C temperature coefficient

Description

The LM137 series are adjustable 3-terminal negative voltage regulators capable of supplying in excess -1.5 A over a -1.2 to -37 V output voltage range. They are exceptionally easy to use and require only two external resistors to set the output voltage. Further, both line and load regulation are better than standard fixed regulators. Also, LM137 regulators are supplied in standard transistor packages which are easily mounted and handled. In addition to higher performance than fixed regulators, the LM137 series offer full overload protection available only in integrated circuits. Included on the chip are current limit, thermal overload protection and safe area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected.

TO-220 TO-3

Part numbers	Order codes	Packages	Temperature range
LM137	LM137K	TO-3	- 55 °C to 150 °C
LM337	LM337K	TO-3	0 °C to 125 °C
LM337	LM337SP	TO-220	0 °C to 125 °C

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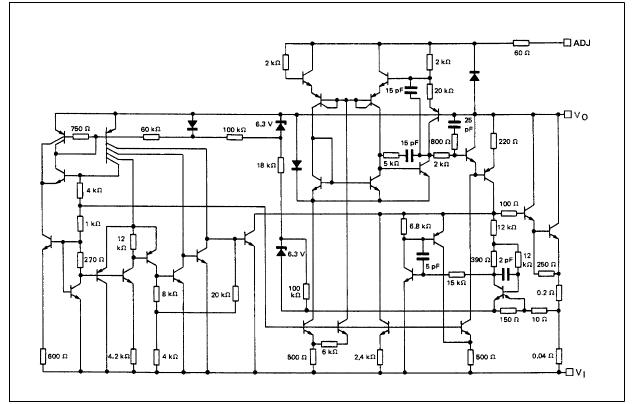
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1 Diagram

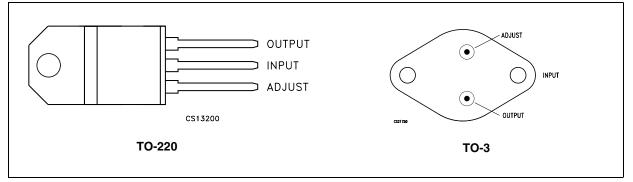
Figure 1. Schematic diagram





2 Pin configuration







3 Maximum ratings

Table 2. Absolute maximum ratings	Table 2.	Absolute	maximum	ratings
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Symbol	Parameter	Value	Unit		
V _I - V _O	Input output voltage differential	40	V		
Ι _Ο	Output current	1.5	А		
PD	Power dissipation	Internally limited			
T _{STG}	Storage temperature range	- 65 to 150	°C		
т		LM137	- 55 to 150	°C	
T _{OP}	Operating junction temperature range		0 to 125		

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

Table 3. Thermal data

Symbol	Parameter	TO-220	TO-3	Unit
R _{thJC}	Thermal resistance junction-case max.	3	4	°C/W
R _{thJA}	Thermal resistance junction-ambient max.	70	35	°C/W



4 **Electrical characteristics**

 T_J = -55 to 150 °C, V_I - V_O = 5 V, I_O = 0.5 A unless otherwise specified.

Table 4.	Electrical characteri	STICS OF LIVE 137			-	-		
Symbol	Parameter	Test condi	Min.	Тур.	Max.	Unit		
		$\begin{split} T_a &= 25^\circ C \\ \hline V_I - V_O &= 3 \text{ to } 40 \text{ V}, T_J = T_{min} \text{ to } T_{max} \\ \hline I_O &= 10 \text{mA to } I_O(\text{max}) P \leq P_{max} \end{split}$		-1.225	-1.25	-1.275		
V _{REF}	Reference voltage			-1.2	-1.25	-1.3	V	
K	Line regulation ⁽¹⁾	T _a = 25°C	I _O = 0.1 A		0.01	0.02	%/V	
K _{VI}		$ V_{I} - V_{O} = 3 \text{ to } 40 \text{ V}$	I _O = 20 mA		0.01	0.02	70/ V	
K	Load regulation ⁽¹⁾	T _a = 25°C	$ V_O \le 5 V$		15	25	mV	
K _{VO}		$ I_0 = 10$ mA to $ I_{O(max)} $	$ V_0 \ge 5 V$		0.3	0.5	%	
	Thermal regulation	$T_a = 25^{\circ}C$, pulse 10 ms		0.002	0.02	%/W		
I _{ADJ}	Adjustment pin current			65	100	μA		
ΔI_{ADJ}	Adjustment pin current change	$T_a = 25^{\circ}C, I_0 = 10 \text{ mA}$ $ V_1 - V_0 = 3 \text{ to } 40 \text{ V}$		2	5	μA		
K _{VI}	Line regulation ⁽¹⁾	$ V_{\rm I} - V_{\rm O} = 3 \text{ to } 40 \text{ V}$		0.02	0.05	%/V		
K	Load regulation ⁽¹⁾		$ V_O \le 5 V$		20	50	mV	
K _{VO}		$ I_O = 10$ mA to $ I_O(max) $ $ V_O \ge 5$ V			0.3	1	%	
		$ V_{I} - V_{O} \le 40 V$	$ V_{1} - V_{0} \le 40 \text{ V}$		2.5	5		
I _{O(min)}	Minimum load current	$ V_{I} - V_{O} \le 10 \text{ V}$			1.2	3	3 mA	
		$ V_{I} - V_{O} \le 15 \text{ V}$		1.5	2.2			
I _{OS}	Short circuit output current	V _I - V _O = 40 V, T _J =25°C		0.24	0.4		A	
V _{NO}	RMS output noise (% of V _O)	$T_a = 25^{\circ}C$, f = 10 Hz to		0.003		%		
-	Disula nais stisus natio	V _O = -10 V, f = 120 Hz			60		JD	
R_{VF}	Ripple rejection ratio	C _{ADJ} = 10 μF		66	77		dB	
K _{VT}	Temperature stability				0.6		%	
K _{VH}	Long term stability	T _a = 125°C, 1000 H			0.3	1	%	

Table 4. Electrical characteristics of LM137

1. Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

 $T_J = 0$ to 150 °C unless otherwise specified.



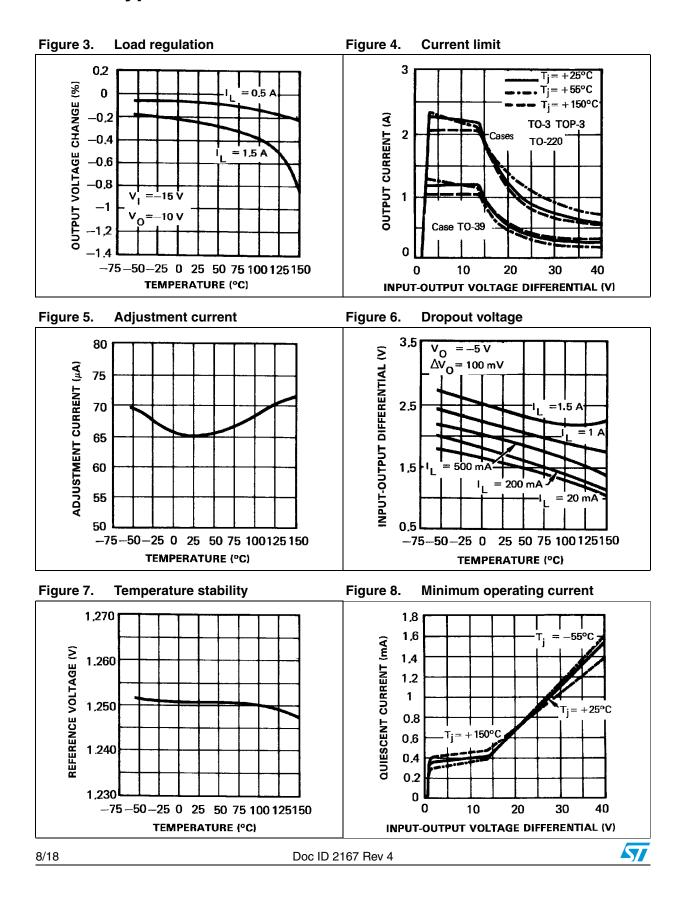
Symbol	Parameter	Test condi	Min.	Тур.	Max.	Unit	
		$T_a = 25^{\circ}C$		-1.213	-1.25	-1.287	
V _{REF}	Reference voltage	$ \begin{split} V_I - V_O &= 3 \text{ to } 40 \text{ V}, T_J = T_{min} \text{ to } T_{max} \\ I_O &= 10 \text{mA to } I_{O(max)} , P \leq P_{max} \end{split} $		-1.2	-1.25	-1.3	V
K _{VI}	Line regulation ⁽¹⁾	T _a = 25°C	I _O = 0.1 A		0.01	0.04	%/V
		$ V_{I} - V_{O} = 3 \text{ to } 40 \text{ V}$	I _O = 20 mA		0.01	0.04	/0/ v
K _{VO}	Load regulation ⁽¹⁾	$T_a = 25^{\circ}C$	$ V_O \le 5 V$		15	50	mV
ICVO		$ I_0 = 10$ mA to $ I_{O(max)} $	$ V_O \ge 5 V$		0.3	1	%
	Thermal regulation	$T_a = 25^{\circ}C$, pulse 10 ms			0.003	0.04	%/W
I _{ADJ}	Adjustment pin current			65	100	μA	
ΔI_{ADJ}	Adjustment pin current change	$T_a = 25^{\circ}C$, $ I_O = 10 \text{ mA to } I_O(\text{max}) $ $ V_I - V_O = 3 \text{ to } 40 \text{ V}$			2	5	μA
K _{VI}	Line regulation ⁽¹⁾	$ V_{\rm I} - V_{\rm O} = 3 \text{ to } 40 \text{ V}$		0.02	0.07	%/V	
K	Load regulation ⁽¹⁾		$ V_O \le 5 V$		20	70	mV
K _{VO}		$ I_{O} = 10\text{mA to } I_{O(\text{max})} $ $ V_{O} \ge 5 \text{ V}$			0.3	1.5	%
	Minimum load ourrent	$ V_{I} - V_{O} \le 40 \text{ V}$			2.5	10	~ ^
I _{O(min)}	Minimum load current	$ V_{I} - V_{O} \le 10 \text{ V}$			1.5	6	mA
		$ V_{I} - V_{O} \le 15 V$		1.5	2.2		۸
I _{OS}	Short circuit output current	$ V_{I} - V_{O} = 40 \text{ V}, \text{ T}_{J} = 25^{\circ}\text{C}$		0.15	0.4		A
V _{NO}	RMS output noise (% of V _O)	T _a = 25°C, f = 10 Hz to 10 kHz			0.003		%
Р	$V_{O} = -10 \text{ V}, \text{ f} = 120 \text{ Hz}$		60	60		٩D	
R_{VF}	Ripple rejection ratio	C _{ADJ} = 10 μF		66	77		dB
K _{VT}	Temperature stability				0.6		%
K _{VH}	Long term stability	T _a = 125°C, 1000 H			0.3	1	%

Table 5. Electrical characteristics of LM337

1. Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.



5 Typical characteristics



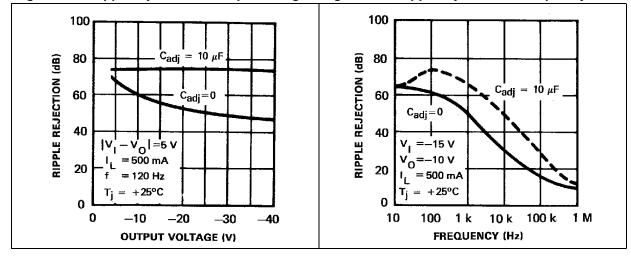
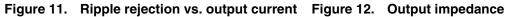
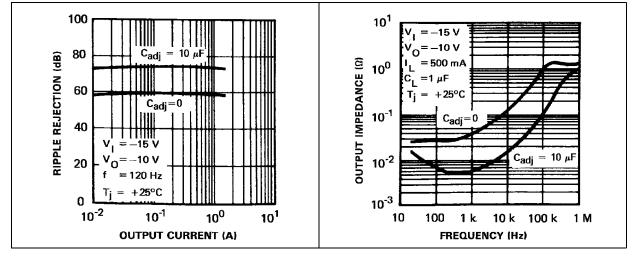


Figure 9. Ripple rejection vs. output voltage Figure 10. Ripple rejection vs. frequency







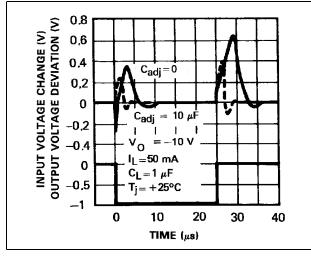
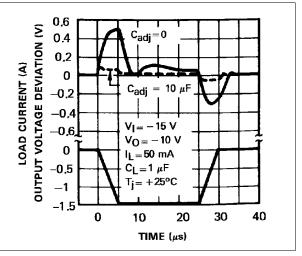


Figure 14. Load transient response



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6 Thermal regulation

When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since power dissipation is large.

Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50 ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of V_O, per watt, within the first 10ms after a step of power, is applied.

The LM137 specification is 0.02%/W max. In *Figure 1*, a typical LM337's output drifts only 3 mV for 0.03% of $V_0 = -10$ V) when a 10 W pulse is applied for 10 ms. This performance is thus well inside the specification limit of 0.02%/W x 10 W = 0.2% max. When the 10 W pulse is ended the thermal regulation again shows a 3 mV step as the LM137 chip cools off. Note that the load regulation error of about 8 mV (0.08%) is additional to the thermal regulation error.

In *Figure 2*, when the 10 W pulse is applied for 100 ms, the output drifts only slightly beyond the drift in the first 10 ms and the thermal error stays well within 0.1% (10 mV).



7 Typical application

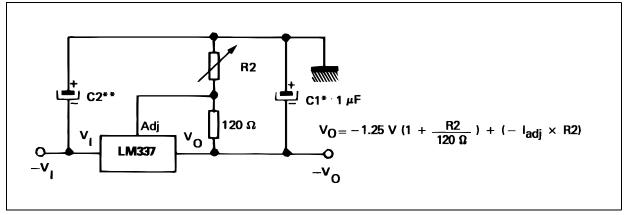
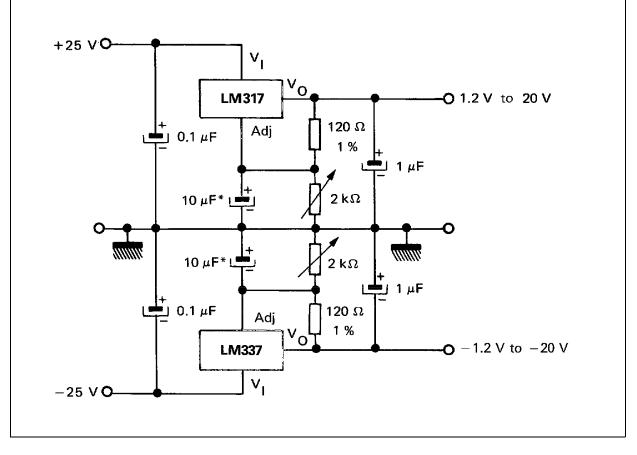


Figure 15. Adjustable negative voltage regulator

* C1 = 1 μ F solid tantalum or 10 μ F aluminium electrolytic required for stability.

** C2 = 1 µF solid tantalum is required only if regulator is more than 10 cm from power supply filter capacitors





 * The 10 μF capacitors are optimal to improve ripple rejection.



Figure 17. Current regulator

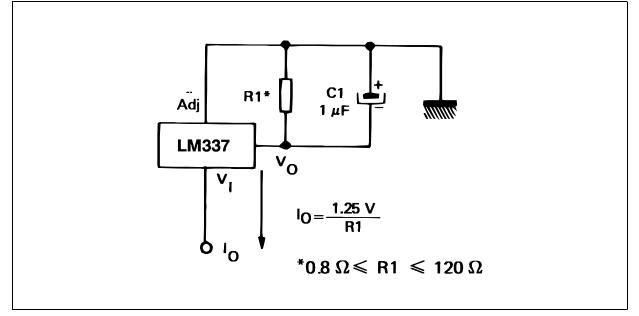
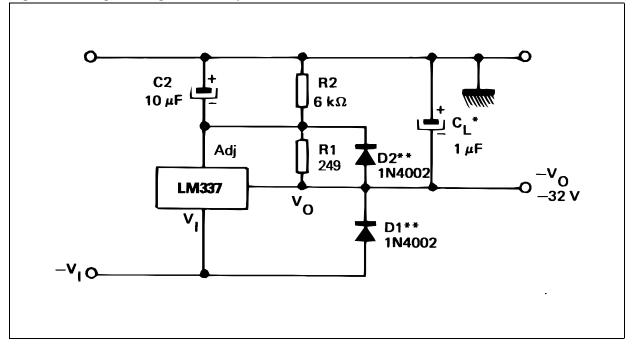
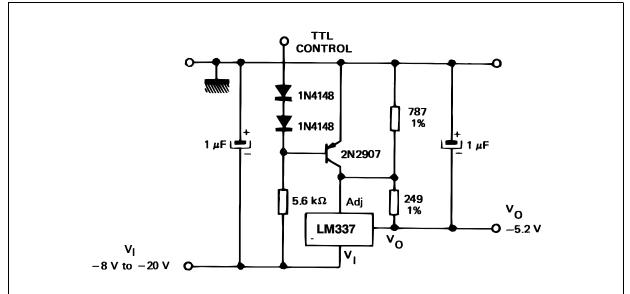


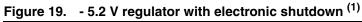
Figure 18. Negative regulator with protection diodes



- * When CL is larger than 20 $\mu\text{F},$ D1 protects the LM137 in case the input supply is shorted.
- ** When C2 is larger than 10 μF and V_O is larger than 25 V, D2 protects the LM137 in case the output is shorted.

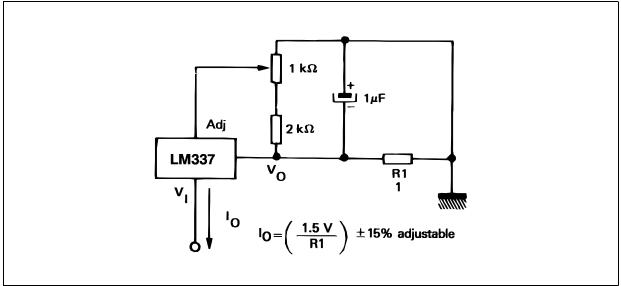






1. Minimum output = - 1.3 V when control input is low.

Figure 20. Current regulator





8 Package mechanical data

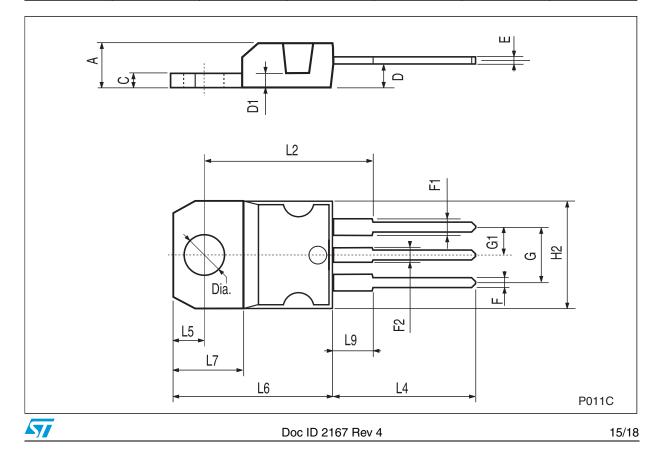
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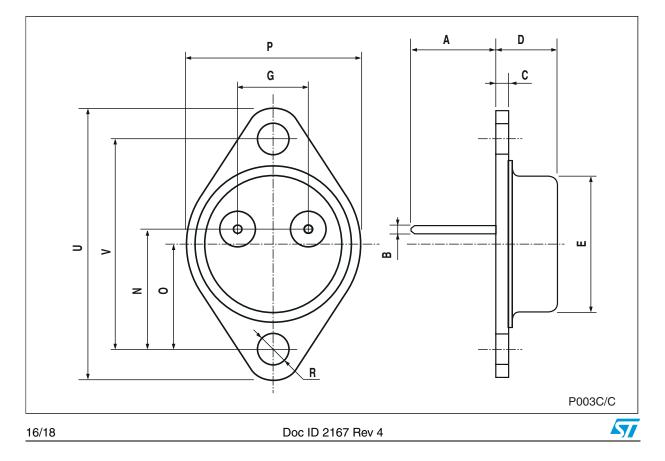


Dim.		mm.		inch.		
Dini.	Min.	Тур.	Max.	Min.	Тур.	Max.
А	4.40		4.60	0.173		0.181
С	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151





TO-3 mechanical data						
Dim.		mm.				
Dini.	Min.	Тур.	Max.	Min.	Тур.	Max.
А		11.85			0.466	
В	0.96	1.05	1.10	0.037	0.041	0.043
С			1.70			0.066
D			8.7			0.342
E			20.0			0.787
G		10.9			0.429	
Ν		16.9			0.665	
Р			26.2			1.031
R	3.88		4.09	0.152		0.161
U			39.5			1.555
V		30.10			1.185	



9 Revision history

Date	Revision	Changes
19-Jul-2004	1	First issue.
10-Jan-2005	2	Modified pin connection for TO-3.
17-Jul-2008	3	Added: Table 1 on page 1.
03-Oct-2011	4	Modified: Table 1 on page 1.

Table 6.Document revision history



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