# **SB80W10T**

# Schottky Barrier Diode 100V, 8A, Low IR, Monolithic Dual TP Common Cathode



# Features

• VF max=0.8V

• IR max=0.1mA

• Halogen free compliance

# **Specifications**

## Absolute Maximum Ratings at Ta=25°C

5				
Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>		100	V
Nonrepetitive Peak Reverse Surge Voltage	V <sub>RSM</sub>		105	V
Average Output Current	I <sub>O</sub> *1	50Hz resistive load, sine wave Tc=62°C	8	А
	I0*2	50Hz resistive load, sine wave Tc=121°C	4	А
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	40	А
Junction Temperature	Tj		-55 to +150	°C
Storage Temperature	Tstg		-55 to +150	°C

Note) \*1. Indicates the total value

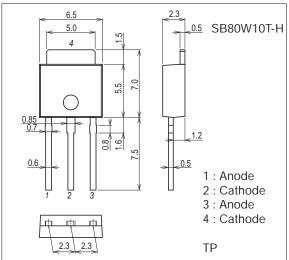
\*2. Value per element

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### Package Dimensions

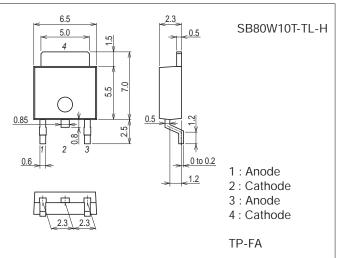
unit : mm (typ)







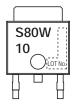
7003-002



### Product & Package Information

- Package : TP
- JEITA, JEDEC : SC-64, TO-251
- Minimum Packing Quantity : 500 pcs./bag

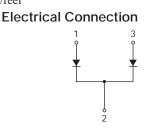




Package : TP-FA

- JEITA, JEDEC : SC-63, TO-252
- Minimum Packing Quantity : 700 pcs./reel

Packing Type (TP-FA) : TL



Semiconductor Components Industries, LLC, 2013 September, 2013

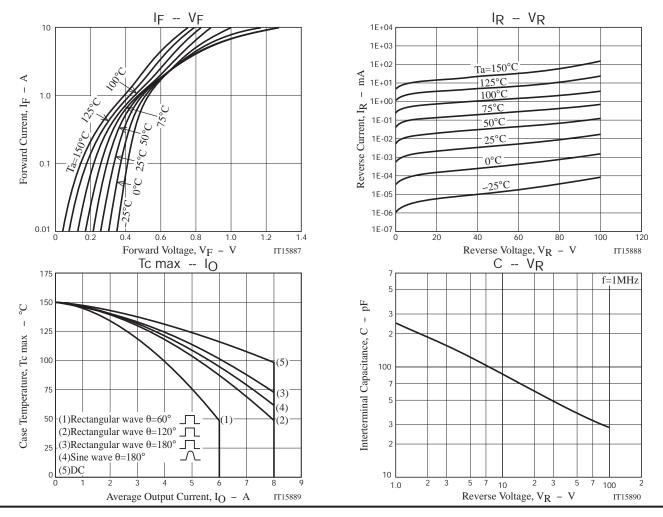
#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Symbol Conditions	Ratings			Unit
Faianietei	Symbol		min	typ	max	Unit
Reverse Voltage	VR	IR=1mA, Tj=25°C *2	100			V
Forward Voltage	VF	I <sub>F</sub> =3.0A, Tj=25°C *2			0.8	V
Reverse Current	IR	V <sub>R</sub> =50V, Tj=25°C *2			0.1	mA
Interterminal Capacitance	С	V <sub>R</sub> =10V, Tj=25°C *2		90		рF
Transient Thermal Resistance	Rth(j-c)	Junction-Case : Smoothed DC		6		°C/W

Note) \*2. Value per element

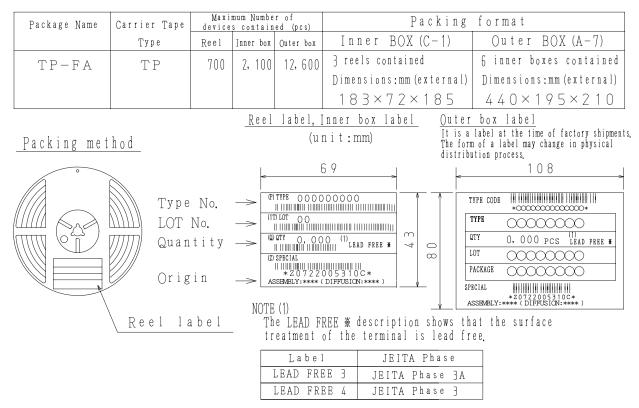
#### **Ordering Information**

0			
Device	Package	Shipping	memo
SB80W10T-H	TP	500pcs./bag	Dh Free and Helegen Free
SB80W10T-TL-H	TP-FA	700pcs./reel	Pb Free and Halogen Free



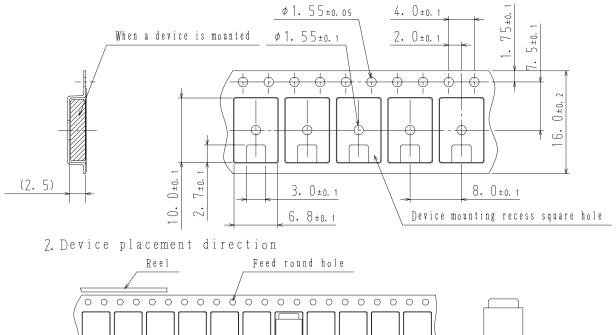
## Taping Specification SB80W10T-TL-H

Packing Format



# Taping configuration

1. Carrier tape size (unit:mm)



Those with one electrode terminal on the feed hole side ..... TL

Land Pattern Example

# Outline Drawing SB80W10T-TL-H

0. 85±0. 2

0. 7±0.2

0. 6±0. 2

2. 3±0. 2

H

Pin 2 is idle pin with electrical designation only carried.

2. 3±0. 2

#### Mass (g) Unit Unit: mm 0.282 \* For reference mm 7.0 6. 5±0. 2 2. 3±0. 2 5. O±0. 2 1. 5±0. 2 0. 5±0. 1 4 7.0 [\*1] 2.0 1.5 7. 0±0. 3 5. 5±0. 2 LOT No. 2.5 <u>< 2.3</u> 1. 2±0. 3 2.3 0. 5±0. 15 Æ 2. 5±0. 3 i 0. 8±0. 3 1 2 3 1. 2±0. 3 0~0. 2

\*1:Lot indication

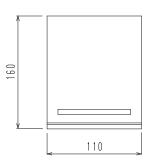
# Bag Packing Specification SB80W10T-H

1. Packing Format

Package Name		Maximum Numbe	r of devices cont	ained (pcs)	
I GOUGSO Hamo	Bag	Inner box	Outer box		
TP		B-1	A-1	A-2	
	500	10,000	50,000	30,000	
		Packing format (Dimensions:mm (external))			
		Inner box Outer box			
		B-1	A-1	A-2	
4 4 5 × 2 2 5 × 5 5		470×250×300	470×250×190		

<u>2. Bag dimensions</u>





# <u>4. Outer box label</u>

(unit:mm)

It is a label at the time of factory shipments, The form of a label may change in physical distribution process.

3. Bag	label, Inner	box label
	(unit:mm)	

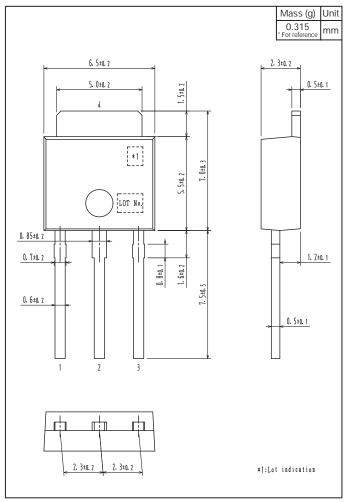
	L 69	
Туре No. —>	(P) TYPE 0000000000	Î
LOT No>		
Quantity —>	(Q) QTY O, OOO (1) 	4 3
Origin ->	(2) SPECIAL 	V

NOTE (1) The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

Label			JEITA Phase
LEAD	FREE	3	JEITA Phase 3A
LEAD	FREE	4	JEITA Phase 3

	TYPE CODE
	TYPE COCCOCCO
	QTY 0, 000 PCS LEAD FREE #
8	00000000 101
	PACKAGE COCOCOCO
	SPECIAL #20722005310C* ASSEMELY:**** (DIFFUSION:*****)
	108

# Outline Drawing SB80W10T-H



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal