ON Semiconductor

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Switching Diode, Dual, Common Anode, 70 V

BAW56WT1G, SBAW56WT1G

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Max	Unit	
Reverse Voltage		V _R	70	V
Forward Current		IF	200	mA
Peak Forward Surge Current		I _{FM(surge)}	500	mA
Forward Surge Max Current (Single Square Wave)	1 μs 1 ms 1 s	I _{FSM}	4 1 0.5	А

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS (T_A = 25°C)

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C	P _D	200	mW
Derate above 25°C		1.6	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	625	°C/W
Total Device Dissipation Alumina Substrate (Note 2) T _A = 25°C	P_{D}	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

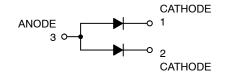


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SC-70 CASE 419 STYLE 4



MARKING DIAGRAM



A1 = Device Code

M = Date Code*

Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
BAW56WT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel
SBAW56WT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel

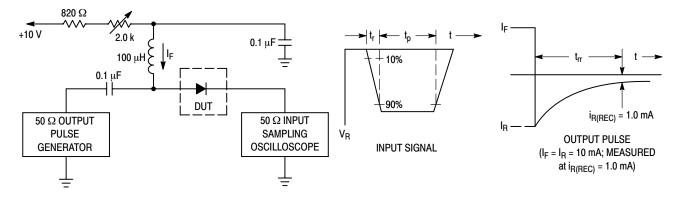
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BAW56WT1G, SBAW56WT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				•
Reverse Breakdown Voltage (I _(BR) = 100 μA)	V _(BR)	70	-	V
Reverse Voltage Leakage Current $(V_R = 25 \text{ V}, T_J = 150^{\circ}\text{C})$ $(V_R = 70 \text{ V})$ $(V_R = 70 \text{ V}, T_J = 150^{\circ}\text{C})$	I _R	- - -	30 2.5 50	μΑ
Diode Capacitance (V _R = 0, f = 1.0 MHz)	C _D	-	2.0	pF
Forward Voltage (I _F = 1.0 mA) (I _F = 10 mA) (I _F = 50 mA) (I _F = 150 mA)	V _F	- - - -	715 855 1000 1250	mV
Reverse Recovery Time (I _F = I _R = 10 mA, R _L = 100 Ω , I _{R(REC)} = 1.0 mA) (Figure 1)	t _{rr}	-	6.0	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



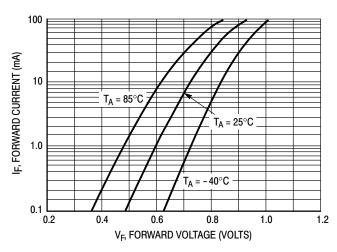
Notes: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{ri}

Figure 1. Recovery Time Equivalent Test Circuit

BAW56WT1G, SBAW56WT1G

TYPICAL CHARACTERISTICS



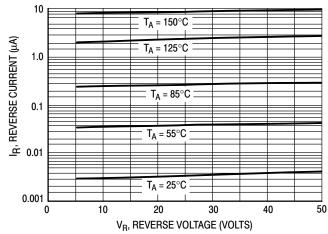


Figure 2. Forward Voltage

Figure 3. Leakage Current

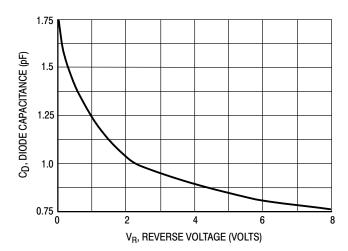


Figure 4. Capacitance



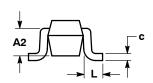
SC-70 (SOT-323) CASE 419-04 ISSUE N

DATE 11 NOV 2008

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	MOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.70 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC				0.026 BSC	;
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095

0.05 (0.002)



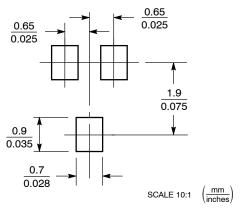
GENERIC MARKING DIAGRAM



XX = Specific Device Code Μ = Date Code = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6: PIN 1. EMITTER 2. BASE	STYLE 7: PIN 1. BASE 2. EMITTER	STYLE 8: PIN 1. GATE 2. SOURCE	STYLE 9: PIN 1. ANODE 2. CATHODE	STYLE 10: PIN 1. CATHODE 2. ANODE	STYLE 11: PIN 1. CATHODE 2. CATHODE
COLLECTOR	COLLECTOR	3. DRAIN	CATHODE-ANODE	3. ANODE-CATHODE	CATHOD

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