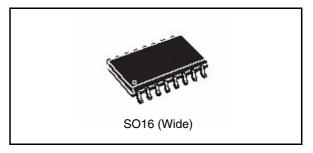


Low drop dual power operational amplifiers

Features

- Output current up to 1 A
- Operates at low voltages
- Single or split supply
- Large common-mode and differential-mode range
- Low input-offset voltage
- Ground compatible inputs
- Low saturation voltage
- Thermal shutdown
- Clamp diode



Description

The L2720W is a monolithic integrated circuit in SO16 (Wide) package, intended for use as a power operational amplifier in a wide range of applications including servo amplifiers and power supplies.

It is particularly suitable for driving coils, inductive loads and for use in motors.

The high gain and high output power capability provide superior performance whenever an operational amplifier/power booster combination is required.

Table 1. Device summary

| Order code | Package | Packaging |
|------------|-------------|---------------|
| L2720W | SO16 (Wide) | Tube |
| L2720W13TR | SO16 (Wide) | Tape and reel |

1 Connection diagrams

Figure 1. Block diagram

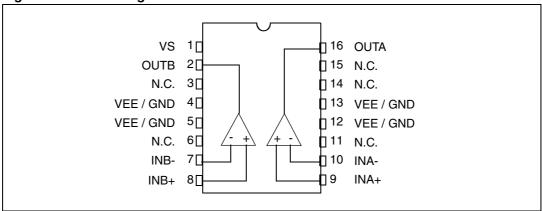
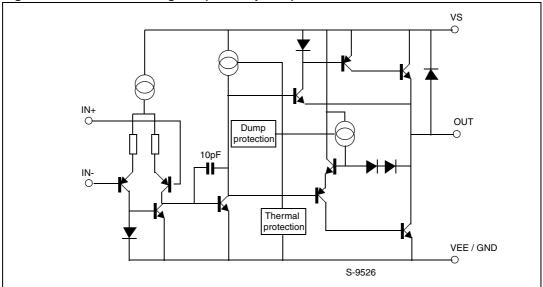


Figure 2. Schematic diagram (one amplifier)



L2720W Pin out

2 Pin out

Figure 3. Pin connection (top view)

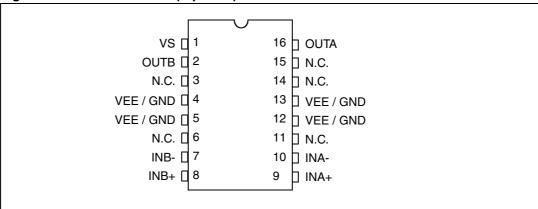


Table 2. Pin description

| Pin | Name | Туре | Description | |
|-----|-----------|--------|---------------------------------|--|
| 1 | VS | Power | Power supply positive | |
| 2 | OUTB | Output | Amplifier B output | |
| 3 | N.C. | - | No internal connection | |
| 4 | VEE / GND | Power | Power supply negative or ground | |
| 5 | VEE / GND | Power | Power supply negative or ground | |
| 6 | N.C. | - | No internal connection | |
| 7 | INB- | Input | Amplifier B input | |
| 8 | INB+ | Input | Amplifier B input | |
| 9 | INA+ | Input | Amplifier A input | |
| 10 | INA- | Input | Amplifier A input | |
| 11 | N.C. | - | No internal connection | |
| 12 | VEE / GND | Power | Power supply negative or ground | |
| 13 | VEE / GND | Power | Power supply negative or ground | |
| 14 | N.C. | - | No internal connection | |
| 15 | N.C. | - | No internal connection | |
| 16 | OUTA | Output | Amplifier A output | |

3 Electrical specifications

3.1 Absolute maximum ratings

Table 3. Absolute maximum ratings

| Pin/symbol | Description | Min | Max | Unit |
|-----------------------------------|--|-----|-----------------|------|
| V _S | Supply voltage | - | 28 | V |
| V _{S-PK} | Peak supply voltage (50 ms) | - | 50 | ٧ |
| V _i | Input voltage range | - | V _s | ٧ |
| V _i | Differential input voltage range | - | ±V _S | ٧ |
| Io | DC output current | - | 1 | Α |
| I _{O-PK} | Peak output current (non repetitive) | - | 1.5 | Α |
| T _{op} | Operating ambient temperature range | -40 | 125 | °C |
| T _{stg} , T _j | Storage and junction temperature range | -40 | 150 | °C |

3.2 Thermal data

Table 4. Thermal data

| Device | Parameter | | Тур | Max | Unit |
|------------------------|--|---|-----|-----|------|
| R _{th j-amb} | Thermal resistance junction to ambient (1) | - | 65 | - | °C/W |
| R _{th j-case} | Thermal resistance junction to case pins (2) | | 12 | - | °C/W |

^{1.} On double layer PCB with 4 cm² copper dissipating area

3.3 Recommended operating conditions

Table 5. Recommended operating conditions

| Symbol | Parameter | | Тур | Max | Unit |
|-----------------|------------------------------|------|-----|----------------------------------|------|
| V | Positive single power supply | 4.0 | - | 28 | V |
| V _S | Positive split power supply | 2.0 | - | 14 | V |
| V | Negative single power supply | - | 0 | - | V |
| V _E | Negative split power supply | -2.0 | - | -14 | V |
| V _{IN} | Input voltage | | - | V _S to V _E | V |

^{2.} Referred to pins 4, 5, 12 and 13.

3.4 Electrical characteristics

The electrical specifications in *Table 6* below are given for operation under the conditions $V_S = 24$ V, $T_{amb} = -40$ °C to 125 °C and RI connected to GND, unless otherwise specified

Table 6. Electrical characteristics

| Symbol | Parameter | | ions | Min | Тур | Max | Unit |
|--------------------------|--|--|--|-----|-----|-----|------------|
| | Quiacont aurrent | V V /2 | T _{amb} = 25 °C | - | 10 | 15 | mA |
| Is | Quiescent current | $V_O = V_S / 2$ | - | - | 10 | 18 | |
| lih | |],, | T _{amb} = 25 °C | - | 0.2 | 1 | μА |
| lib | Input bias current | V _{CM} = 0 | - | - | 0.2 | 1 | |
| lob | Input offoot ourront | V = 0 | T _{amb} = 25 °C | - | - | 100 | |
| IOD | Input offset current | $V_{CM} = 0$ | - | - | - | 100 | nA |
| V | Input offset voltage | T _{amb} = 25 °C | | -10 | - | 10 | mV |
| V _{os} | input onset voltage | - | | -10 | - | 10 | Tilly |
| $\Delta V_{os}/\Delta T$ | Average temperature coefficient of Vos | - | | - | 20 | - | μV/° C |
| SR | Slew rate | | Vin = -10 V to +10 V, $R_L = 2 k\Omega$, $C_L = 100 pF$, $Av = -1$, $T_{amb} = 25 °C$ | | 2 | - | V/μs |
| В | Gain-bandwidth product | - | | - | 1.2 | - | MHz |
| C | Open leep voltage gain | f = 100 Hz | | 70 | 80 | - | - dB |
| G _v | Open loop voltage gain | f = 1 kHz | | - | 60 | - | |
| CMRR | Common mode rejection ratio | f = 1 kHz | | 66 | 84 | - | dB |
| 0) (DD | Supply voltage rejection ratio | f = 100 Hz $R_G = 10 \text{ k}\Omega$ $V_R = 0.5 \text{ V}$ | V _s = 24 V | - | 70 | - | dB |
| SVRR | | | Vs = ±12 V | 60 | 75 | - | |
| | | I _p = 100 mA | T _{amb} = 25 °C | - | 0.7 | 1 | |
| V | Drop voltage high | | - | - | 0.8 | 1.5 | |
| $V_{DROP(H)}$ | Drop voltage high | L _ 1 A | T _{amb} = 25 °C | - | 1.0 | 1.5 | V |
| | | I _p = 1 A | - | - | 1.1 | 1.5 | |
| V _{DROP(L)} | | I _p = 100 mA | T _{amb} = 25 °C | - | 0.3 | 0.7 | |
| | Drop voltage low | | - | - | 0.4 | 1 | V |
| | Drop voltage low | L = 1 A | T _{amb} = 25 °C | - | 0.5 | 1 |] ' |
| | | I _p = 1 A | - | - | 1.3 | 1.5 | 7 |
| | | | V _s = 24 V | - | 60 | - | |
| C _s | Channel separation | | - | 60 | - | dB | |
| e _N | Input noise voltage | B = 22 Hz to 22 kHz, T _{amb} = 25 °C | | - | 10 | - | μV |



 Table 6.
 Electrical characteristics (continued)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------|---------------------|---|-----|-----|-----|------|
| I _N | Input noise current | B = 22 Hz to 22 kHz, T_{amb} = 25 °C | - | 200 | - | pА |
| φ _m | Phase margin | $R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF},$ $T_{amb} = 25 \text{ °C}$ | - | 65 | - | °C |
| A _m | Gain margin | $R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF},$ $T_{amb} = 25 \text{ °C}$ | - | 15 | - | dB |

3.5 Characterization curves

Figure 4. Quiescent current vs supply current

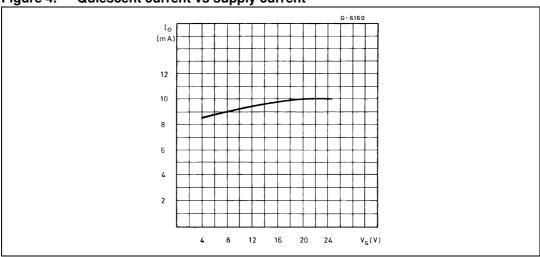
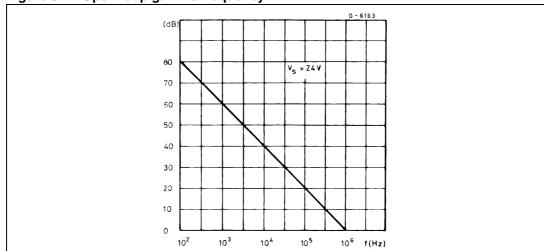


Figure 5. Open loop gain vs frequency



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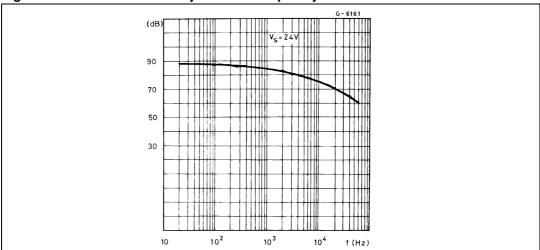
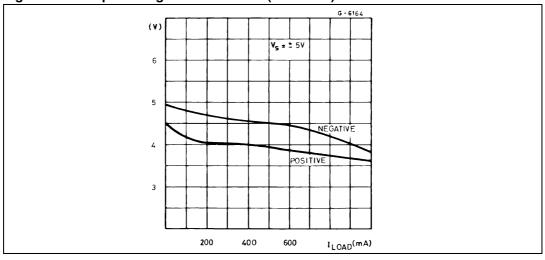
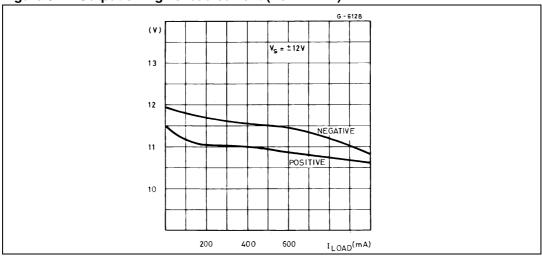


Figure 6. Common mode rejection vs frequency

Figure 7. Output swing vs load current ($Vs = \pm 5 V$)







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Figure 9. Supply voltage rejection vs frequency

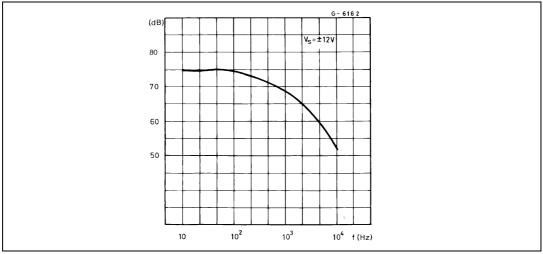


Figure 10. Channel separation vs frequency

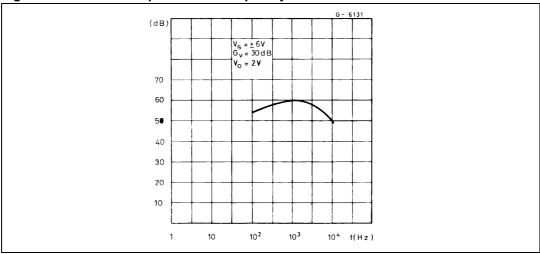
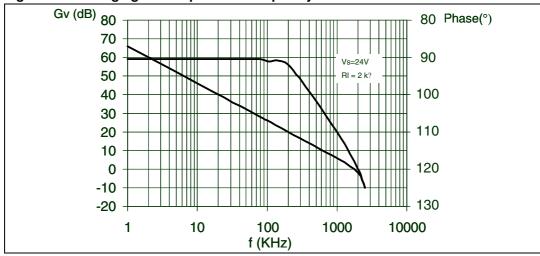


Figure 11. Voltage gain and phase vs frequency



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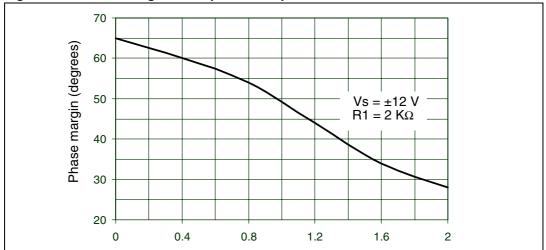


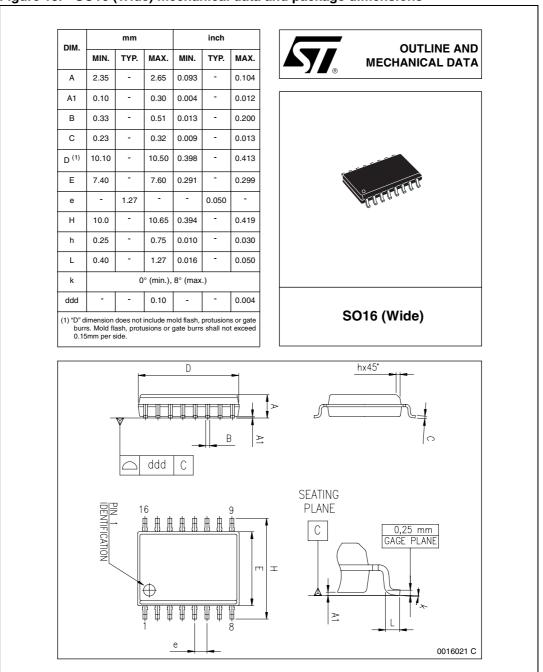
Figure 12. Phase margin vs output load capacitance

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4 Package mechanical data

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Figure 13. SO16 (Wide) mechanical data and package dimensions



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L2720W Revision history

5 Revision history

Table 7. Document revision history

| Date | Revision | Changes |
|-------------|----------|--|
| 04-Apr-2007 | 1 | Initial release. |
| 03-Sep-2010 | 2 | Complete update and change in presentation |

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