Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT223 plastic package. NPN complement: PBSS4540Z.

2. Features and benefits

- · Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation.
- AEC-Q101 qualified

3. Applications

- · Supply line switching circuits
- Battery management applications
- DC/DC converter applications
- · Strobe flash units
- · Heavy duty battery powered equipment (motor and lamp drivers)
- · MOSFET driver applications.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-40	V
I _C	collector current		-	-	-5	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-10	Α
R _{CEsat}	collector-emitter saturation resistance	I_C = -2 A; I_B = -200 mA; t_p ≤ 300 μs; pulsed; δ ≤ 0.02; T_{amb} = 25 °C	-	55	80	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	C
2	С	collector		в
3	Е	emitter		- N
4	С	collector	☐1 ☐2 ☐3 SC-73 (SOT223)	E sym132



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6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBSS5540Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223		

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS5540Z	PB5540

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-40	V
V _{CEO}	collector-emitter voltage	open base		-	-40	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-5	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-10	Α
I _{BM}	peak base current			-	-2	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.35	W
			[2]	-	2	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm²

9. Thermal characteristics

Table 6. Thermal characteristics

	Symbol	Parameter	Conditions		Min	Тур	Max	Unit
	R _{th(j-a)} thermal resistance from junction to ambient	thermal resistance from	in free air	[1]	-	-	92	K/W
		junction to ambient		[2]	-	-	62	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

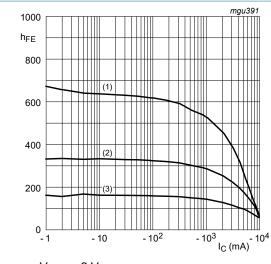
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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A	-40	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = -10 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-40	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage (collector open)	I_E = -100 μ A; I_B = 0 mA; T_{amb} = 25 °C	-6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = -30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C	-	-	-50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V_{CE} = -2 V; I_{C} = -500 mA; T_{amb} = 25 °C	250	350	-	
		V_{CE} = -2 V; I_{C} = -1 A; t_{p} ≤ 300 μs; pulsed; δ ≤ 0.02; T_{amb} = 25 °C	200	300	-	
		V_{CE} = -2 V; I_{C} = -2 A; t_{p} ≤ 300 μs; pulsed; δ ≤ 0.02; T_{amb} = 25 °C	150	250	-	
		V_{CE} = -2 V; I_{C} = -5 A; t_{p} ≤ 300 μs; pulsed; δ ≤ 0.02; T_{amb} = 25 °C	50	150	-	
V _{CEsat}	collector-emitter	I_C = -500 mA; I_B = -5 mA; T_{amb} = 25 °C	-	-80	-120	mV
	saturation voltage	I _C = -1 A; I _B = -10 mA; T _{amb} = 25 °C	-	-120	-170	mV
		I _C = -2 A; I _B = -200 mA; T _{amb} = 25 °C	-	-110	-160	mV
		I _C = -5 A; I _B = -500 mA; T _{amb} = 25 °C	-	-250	-375	mV
R _{CEsat}	collector-emitter saturation resistance	I_C = -2 A; I_B = -200 mA; $t_p \le 300 \mu s$; pulsed; $\delta \le 0.02$; T_{amb} = 25 °C	-	55	80	mΩ
V _{BEsat}	base-emitter saturation voltage	I_C = -5 A; I_B = -500 mA; T_{amb} = 25 °C	-	-	-1.3	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = -2 V; I_{C} = -2 A; T_{amb} = 25 °C	-	-0.8	-1.25	V
f _T	transition frequency	V_{CE} = -10 V; I_{C} = -100 mA; f = 100 MHz; T_{amb} = 25 °C	60	120	-	MHz
C _c	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	90	105	pF

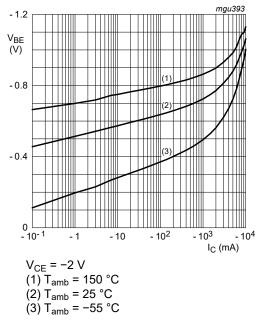
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$$(2) T_{amb} = 25 °C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 1. DC current gain as a function of collector current; typical values

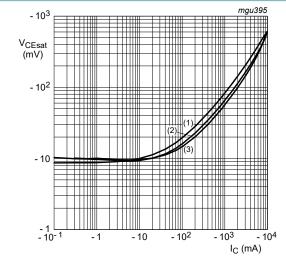


$$(1) T_{amb} = 150 °C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 2. Base-emitter voltage as a function of collector current; typical values



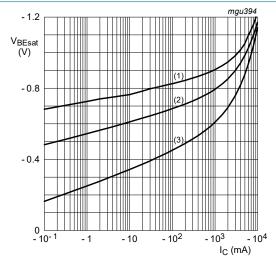
$$I_C/I_B = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = -55 °C$$

Fig. 3. Collector-emitter saturation voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 20$$

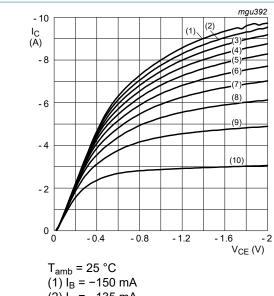
$$(1) T_{amb} = 150 °C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values

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 $(2) I_B = -135 \text{ mA}$

 $(3) I_B^- = -120 \text{ mA}$

 $(4) I_B = -105 \text{ mA}$

 $(5) I_B = -90 \text{ mA}$

(6) $I_B = -75 \text{ mA}$

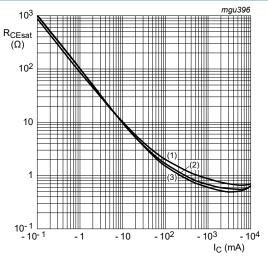
 $(7) I_B = -60 \text{ mA}$

 $(8) I_B = -45 \text{ mA}$

(9) $I_B = -30 \text{ mA}$

 $(10) I_B = -15 \text{ mA}$

Fig. 5. Collector current as a function of collectoremitter voltage; typical values



 $I_C/I_B = 20$

(1) $T_{amb} = 150 \,^{\circ}C$ (2) $T_{amb} = 25 \,^{\circ}C$

(3) $T_{amb} = -55 \, ^{\circ}C$

Fig. 6. Collector-emitter equivalent on-resistance as a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline

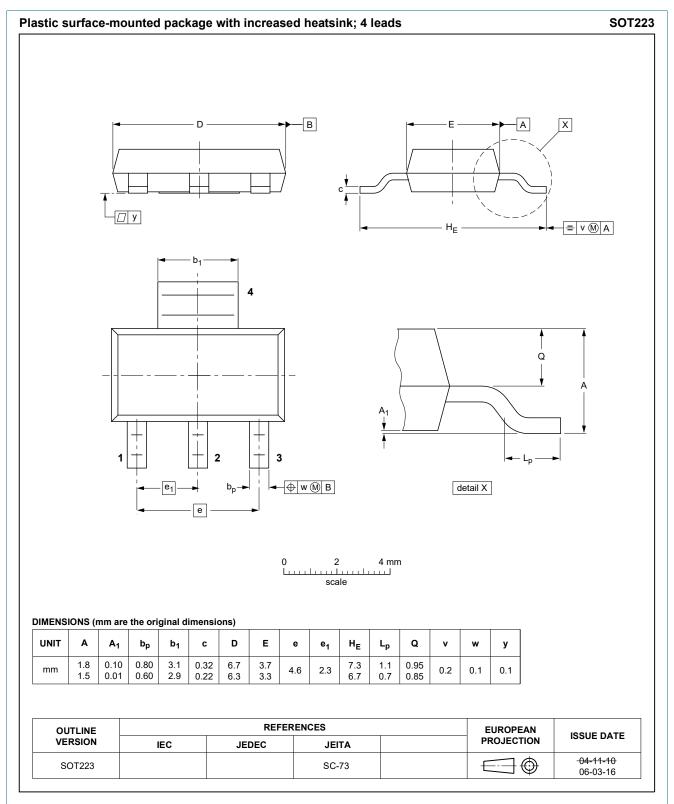
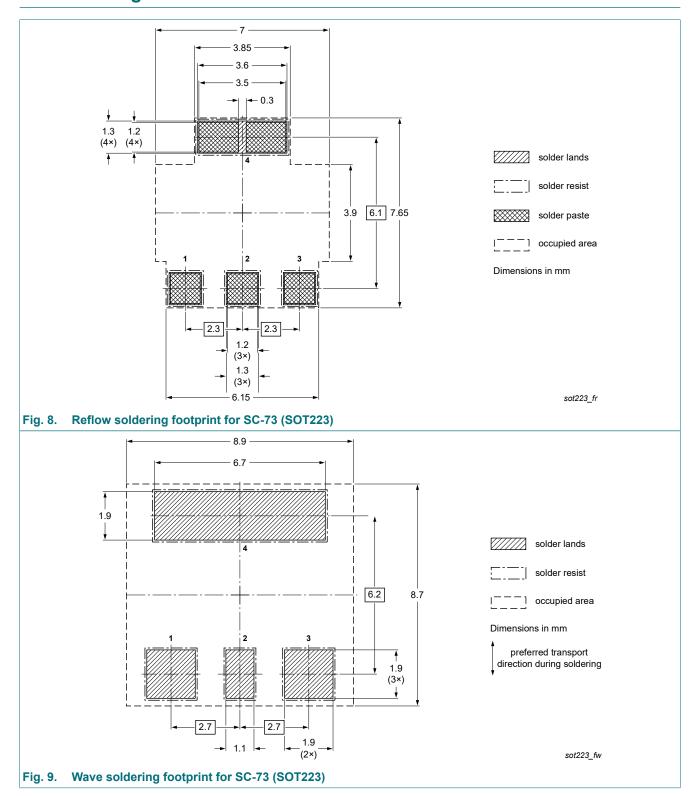


Fig. 7. Package outline SC-73 (SOT223)

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS5540Z v.3	20190920	Product data sheet	-	PBSS5540Z v.2
Modifications:	information" an Characteristics The format of th Nexperia.	C-Q101 qualification added in the control of the co	esigned to comply with	h the identity guidelines of
PBSS5540Z v.2	20010921	Product data sheet	-	PBSS5540Z v.1
PBSS5540Z v.1	20010126	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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