

# KA1L0380B/KA1L0380RB/ KA1M0380RB/KA1H0380RB

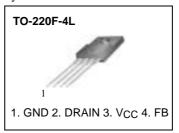
# Fairchild Power Switch(FPS)

#### **Features**

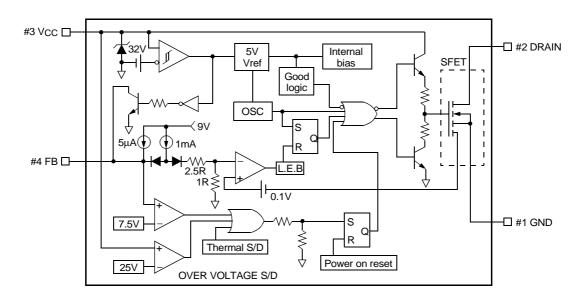
- Precision Fixed Operating Frequency
- KA1L0380B/KA1L0380RB (50kHz)
- KA1M0380RB (67kHz)
- KA1H0380RB (100kHz)
- Pulse by Pulse Over Current Limiting
- · Over Load Protection
- Over Voltage Protection (Min. 23V)
- Internal Thermal Shutdown Function
- Under Voltage Lockout
- Internal High Voltage Sense FET
- Auto Restart Mode (KA1L0380RB/KA1M0380RB/ KA1H0380RB)
- Latch Mode (KA1L0380B)

### **Description**

The Fairchild Power Switch(FPS) product family is specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM controller IC. PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, temperature compensated precision current sources for loop compensation and fault protection circuit compared to discrete MOSFET and controller or RCC switching converter solution, The Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase & efficiency, productivity, and system reliability. It has a basic platform well suited for cost effective design in either a flyback converter or a forward converter.



### **Internal Block Diagram**



# **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Maximum Drain Voltage (1)	V <sub>D,MAX</sub>	800	V
Drain-Gate Voltage (RGS=1MΩ)	VDGR	800	V
Gate-Source (GND) Voltage	Vgs	±30	V
Drain Current Pulsed (2)	IDM	12	ADC
Single Pulsed Avalanche Energy (3)	Eas	95	mJ
Avalanche Current <sup>(4)</sup>	IAS	10	Α
Continuous Drain Current (Tc=25°C)	ID	3.0	ADC
Continuous Drain Current (T <sub>C</sub> =100°C)	ID	2.1	ADC
Maximum Supply Voltage	VCC,MAX	30	V
Input Voltage Range	VFB	-0.3 to VSD	V
Total Power Dissipation	PD	35	W
	Darting	0.28	W/°C
Operating Ambient Temperature	TA	-25 to +85	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

### Notes:

- 1.  $T_j = 25^{\circ}C$  to  $150^{\circ}C$
- 2. Repetitive rating: Pulse width limited by maximum junction temperature
- 3. L = 51mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>j</sub> = 25 $^{\circ}$ C
- 4. L = 13 $\mu H$ , starting  $T_j = 25^{\circ} C$

# **Electrical Characteristics (SFET part)**

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	800	-	-	V
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =Max., Rating, V <sub>GS</sub> =0V	-	-	50	μА
		V <sub>DS</sub> =0.8Max., Rating, V <sub>GS</sub> =0V, T <sub>C</sub> =125°C	-	-	200	μА
Static Drain-Source on Resistance (Note)	RDS(ON)	VGS=10V, ID=1.5A	-	4.0	5.0	Ω
Forward Transconductance (Note)	gfs	V <sub>DS</sub> =15V, I <sub>D</sub> =1.5A	1.5	2.5	-	S
Input Capacitance	Ciss		-	779	-	
Output Capacitance	Coss	VGS=0V, VDS=25V, f=1MHz	-	75.6	-	pF
Reverse Transfer Capacitance	Crss	1-11/11/2	-	24.9	-	
Turn on Delay Time	td(on)	V <sub>DD</sub> =0.5B V <sub>DSS</sub> , I <sub>D</sub> =3.0A (MOSFET switching time is essentially independent of	-	40	-	
Rise Time	tr		-	95	-	nS
Turn Off Delay Time	td(off)		-	150	-	113
Fall Time	tf	operating temperature)	-	60	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=3.0A, VDS=0.5B VDSS (MOSFET switching time is essentially independent of operating temperature)	-	-	34	
Gate-Source Charge	Qgs		-	7.2	-	nC
Gate-Drain (Miller) Charge	Qgd		-	12.1	-	

#### Note:

1. Pulse test: Pulse width  $\leq 300 \mu S$ , duty cycle  $\leq 2\%$ 

$$2. S = \frac{1}{R}$$

### **Electrical Characteristics (Control Part)** (Continued)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	
UVLO SECTION	UVLO SECTION						
Start Threshold Voltage	VSTART	-	14	15	16	V	
Stop Threshold Voltage	VSTOP	After turn on	9	10	11	V	
OSCILLATOR SECTION						•	
Initial Accuracy	Fosc	KA1L0380B	45	50	55	kHz	
		KA1L0380RB	45	50	55		
		KA1M0380RB	61	67	73		
		KA1H0380RB	90	100	110		
Frequency Change With Temperature (2)	ΔF/ΔΤ	-25°C ≤ Ta ≤ +85°C	-	±5	±10	%	
		KA1L0380B	74	77	80		
Marianum Dutu Cuala	Dmax	KA1L0380RB	74	77	80	%	
Maximum Duty Cycle	Dillax	KA1M0380RB	74	77	80		
		KA1H0380RB	64	67	70		
FEEDBACK SECTION							
Feedback Source Current	IFB	Ta=25°C, $0V \le Vfb \le 3V$	0.7	0.9	1.1	mA	
Shutdown Feedback Voltage	VsD	-	6.9	7.5	8.1	V	
Shutdown Delay Current	Idelay	Ta=25°C, 5V ≤ Vfb ≤ V <sub>SD</sub>	4.0	5.0	6.0	μΑ	
REFERENCE SECTION							
Output Voltage (1)	Vref	Ta=25°C	4.80	5.00	5.20	V	
Temperature Stability (1)(2)	Vref/∆T	-25°C ≤ Ta ≤ +85°C	-	0.3	0.6	mV/°C	
CURRENT LIMIT (SELF-PROTECTION)	SECTION						
Peak Current Limit	IOVER	Max. inductor current	1.89	2.15	2.41	Α	
PROTECTION SECTION							
Thermal Shutdown Temperature (1)	T <sub>SD</sub>	-	140	160	-	°C	
Over Voltage Protection Voltage	Vovp	-	23	25	28	V	
TOTAL DEVICE SECTION							
Start-Up Current	ISTART	VCC=14V	0.1	0.3	0.45	mA	
Operating Supply Current (Control Part Only)	IOP	Ta=25°C	6	12	18	mA	
V <sub>CC</sub> Zener Voltage	Vz	ICC=20mA	30	32.5	35	V	

#### Note:

- 1. These parameters, although guaranteed, are not 100% tested in production
- 2. These parameters, although guaranteed, are tested in EDS (wafer test) process

### **Typical Performance Characteristics**

(These characteristic graphs are normalized at Ta=25°C)

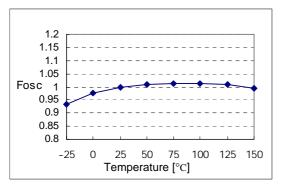


Figure 1. Operating Frequency

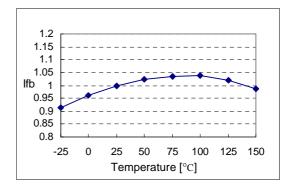
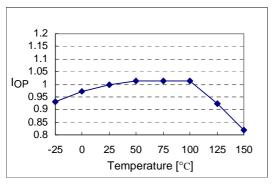


Figure 2. Feedback Source Current



**Figure 3. Operating Supply Current** 

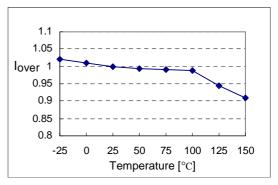


Figure 4. Peak Current Limit

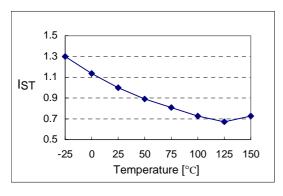


Figure 5. Start up Current

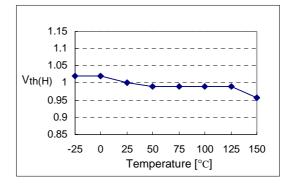


Figure 6. Start Threshold Voltage

### **Typical Performance Characteristics** (Continued)

(These characteristic graphs are normalized at Ta=25°C)

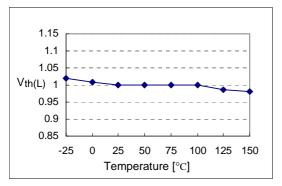


Figure 7. Stop Threshold Voltage

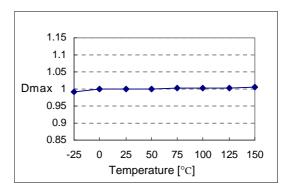


Figure 8. Maximum Duty Cycle

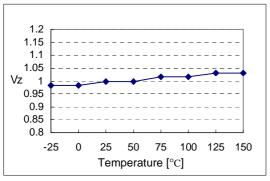


Figure 9. VCC Zener Voltage

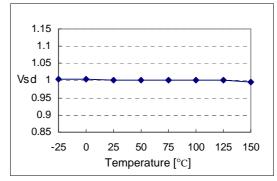


Figure 10. Shutdown Feedback Voltage

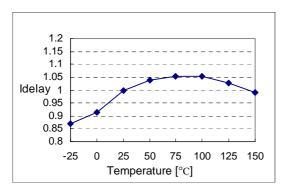


Figure 11. Shutdown Delay Current

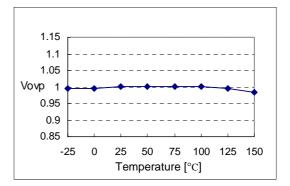


Figure 12. Over Voltage Protection

# **Typical Performance Characteristics** (Continued)

(These characteristic grahps are normalized at Ta=25 $^{\circ}$ C)

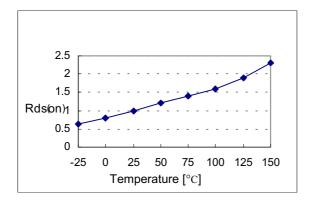
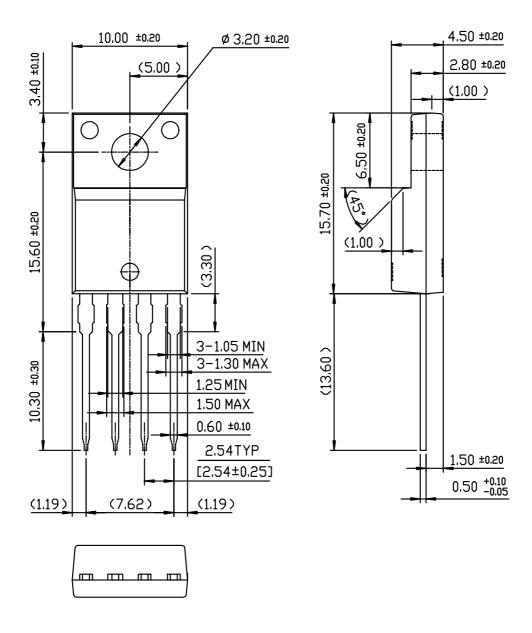


Figure 13. Static Drain-Source on Resistance

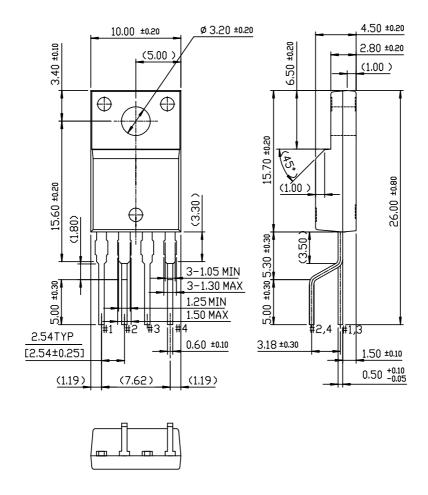
## **Package Dimensions**

# TO-220F-4L



## Package Dimensions (Continued)

# TO-220F-4L(Forming)



### **Ordering Information**

Product Number	Package	Rating	Fosc		
KA1L0380B-TU	TO-220F-4L	800V, 3A	50kHz		
KA1L0380B-YDTU	TO-220F-4L(Forming)	600 V, 3A	JUKHZ		
KA1L0380RB-TU	TO-220F-4L	800V, 3A	50kHz		
KA1L0380RB-YDTU	TO-220F-4L(Forming)	600 V, 3A	JUKIIZ		
KA1M0380RB-TU	TO-220F-4L	800V, 3A	67kHz		
KA1M0380RB-YDTU	TO-220F-4L(Forming)	600 V, 3A	U/KHZ		
KA1H0380RB-TU	TO-220F-4L	800V, 3A	100kHz		
KA1H0380RB-YDTU	TO-220F-4L(Forming)	000 V, 3A	TOURHZ		

TU : Non Forming Type YDTU : Forming Type

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- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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