



600 V, 0.5 A NPN high-voltage low VCEsat (BISS) transistor 7 September 2020 Product data sheet

1. General description

NPN high-voltage low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV9560Z

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability
- + High collector current gain h_{FE} at high I_C
- AEC-Q101 qualified

3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- LCD backlighting
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

4. Quick reference data

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Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	600	V
I _C	collector current			-	-	0.5	А
h _{FE}	DC current gain	V_{CE} = 10 V; I _C = 50 mA; T _{amb} = 25 °C		70	135	-	



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	2, 4
2	С	collector		
3	E	emitter		
4	С	collector		3
			SC-73 (SOT223)	sym016

6. Ordering information

Table 3. Ordering information						
Type number	pe number Package					
	Name	Description	Version			
PBHV8560Z	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223			

7. Marking

Table 4. Marking codes				
Type number	Marking code			
PBHV8560Z	HV856Z			

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8. Limiting values

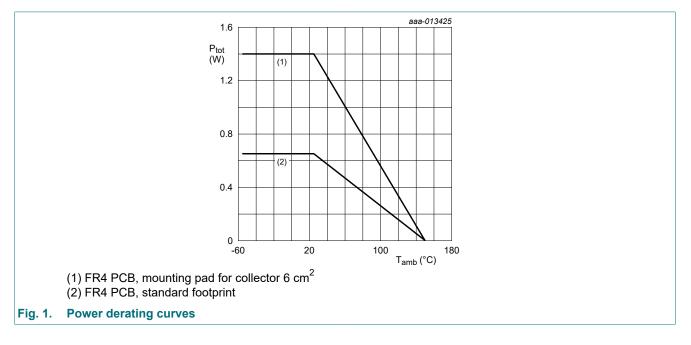
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	600	V
V _{CEO}	collector-emitter voltage	open base		-	600	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	600	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	0.5	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.4	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

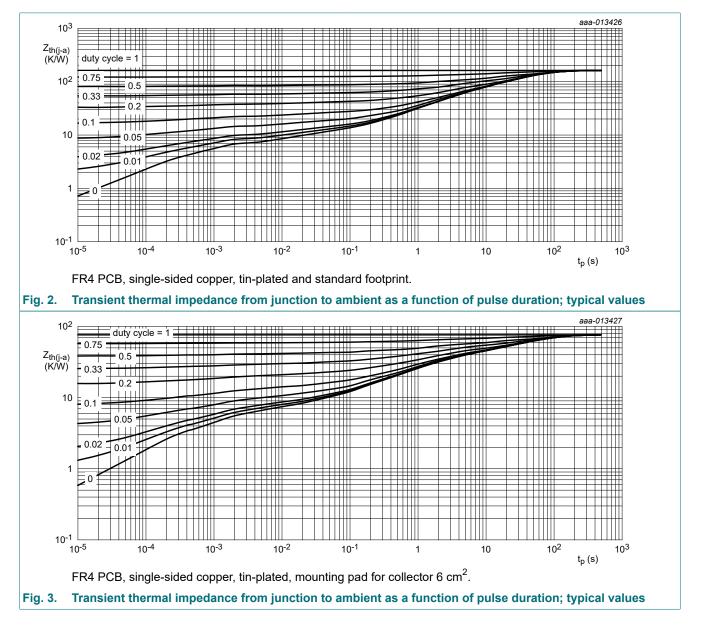


9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)} thermal resistance from junction to ambient		ermal resistance from in free air	[1]	-	-	190	K/W
		[2]	-	-	89	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

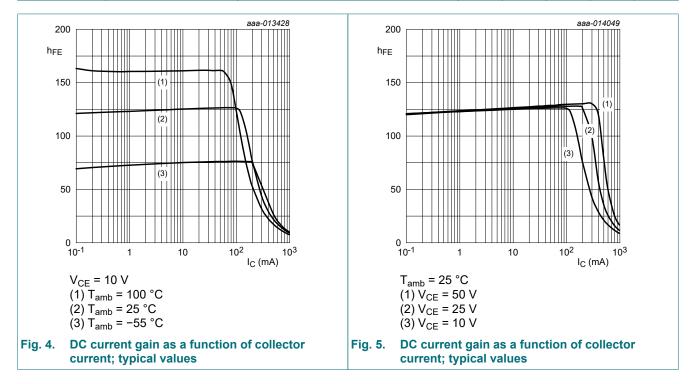
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



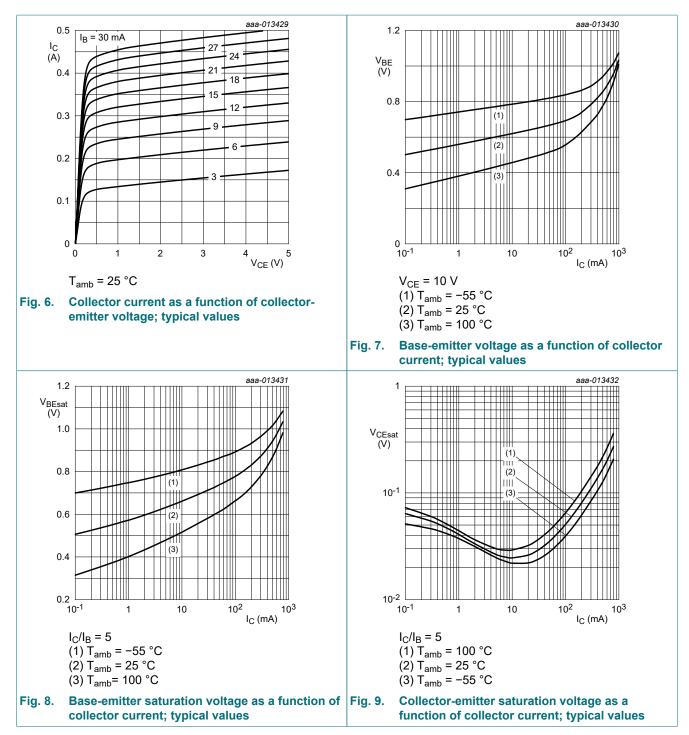
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 400 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 400 V; I _E = 0 A; T _j = 150 °C	-	-	10	μA
I _{CES}	collector-emitter cut-off current	V _{CE} = 400 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 50 mA; T _{amb} = 25 °C	70	135	-	
			70	135	-	
V _{CEsat}	collector-emitter	I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C	-	50	100	mV
	saturation voltage	I_{C} = 100 mA; I_{B} = 20 mA; $t_{p} \le 300 \ \mu$ s; $\delta \le 0.02$; T_{amb} = 25 °C; pulsed	-	50	100	mV
V _{BEsat}	base-emitter saturation voltage	$\label{eq:lc} \begin{array}{l} I_{C} = 50 \text{ mA}; \ I_{B} = 5 \text{ mA}; \ t_{p} \leq \ 300 \ \mu \text{s}; \\ pulsed; \ \delta \leq \ 0.02; \ T_{amb} = 25 \ ^\circ \text{C} \end{array}$	-	-	950	mV
C _c	collector capacitance	V _{CB} = 20 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	7.5	-	pF
C _e	emitter capacitance	V _{EB} = 0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	710	-	pF



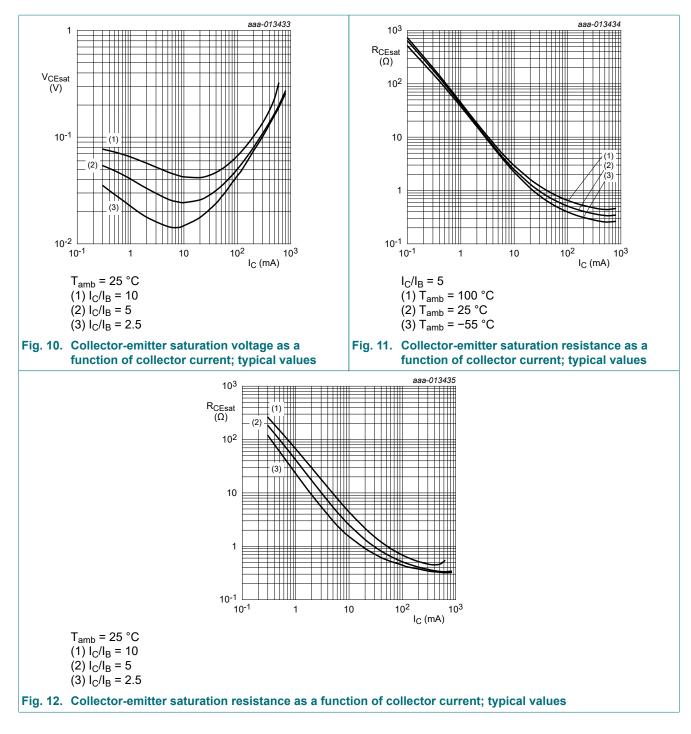
PBHV8560Z

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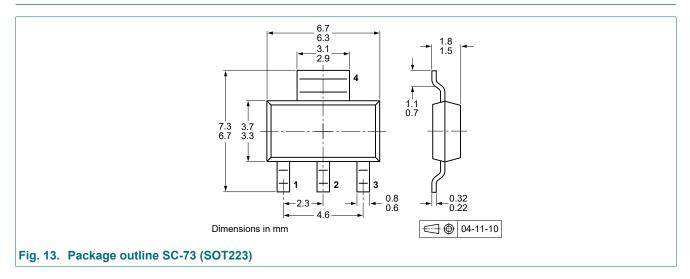
PBHV8560Z

11. Test information

Quality information

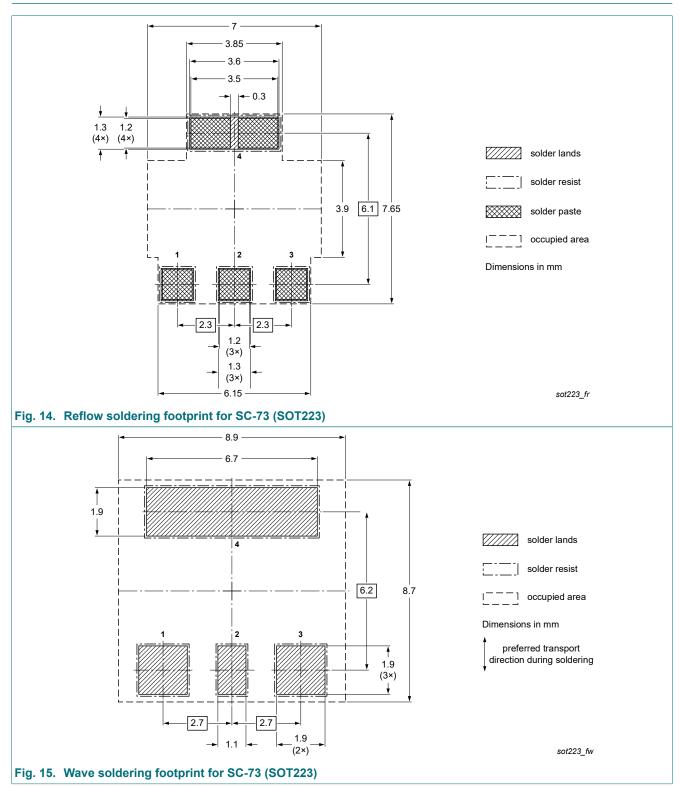
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PBHV8560Z v.2	20200907	Product data sheet	-	PBHV8560Z v.1		
Modifications:	Characteristics: Legend corrected at Figure 5					
PBHV8560Z v.1	20150313	Product data sheet	-	-		

PBHV8560Z

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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