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September 2014

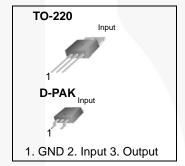
KA79MXX / LM79MXX 3-Terminal 0.5 A Negative Voltage Regulator

Features

- · No External Components Required
- Output Current in Excess of 0.5 A
- · Internal Thermal Overload
- Internal Short-Circuit Current Limiting
- Output Transistor Safe Area Compensation
- Output Voltages: -5 V, -12 V

Description

The KA79MXX / LM79MXX series of three terminal medium current negative voltage regulators are monolithic integrated circuits designed as fixed-voltage regulators. These regulators employ internal current limiting, thermal shutdown, and safe area compensation.

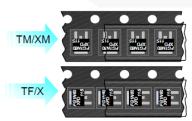


Ordering Information(1)

Product Number	Package	Packing Method	Operating Temperature	
KA79M05TU	TO-220 (Dual Gauge)	Rail		
KA79M05RTM		Tape and Reel 0 to +125°C		
KA79M05RTF	D-PAK		0 to 1125°C	
KA79M12RTM	D-PAK		0 t0 +125 C	
KA79M12RTF				
LM79M05CT	TO-220 (Single Gauge)	Rail		

Note:

1. Refer to below figure for TM / TF suffix of DPAK packing option.



D-PAK Unit Orientation

Block Diagram

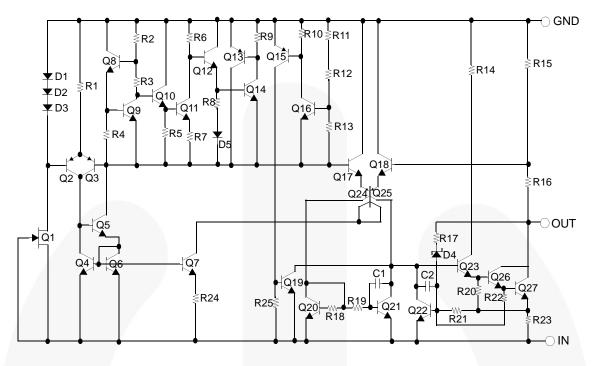


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Unit	
V _I	Input Voltage $V_O = -5 \text{ V to } -12$		-35	V
$R_{\theta JC}$	Thermal Resistance, Junction-Case TO-220		5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-Air TO-220		65	°C/W
T _{OPR}	Operating Temperature Range		0 to +125	°C
T _{STG}	Storage Temperature Range	-65 to +150	°C	

Electrical Characteristics (KA79M05 / KA79M05R / LM79M05)

Refer to test circuit, $0^{\circ}C \le T_{J} \le +125^{\circ}C$, $I_{O} = 350$ mA, $V_{I} = -10$ V, $C_{I} = 0.33$ μ F, $C_{O} = 0.1$ μ F unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
V	Output Voltage	T _J = +25°C		-4.80	-5.00	-5.20	V
V _O	Output voltage	$I_0 = 5 \text{ mA to } 350 \text{ mA}, V_1 = -7 \text{ V to } -25 \text{ V}$		-4.75	-5.00	-5.25	
41/	Line Regulation ⁽²⁾	T -125°C	$V_1 = -7 \text{ V to } -25 \text{ V}$		7	50	mV
ΔV_{O}	Line Regulation 7	T _J =+25°C	$V_1 = -8 \text{ V to } -25 \text{ V}$		2	30	1 1110
ΔV _O	Load Regulation ⁽²⁾	$I_{O} = 5 \text{ mA to } 500 \text{ mA}, T_{J} = +25^{\circ}\text{C}$			30	100	mV
IQ	Quiescent Current	T _J = +25°C			3.0	6.0	mA
Al	Quiacont Current Change	$I_{O} = 5 \text{ mA to } 350 \text{ m}$	mA			0.4	mA
ΔI_{Q}	Quiescent Current Change	$I_0 = 200 \text{ mA}, V_1 =$	-8 V to -25 V			0.4	IIIA
ΔVο/ΔΤ	Output Voltage Drift	$I_O = 5 \text{ mA}$			-0.2		mV/°C
V _N	Output Noise Voltage f = 10 Hz to 100 kF		Hz, T _A = +25°C		40		μV
RR	Ripple Rejection	e Rejection $f = 120 \text{ Hz}, V_J = -8 \text{ V to } -18 \text{ V}$		54	60		dB
V _D	Dropout Voltage	$T_J = +25^{\circ}C$, $I_O = 500 \text{ mA}$			1.1		V
I _{SC}	Short-Circuit Current	$T_J = +25^{\circ}C, V_I = -35 \text{ V}$			140		mA
I _{PK}	Peak Current	$T_J = +25^{\circ}C$			650		mA

Note:

2. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

© 2001 Fairchild Semiconductor Corporation KA79MXX / LM79MXX Rev. 1.1.1

Electrical Characteristics (KA79M12R)

Refer to test circuit, $0^{\circ}C \leq T_{J} \leq$ +125°C, I_{O} = 350 mA, V_{I} = -19 V, C_{I} = 0.33 μ F, C_{O} = 0.1 μ F unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit
		T _J = +25°C		-11.5	-12.0	-12.5	V
V _O	Output Voltage	I _O = 5 mA to 350 mA, V _I = -14.5 V to -30 V		-11.4	-12.0	-12.6	
4)/	Line Regulation ⁽³⁾	T _J =+25°C	$V_I = -14.5 \text{ V to } -30 \text{ V}$		8.0	80	mV
ΔV_{O}	Line Regulation(*)		V _I = -15 V to -25 V		3.0	50	
ΔV_{O}	Load Regulation ⁽³⁾	T _J = +25°C	$I_{O} = 5.0 \text{ mA to } 500 \text{ mA}$		30	240	mV
IQ	Quiescent Current	$T_J = +25^{\circ}C$			3	6	mA
Al	Quiescent Current Change	$I_O = 5 \text{ mA to}$	o 350 mA			0.4	
ΔI_{Q}		V _I = -14.5 V	′ to -30 V			0.4	mA
$\Delta V_O/\Delta T$	Output Voltage Drift	I _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage $f = 10 \text{ Hz}$ to 100 kHz , $T_A = +25^{\circ}\text{C}$			75		μV	
RR	Ripple Rejection	f = 120 Hz, V _I = -15 V to -25 V		54	60		dB
V _D	Dropout Voltage	$I_{O} = 500 \text{ mA}, T_{J} = +25^{\circ}\text{C}$			1.1		V
I _{SC}	Short Circuit Current	V _I = -35 V, T _J = +25°C			140		mA
I _{PK}	Peak Current	T _J = +25°C			650		mA

Note:

3. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Typical Performance Characteristics

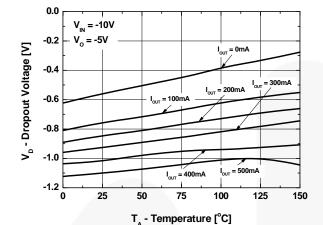


Figure 2. Dropout Voltage

Typical Applications

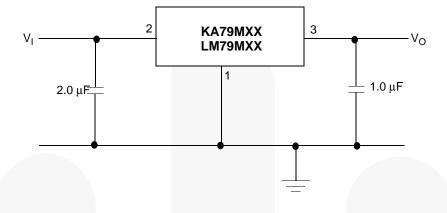


Figure 3. Fixed Output Regulator

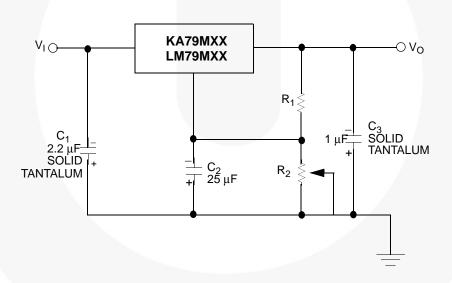
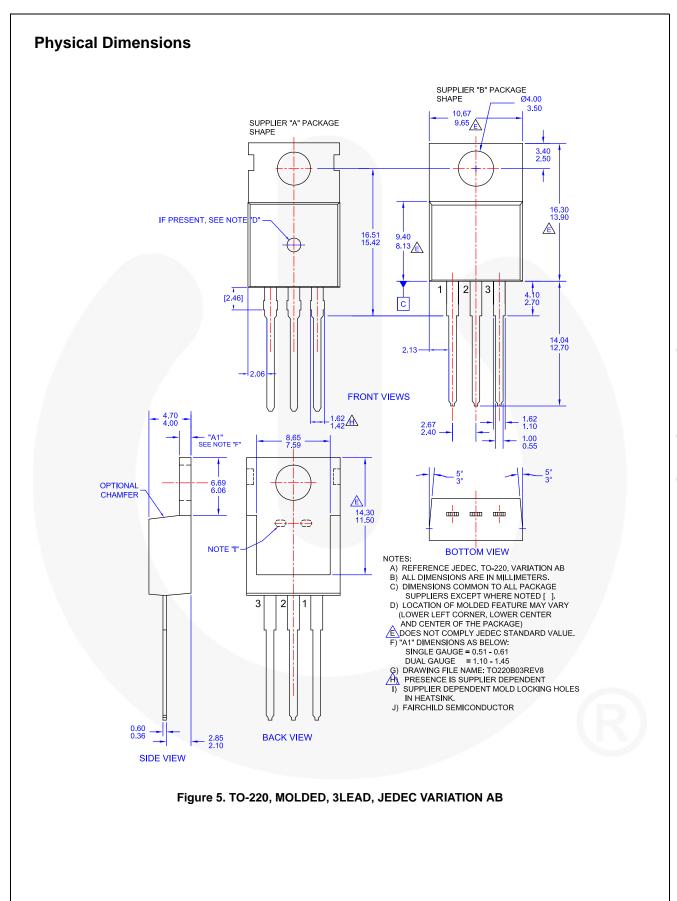


Figure 4. Variable Output

Notes:

- 4. To specify an output voltage, substitute voltage value for "XX".
- 5. C_l is required if the regulator is located an appreciable distance from the power supply filter. For value given, capacitor must be solid tantalum. If aluminium electronics are used, 25 μ F aluminum electrolytic may be substituted.
- 6. C_2 improves transient response and ripple rejection. Do not increase beyond 50 μF .



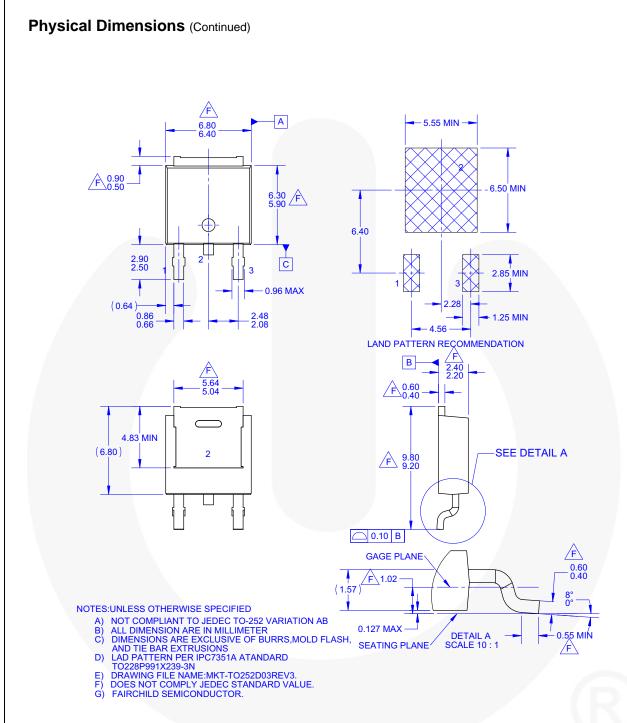


Figure 6. 3-LEAD, TO-252, JEDEC TO-252 VAR. AB, SURFACE MOUNT (DPAK)





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