

65 V, 100 mA PNP/PNP general-purpose double transistor 6 May 2021 Product data sheet

### 1. General description

PNP/PNP general-purpose double transistor in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

NPN/NPN complement: BC846BSH-Q

NPN/PNP complement: BC846BPNH-Q

## 2. Features and benefits

- Low collector capacitance
- Low collector-emitter saturation voltage
- Closely matched current gain
- · Reduces number of components and board space
- No mutual interference between the transistors
- High-temperature applications up to 175 °C
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

· General-purpose switching and amplification

### 4. Quick reference data

Table 1. Qui	ck reference data		1				
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor	·					
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-65	V
I <sub>C</sub>	collector current			-	-	-100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; I <sub>C</sub> = -2 mA; T <sub>amb</sub> = 25 °C		200	300	450	



## 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	□6 □5 □4	C1 B2 E2
2	B1	base TR1		
3	C2	collector TR2	0	
4	E2	emitter TR2		
5	B2	base TR2	TSSOP6 (SOT363)	E1 B1 C2
6	C1	collector TR1		sym138

## 6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
BC856BSH-Q		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363				

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
BC856BSH-Q	2E%

[1] % = placeholder for manufacturing site code

## 8. Limiting values

### Table 5. Limiting values

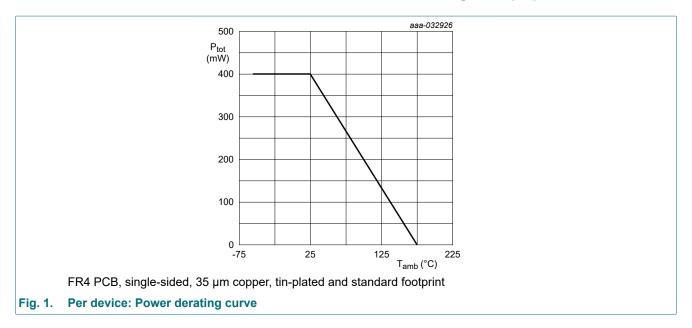
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
Per transiste	or	1	1			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-65	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-7	V
I <sub>C</sub>	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	270	mW
Per device		,	1		-	
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	400	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.

BC856BSH-Q

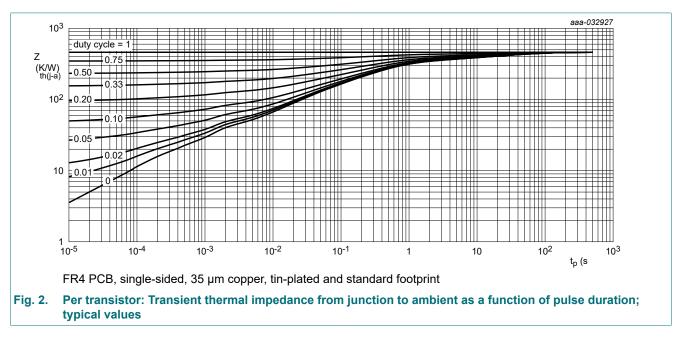
### 65 V, 100 mA PNP/PNP general-purpose double transistor



### 9. Thermal characteristics

Table 6. Ther	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transiste	or		ľ				
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	556	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	170	K/W
Per device			,				
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	375	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

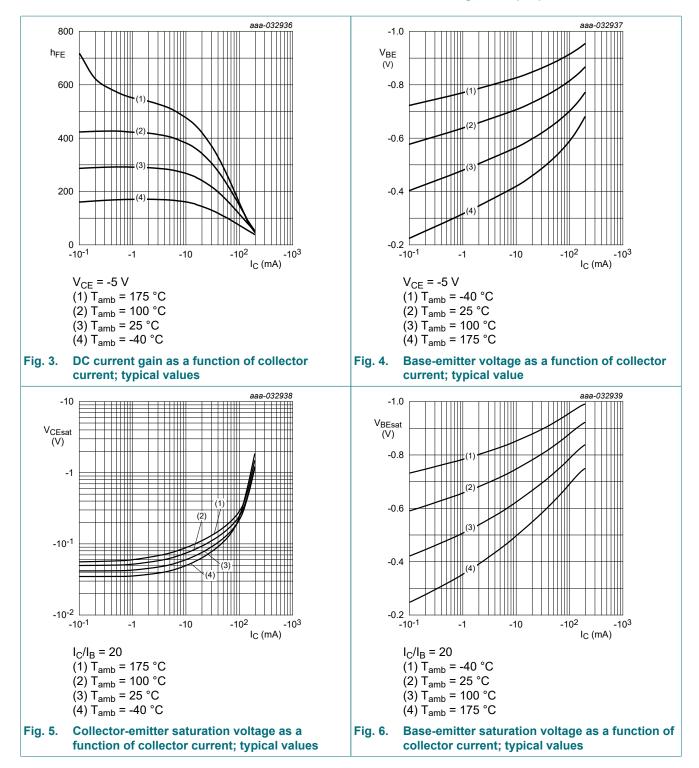


## **10. Characteristics**

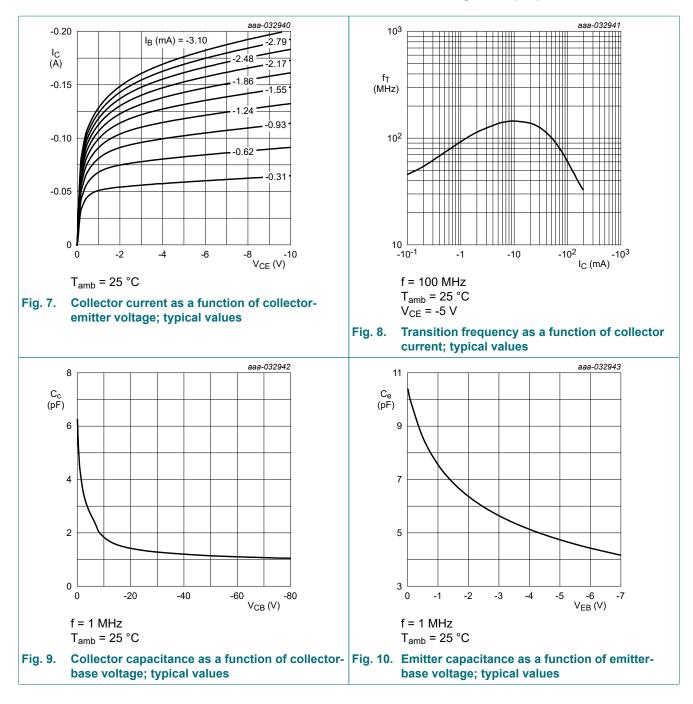
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or		1				
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_{C}$ = -100 µA; $I_{E}$ = 0 A; $T_{amb}$ = 25 °C		-80	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = -2 mA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-65	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>C</sub> = 0 A; I <sub>E</sub> = -100 μA; T <sub>amb</sub> = 25 °C		-7	-	-	V
000	collector-base cut-off	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-15	nA
	current	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -7 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; I <sub>C</sub> = -2 mA; T <sub>amb</sub> = 25 °C		200	300	450	
V <sub>CEsat</sub>	CEsat collector-emitter saturation voltage	$I_{C}$ = -10 mA; $I_{B}$ = -0.5 mA; $T_{amb}$ = 25 °C		-	-50	-100	mV
		$I_C$ = -100 mA; $I_B$ = -5 mA; pulsed; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C		-	-200	-300	mV
DESat	base-emitter saturation	$I_{C}$ = -10 mA; $I_{B}$ = -0.5 mA; $T_{amb}$ = 25 °C	[1]	-	-750	-850	mV
	voltage	$I_{C}$ = -100 mA; $I_{B}$ = -5 mA; $T_{amb}$ = 25 °C		-	-875	-	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -2 mA; T <sub>amb</sub> = 25 °C	[2]	-600	-655	-700	mV
		$V_{CE}$ = -5 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	[2]	-	-705	-770	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	1.8	-	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = -0.5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	8.5	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		100	-	-	MHz
NF	noise figure	$V_{CE}$ = -5 V; I <sub>C</sub> = -0.2 mA; R <sub>S</sub> = 2 kΩ; f = 10 Hz to 15.7 kHz; T <sub>amb</sub> = 25 °C		-	1.7	-	dB
		V <sub>CE</sub> = -5 V; I <sub>C</sub> = -0.2 mA; R <sub>S</sub> = 2 kΩ; f = 1 kHz; B = 200 Hz; T <sub>amb</sub> = 25 °C		-	3.3	-	dB

 $V_{BEsat}$  decreases by about 1.7 mV/K with increasing temperature.  $V_{BE}$  decreases by about 2 mV/K with increasing temperature. [1] [2]

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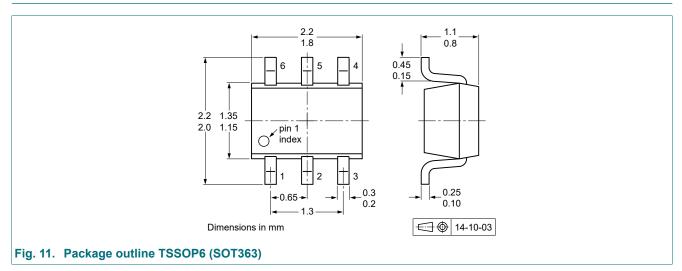


### **11. Test information**

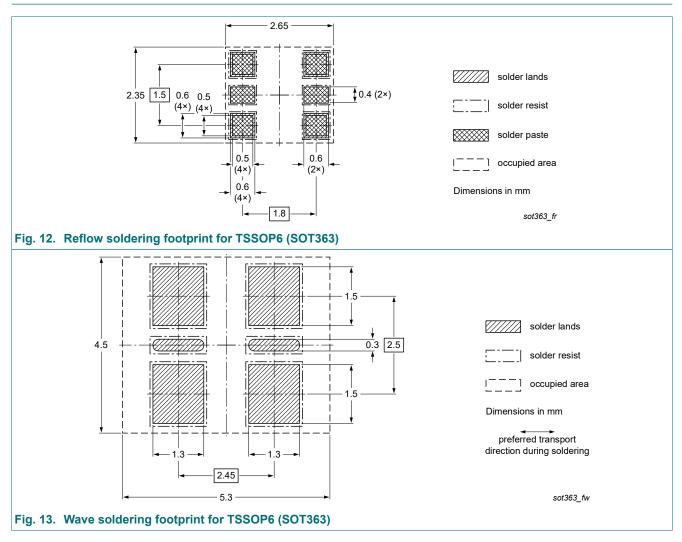
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



## 13. Soldering



BC856BSH-Q

## 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BC856BSH-Q v.1	20210506	Product data sheet	-	-		

BC856BSH-Q

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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