

45 V, 500 mA NPN general-purpose transistors Rev. 1 — 25 January 2022

Product data sheet

1. General description

NPN general-purpose transistor in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview						
Type number Package				PNP complement		
	Name	JEDEC	Version			
BC817-16QBH-Q	DFN1110D-3	MO340-BA	SOT8015	BC807-16QBH-Q		
BC817-25QBH-Q				BC807-25QBH-Q		
BC817-40QBH-Q				BC807-40QBH-Q		

2. Features and benefits

- High power dissipation capability •
- High current
- Three current gain selections
- Suitable for Automatic Optical Inspection (AOI) of solder joint •
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm •
- High-temperature applications up to 175 °C
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- General-purpose switching and amplification
- Space restricted applications

4. Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base; T _{amb} = 25 °C	-	-	45	V
I _C	collector current	T _{amb} = 25 °C	-	-	500	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms; T _{amb} = 25 °C	-	-	1	А
h _{FE}	DC current gain	·				
	BC817-16QBH-Q	$V_{CE} = 1 \text{ V}; \text{ I}_{C} = 100 \text{ mA } \text{T}_{amb} = 25 \text{ °C}$ [1]	100	-	250	
	BC817-25QBH-Q		160	-	400	
	BC817-40QBH-Q		250	-	600	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

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5. Pinning information

Table 3. Pinning				
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	E	emitter	3	
3	С	collector		B f
				É sym021
			Transparent top view DFN1110D-3 (SOT8015)	

6. Ordering information

Table 4. Ordering information

Type number	Package	'ackage						
	Name	Description	Version					
BC817-16QBH-Q	DFN1110D-3	DFN1110D-3: plastic thermal enhanced ultra thin small outline	SOT8015					
BC817-25QBH-Q		package; no leads; 3 terminals; body: 1.1 x 1.0 x 0.5 mm	(MO340-BA)					
BC817-40QBH-Q								

7. Marking

Table 5. Marking

Type number	Marking code
BC817-16QBH-Q	F9
BC817-25QBH-Q	G2
BC817-40QBH-Q	G3

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8. Limiting values

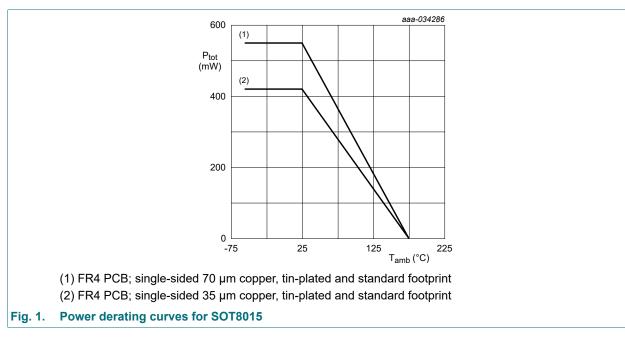
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Conditions		Мах	Unit
V _{CBO}	collector-base voltage	open emitter; T _{amb} = 25 °C	open emitter; T _{amb} = 25 °C		50	V
V _{CEO}	collector-emitter voltage	open base; T _{amb} = 25 °C		-	45	V
V _{EBO}	emitter-base voltage	open collector; T _{amb} = 25 °C	open collector; T _{amb} = 25 °C		5	V
I _C	collector current	T _{amb} = 25 °C	T _{amb} = 25 °C		500	mA
I _{CM}	peak collector current	single pulse; $t_p \le 1$ ms; $T_{amb} = 25$ °C		-	1	А
I _{BM}	peak base current	single pulse; $t_p \le 1 \text{ ms}$; $T_{amb} = 2$	single pulse; t _p ≤ 1 ms; T _{amb} = 25 °C		200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	420	mW
			[2]	-	550	mW
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided 70 µm copper, tin-plated and standard footprint.



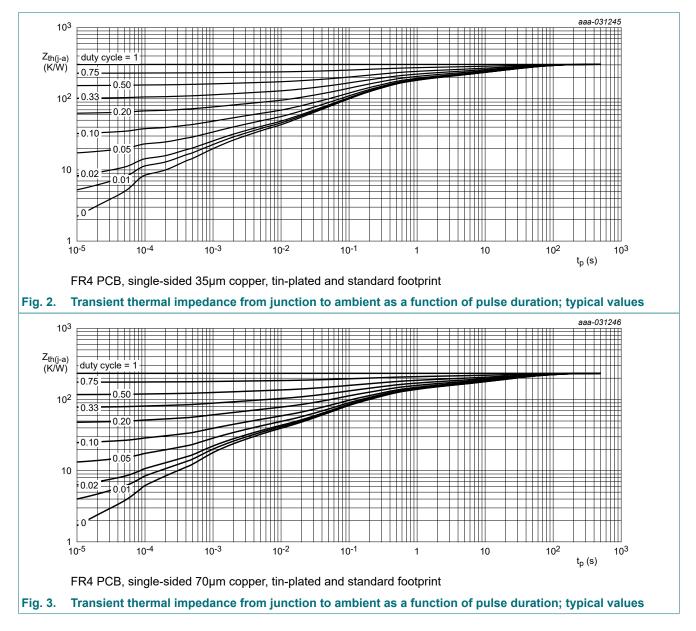
9. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to	in free air; $T_{amb} = 25 \text{ °C}$ [1]	-	-	358	K/W
	ambient	[2]	-	-	272	K/W

[1] Device mounted on an FR4 PCB, single-sided 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided 70 µm copper, tin-plated and standard footprint.



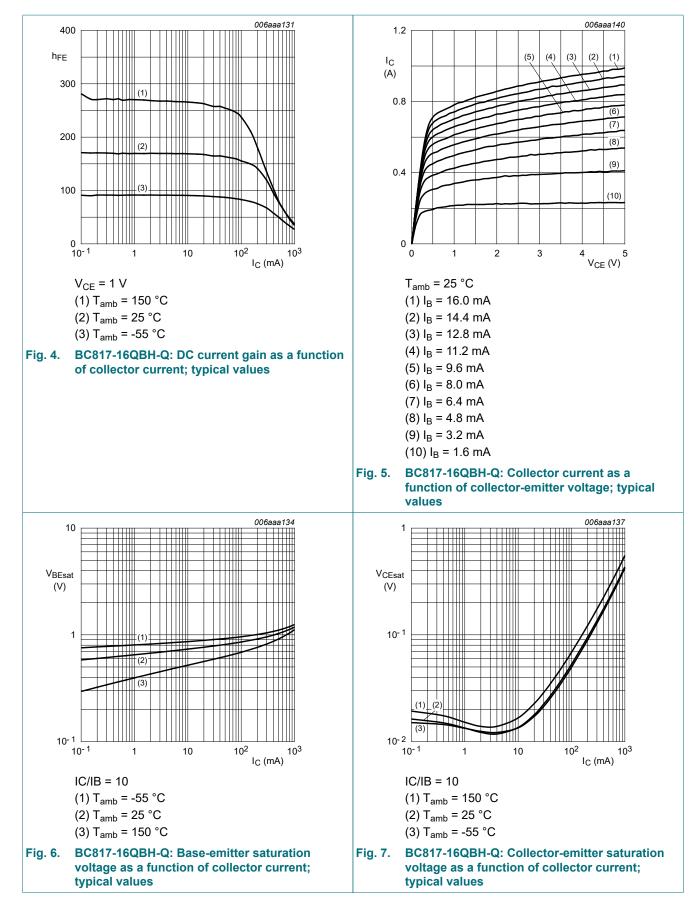
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10. Characteristics

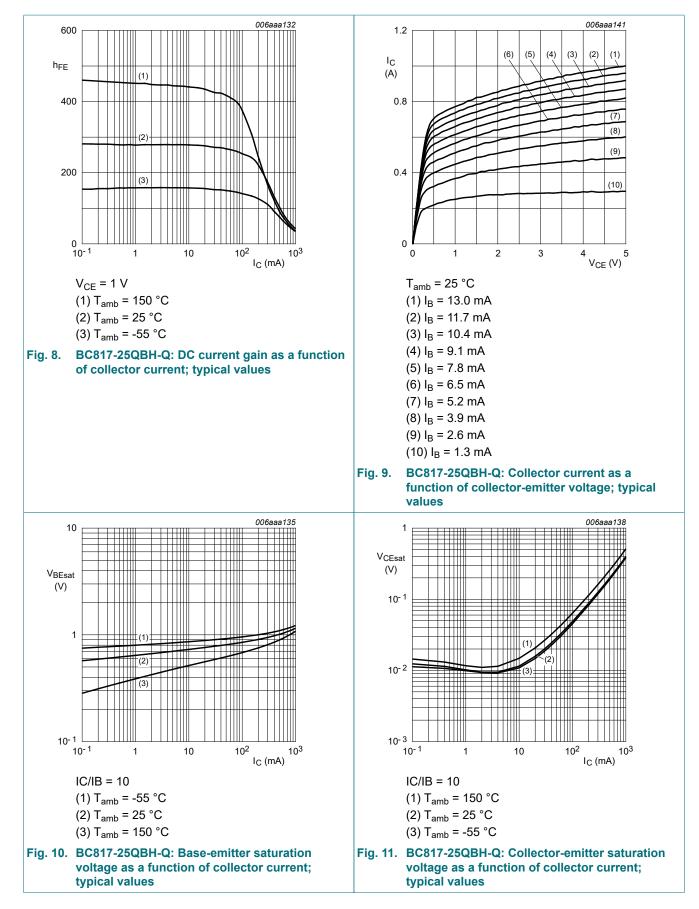
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C		50	-		V	
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 10 mA; I _E = 0 A; T _{amb} = 25 °C		45	-		V	
V _{(BR)EBO}	emitter-base breakdown voltage	I_E = 100 µA; I_C = 0 A; T_{amb} = 25 °C		5	-		V	
I _{CBO}	collector-base	V _{CB} = 20 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA	
	cut-off current	V _{CB} = 20 V; I _E = 0 A; T _j = 150 °C		-	-	5	μA	
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA	
h _{FE}	DC current gain							
	BC817-16QBH-Q	V _{CE} = 1 V; I _C = 100 mA; T _{amb} = 25 °C	[1]	100	-	250		
	BC817-25QBH-Q			160	-	400		
	BC817-40QBH-Q			250	-	600		
		V _{CE} = 1 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	40	-	-		
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA; T _{amb} = 25 °C	[1]	-	-	700	mV	
V _{BE}	base-emitter voltage	V_{CE} = 1 V; I _C = 500 mA; T _{amb} = 25 °C	[1] [2]	-	-	1.2	V	
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C		100	-	-	MHz	
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; \text{ I}_{E} = \text{i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$		-	3	-	pF	

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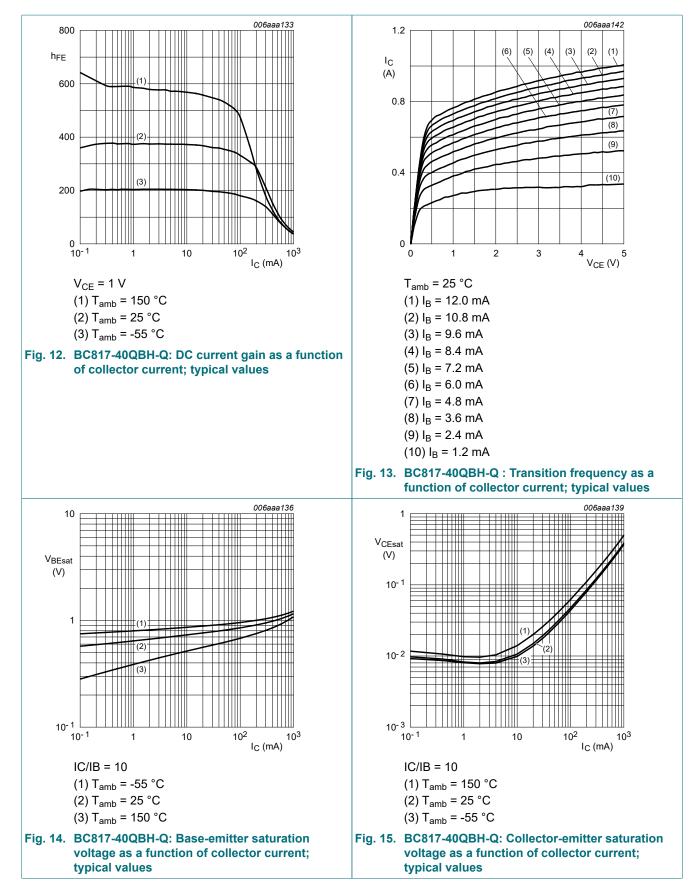


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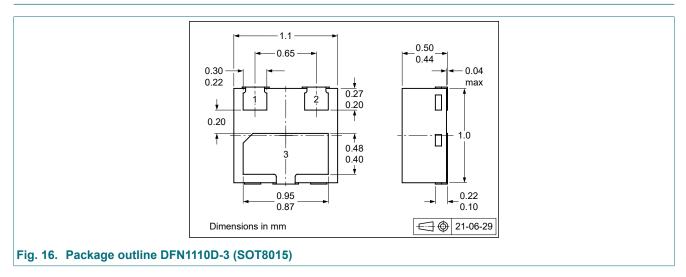


11. Test information

11.1. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

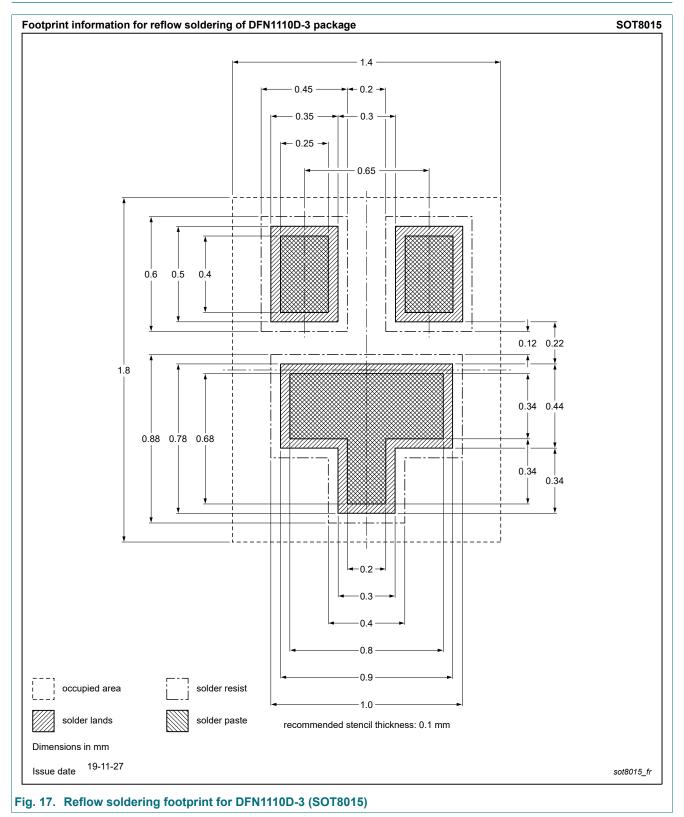
12. Package outline



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13. Soldering



14. Revision history

Table 9. Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BC817QBH-Q_SER v.1	20220125	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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