LV8773

Bi-CMOS LSI

PWM Constant-Current Control Stepping Motor Driver



http://onsemi.com

Overview

The LV8773 is a 2-channel H-bridge driver IC, which supports forward, reverse, brake, and standby of a motor. It is ideally suited for driving brushed DC motors and stepping motors used in office equipment and amusement applications.

Features

- BiCDMOS process IC
- Low on resistance (upper side : 0.3Ω ; lower side : 0.25Ω ; total of upper and lower : 0.55Ω ; Ta = 25° C, I_O = 2A)
- Motor current selectable in two steps
- Output short-circuit protection circuit (selectable from latch-type or auto-reset-type) incorporated
- Unusual condition warning output pins
- No control power supply required

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	VM max		36	V
Output peak current	IO peak	tw ≤ 10ms, duty 20%	2.5	Α
Output current	I _O max		2	Α
Logic input voltage	V _{IN}		-0.3 to +6	V
EMO1/EMO2 input voltage	Vemo/Vemo2		-0.3 to +6	V
Allowable power dissipation	Pd max1	1 unit	3.0	W
	Pd max2	*	6.2	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

^{*} Specified circuit board : 90.0mm×90.0mm×1.6mm, glass epoxy 2-layer board.

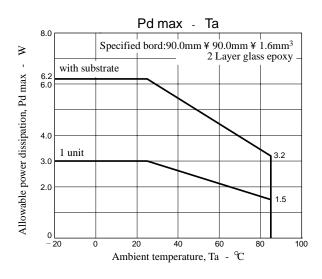
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Allowable Operating Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage range	VM		9 to 32	V
Logic input voltage	V _{IN}		0 to 5.5	V
VREF input voltage range	VREF		0 to 3	V

Electrical Characteristics at Ta = 25°C, VM = 24V, VREF = 1.5V

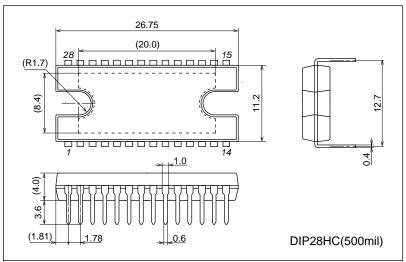
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Parameter	Symbol	Conditions	min	typ	max	Offic
Standby mode current drain	IMst	ST = "L"		100	400	μΑ
Current drain	IM	ST = "H", OE = "L", with no load		3.2	5	mA
VREG5 output voltage	Vreg5	I _O = -1mA	4.5	5	5.5	V
Thermal shutdown temperature	TSD	Design guarantee	150	180	200	°C
Thermal hysteresis width	ΔTSD	Design guarantee		40		°C
Motor driver						
Output on resistance	Ronu	I _O = 2A, Upper-side on resistance		0.3	0.4	Ω
	Rond	I _O = 2A, Lower-side on resistance		0.25	0.33	Ω
Output leakage current	l _O leak				50	μΑ
Diode forward voltage	VD	ID = -2A		1.2	1.4	V
Logic pin input current	I _{IN} L	V _{IN} = 0.8V	4	8	12	μΑ
	I _{IN} H	V _{IN} = 5V	30	50	70	μΑ
Logic high-level input voltage	V _{IN} H		2.0			V
Logic low-level input voltage	V _{IN} L				0.8	V
Current setting comparator	Vtatt0	ATT = L	0.291	0.3	0.309	V
threshold voltage (current attenuation rate switching)	Vtatt1	ATT = H	0.143	0.15	0.157	V
Chopping frequency	Fchop	Cchop = 220pF	36.3	45.4	54.5	kHz
CHOP pin charge/discharge current	Ichop		7	10	13	μΑ
Chopping oscillation circuit	Vtup		0.8	1	1.2	V
threshold voltage	Vtdown		0.4	0.5	0.6	V
VREF pin input current	Iref	VREF = 1.5V	-0.5			μΑ
Charge pump						
VG output voltage	VG		28	28.7	29.8	V
Rise time	tONG	VG = 0.1μF		200		μS
Oscillator frequency	Fosc		90	125	150	kHz
Output short-circuit protection	Output short-circuit protection					
EMO1/EMO2 pin saturation voltage	Vsatemo	Iemo = 1mA			400	mV
CEM pin charge current	Icem	Vcem = 0V	7	10	13	μΑ
CEM pin threshold voltage	Vtcem		0.8	1	1.2	V



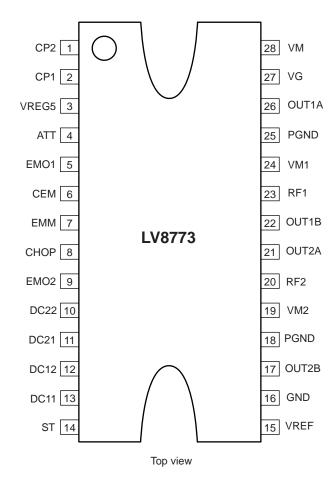
Package Dimensions

unit: mm (typ)

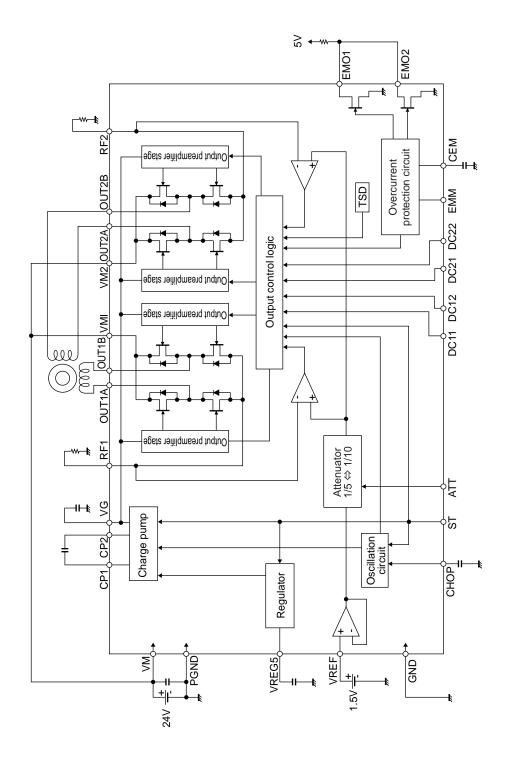
3241A



Pin Assignment



Block Diagram



Pin Functions

Pin Name Pin Fameton Equivalent Circuit	Pin Fu	inctions		T
SEMM Output short-circul protection mode switching by 1	Pin No.	Pin Name	Pin Function	Equivalent Circuit
Limit Court Cour	4	ATT2	Motor holding current switching pin.	VREG5 0
10 DG22 Channel 2 output control input pin 1 11 DG12 Channel 2 output control input pin 1 12 DG12 Channel 1 output control input pin 2 13 DG11 Channel 1 output control input pin 1 14 ST Chip enable pin. 17 OUT2B SBOKΩ 18 PGND 19 VMZ 20 RF2 Channel 2 output point pin 2 21 OUT2A Channel 2 output pin 2 22 RF2 Channel 2 output pin 2 23 RF1 Channel 2 output pin 2 24 VM1 Channel 2 output pin 2 25 QUT1A Channel 2 output pin 2 26 VM1 Channel 2 output pin 2 27 VM3 Channel 3 output pin 3 28 VM Channel 4 output pin 3 28 VM Channel 4 output pin 3 29 Channel 2 output pin 3 20 Channel 3 output pin 4 20 Channel 3 output pin 3 21 OUT2A Channel 2 output pin 3 22 OUT3A Channel 3 output pin 4 23 RF1 Channel 3 output pin 5 24 VM1 Channel 1 output pin 6 25 Channel 4 output pin 6 26 Change pump capacitor connection pin 6 27 CP1 Charge pump capacitor connection pin 6 28 VM Charge pump capacitor connection pin 6 29 Charge pump capacitor connection pin 6 20 CP1 Charge pump capacitor connection pin 6 26 CP1 Charge pump capacitor connection pin 7 27 CP1 Charge pump capacitor connection pin 6 28 VM Charge pump capacitor connection pin 6 29 CP1 Charge pump capacitor connection pin 6 20 CP1 Charge pump capacitor connection pin 6 20 CP1 Charge pump capacitor connection pin 6 20 CP1 Charge pump capacitor connection pin 7 28 VM Charge pump capacitor connection pin 6 29 CP1 Charge pump capacitor connection pin 7 20 CP1 CP1 Charge pump capacitor connection pin 7 20 CP1	7	EMM	Output short-circuit protection mode	VILEGO
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24 VM1 Channel 1 motor power supply pin. Channel 1 OUTA output pin. 27 VG 28 VM 1 CP2 CP1 CP1 Charge pump capacitor connection pin.				
24 VM1 OUT1A Channel 1 motor power supply pin. Channel 1 OUTA output pin. 27 VG Charge pump capacitor connection pin. Motor power supply connection pin. Charge pump capacitor connect	20			26 (22)
27 VG VM Motor power supply connection pin. Charge pump capacitor con	24	VM1		
27 VG 28 VM 1 CP2 CP1 Charge pump capacitor connection pin. GND VREG5 GND GND GND GND GND GND GND G				
27 VG 28 VM 1 CP2 CP1 Charge pump capacitor connection pin. GND VREG5 GND GND GND GND GND GND GND G				10/0
27 VG Charge pump capacitor connection pin. 28 VM Motor power supply connection pin. CP2 CP1 Charge pump capacitor connection pin. GND VREG5 GND GND GND GND GND GND GND G				10Ks2
27 VG Charge pump capacitor connection pin. 28 VM Motor power supply connection pin. CP2 CP1 Charge pump capacitor connection pin. GND VREG5 GND GND GND GND GND GND GND G				25(18) → 500Ω 500Ω
27 VG 28 VM 1 CP2 2 CP1 Charge pump capacitor connection pin. Charge pump capacitor connection pin. Charge pump capacitor connection pin.				
27 VG 28 VM 1 CP2 2 CP1 Charge pump capacitor connection pin. Charge pump capacitor connection pin. Charge pump capacitor connection pin.				
27 VG 28 VM 1 CP2 2 CP1 Charge pump capacitor connection pin. Charge pump capacitor connection pin. Charge pump capacitor connection pin.				GNDO
28 VM CP2 CP1 Motor power supply connection pin. Charge pump capacitor connection pin.				
VREG5 O Wotor power supply connection pin. Charge pump capacitor connection pin. Charge pump capacitor connection pin. Charge pump capacitor connection pin. GND O GND O				(2) (28) (1) (27)
2 CP1 Charge pump capacitor connection pin. Charge pump capacitor connection pin. GND O				
GND ○				→
		OFI	Charge pump capacitor connection pin.	
				\$100Q
				GND O
16 GND Ground.				
	16	GND	Ground.	

Continued on next page.

Continued	from preceding p		
Pin No.	Pin Name	Pin Function	Equivalent Circuit
15	VREF	Constant current control reference voltage input pin.	VREG5 0 500Ω GND 0
3	VREG5	Internal power supply capacitor connection pin.	VMO $2k\Omega$ $78k\Omega$ $26k\Omega$
5	EMO1	Channel 1 output short-circuit state	VREG5 O-
9	EMO2	warning output pin. Channel 2 output short-circuit state warning output pin.	GND O
6	CEM	Pin to connect the output short-circuit state detection time setting capacitor	VREG5 ○ 500Ω GND ○
8	CHOP	Copping frequency setting capacitor connection pin.	VREG5 Ο 500Ω \$ 500Ω

Description of operation

(1) Chip enable function

This IC is switched between standby and operating mode by setting the ST pin. In standby mode, the IC is set to power-save mode and all logic is reset. In addition, the internal regulator circuit and charge pump circuit do not operate in standby mode.

ST	Mode	Internal regulator	Charge pump
Low or Open	Standby mode	Standby	Standby
High	Operating mode	Operating	Operating

(2) Output control logic

inp	out	output			
DC11(21)	DC12(22)	OUT1(2)A	OUT1(2)B	mode	
L	L	OFF	OFF	Stand-by	
Н	L	Н	L	CW (Forward)	
L	Н	L	Н	CCW (reverse)	
Н	Н	L	L	brake	

(3) Blanking period

If, when exercising PWM constant-current chopping control over the motor current, the mode is switched from decay to charge, the recovery current of the parasitic diode may flow to the current sensing resistance, causing noise to be carried on the current sensing resistance pin, and this may result in erroneous detection. To prevent this erroneous detection, a blanking period is provided to prevent the noise occurring during mode switching from being received. During this period, the mode is not switched from charge to decay even if noise is carried on the current sensing resistance pin.

This IC is the blanking time is fixed at approximately 2µs.

(4) Chopping frequency setting

For constant-current control, this IC performs chopping operations at the frequency determined by the capacitor (Cchop) connected between the CHOP pin and GND.

The chopping frequency is set as shown below by the capacitor (Cchop) connected between the CHOP pin and GND.

Fchop = Ichop/ (Cchop
$$\times$$
 Vtchop \times 2) (Hz)

Ichop: Capacitor charge/discharge current, typ 10µA

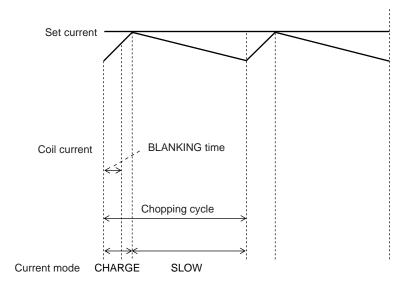
Vtchop: Charge/discharge hysteresis voltage (Vtup-Vtdown), typ 0.5V

For instance, when Cchop is 220pF, the chopping frequency will be as follows:

Fchop =
$$10\mu A/(220pF \times 0.5V \times 2) = 45.4kHz$$

(5) Setting constant-current control

When the current of the motor reaches up to a set current by setting the output current, this IC does the short brake control by the automatic operation so that the current should not increase more than it.



Based on the voltage input to the VREF pin and the resistance connected between RF and GND, the output current that is subject to the constant-current control is set using the calculation formula below:

$$IOUT = (VREF/5)/RF$$
 resistance

The voltage input to the VREF pin can be switched to two-step settings depending on the statuses of the ATT.

Attenuation function for VREF input voltage

ATT	Current setting reference voltage attenuation ratio
Low	100%
High	50%

The formula used to calculate the output current when using the function for attenuating the VREF input voltage is given below.

$$I_{OUT} = (VREF/5) \times (attenuation ratio)/RF resistance$$

Example : At VREF of 1.5V, a reference voltage setting of 100% (ATT = L) and an RF resistance of 0.3Ω , the output current is set as shown below.

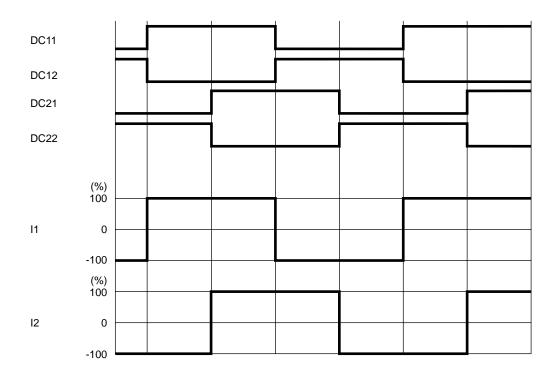
$$I_{OUT} = 1.5V/5 \times 100\%/0.3\Omega = 1.0A$$

If, in this state, ATT = H will be as follows:

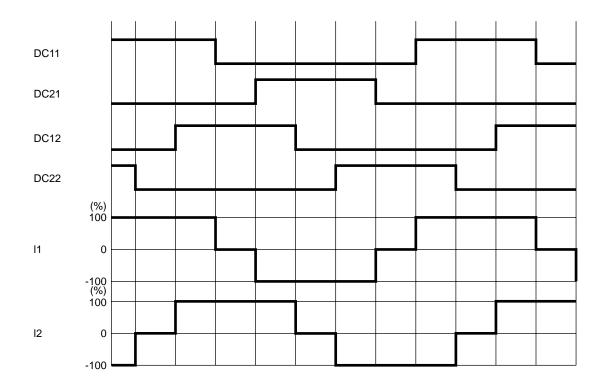
$$I_{OUT} = 1.0A \times 50\% = 500 \text{mA}$$

^{*} The above setting is the output current at 100% of each excitation mode.

(6) Typical current waveform in each excitation mode when stepping motor parallel input control 2-phase excitation (CW mode)



1-2 phase excitation full torque (CW mode)



(7) Output short-circuit protection function

This IC incorporates an output short-circuit protection circuit that, when the output has been shorted by an event such as shorting to power or shorting to ground, sets the output to the standby mode and turns on the warning output in order to prevent the IC from being damaged. In the channels 1 and 2 operate independently. (Even if the output of channel 1 has been short-circuited, channel 2 will operate normally.)

(7-1) Output short-circuit protection operation changeover function

Changeover to the output short-circuit protection of IC is made by the setting of EMM pin.

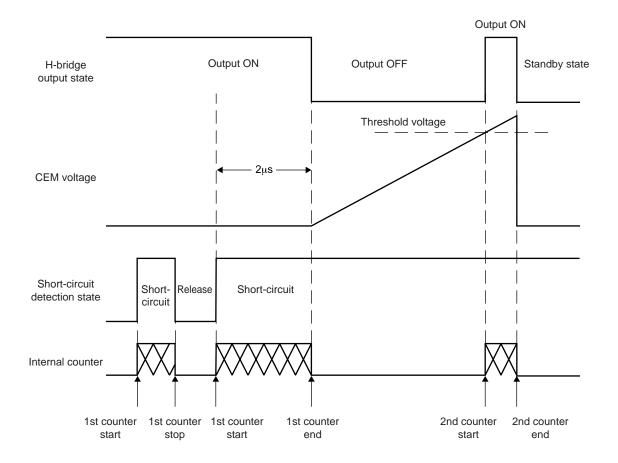
EMM	State	
Low or Open	Latch method	
High	Auto reset method	

(7-2) Latch type

In the latch mode, when the output current exceeds the detection current level, the output is turned OFF, and this state is held.

The detection of the output short-circuited state by the IC causes the output short-circuit protection circuit to be activated.

When the short-circuited state continues for the period of time set using the internal timer (approximately $2\mu s$), the output in which the short-circuiting has been detected is first set to OFF. After this, the output is set to ON again as soon as the timer latch time (Tcem) described later has been exceeded, and if the short-circuited state is still detected, all the outputs of the channel concerned are switched to the standby mode, and this state is held. This state is released by setting ST to low.



(7-3) Auto reset type

In the automatic reset mode, when the output current exceeds the detection current level, the output waveform changes to the switching waveform.

As with the latch system, when the output short-circuited state is detected, the short-circuit protection circuit is activated. When the operation of the short-circuit detection circuit exceeds the timer latch time (Tcem) described later, the output is changed over to the standby mode and is reset to the ON mode again in 2ms (typ). In this event, if the overcurrent mode still continues, the switching mode described above is repeated until the overcurrent mode is canceled.

(7-4) Unusual condition warning output pins (EMO1, EMO2)

The LV8773 is provided with the EMO pin which notifies the CPU of an unusual condition if the protection circuit operates by detecting an unusual condition of the IC. This pin is of the open-drain output type and when an unusual condition is detected, the EMO output is placed in the ON (EMO = Low) state.

The EMO1 pin and the EMO2 pin output unusual condition on 2ch side/ 1ch side respectively.

Furthermore, the EMO (EMO2) pin is placed in the ON state when one of the following conditions occurs.

- 1. Shorting-to-power, shorting-to-ground, or shorting-to-load occurs at the output pin and the output short-circuit protection circuit is activated.
- 2. The IC junction temperature rises and the thermal protection circuit is activated.

Unusual condition	EMO1	EMO2
Channel 1 short-circuit detected	ON	-
Channel 2 short-circuit detected	-	ON
Overheating condition detected	ON	ON

(7-5) Timer latch time (Tcem)

The time taken for the output to be set to OFF when the output has been short-circuited can be set using capacitor Ccem, connected between the CEM pin and GND. The value of capacitor Ccem is determined by the formula given below.

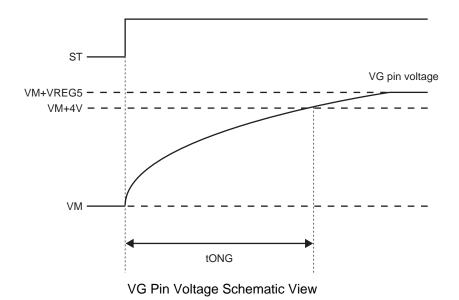
Timer latch : Tcem $Tcem \approx Ccem \times Vtcem/Icem [sec]$

Vtcem : Comparator threshold voltage, typ 1V Icem : CEM pin charge current, typ 10µA

(8) Charge Pump Circuit

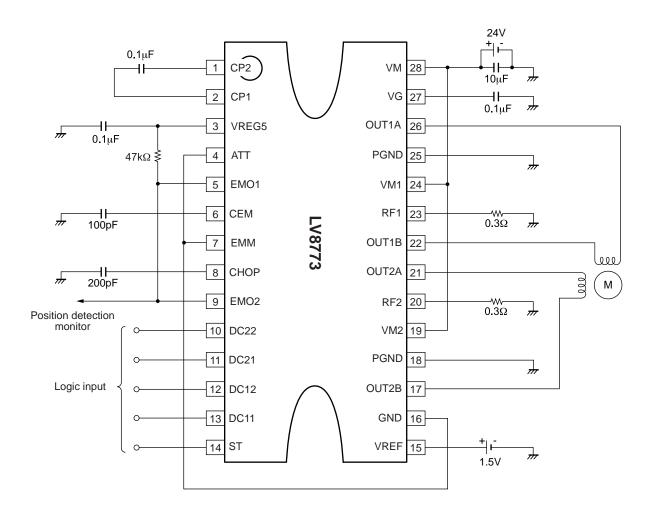
When the ST pin is set High, the charge pump circuit operates and the VG pin voltage is boosted from the VM voltage to the VM + VREG5 voltage.

Begin the drive of the motor after the time of tONG or more because it doesn't turn on the output if the voltage of the VG pin is not pressured to VM+4V or more.



Application Circuit Example

• Stepping motor driver circuit



The formulae for setting the constants in the examples of the application circuits above are as follows: Constant current (100%) setting

When
$$VREF = 1.5V$$

$$I_{OUT} = VREF/5/RF$$
 resistance
= 1.5V/5/0.3 Ω = 1.0A

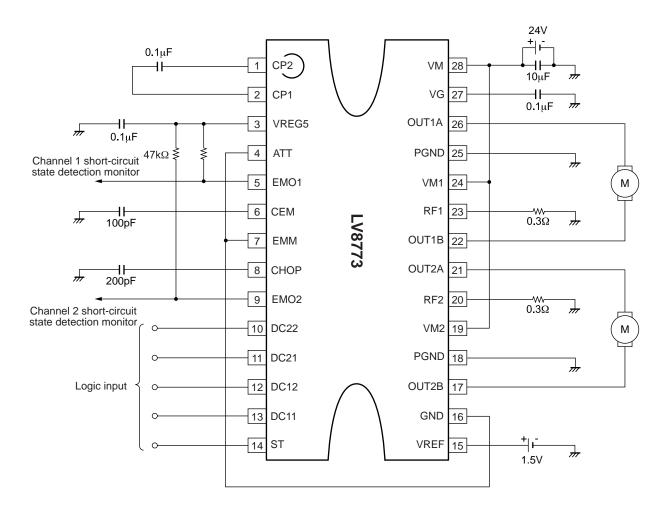
Chopping frequency setting

$$\begin{aligned} Fchop &= Ichop/ \ (Cchop \times Vtchop \times 2) \\ &= 10 \mu A/ \ (220 pF \times 0.5 V \times 2) = 45.4 kHz \end{aligned}$$

Timer latch time when the output is short-circuited

$$\begin{aligned} Tcem &= Ccem \times Vtcem/Icem \\ &= 100pF \times 1V/10\mu A = 10\mu s \end{aligned}$$

• DC motor driver circuit (Constant current control function is used.)



The formulae for setting the constants in the examples of the application circuits above are as follows: Constant current limit (100%) setting

When
$$VREF = 1.5V$$

$$Ilimit = VREF/5/RF\ resistance$$

$$= 1.5V/5/0.3\Omega = 1.0A$$

Chopping frequency setting

Fchop = Ichop/ (Cchop
$$\times$$
 Vtchop \times 2)

$$= 10\mu A/(220pF \times 0.5V \times 2) = 45.4kHz$$

Timer latch time when the output is short-circuited

$$Tcem = Ccem \times Vtcem/Icem$$

$$=100 pF\times 1V/10 \mu A=10 \mu s$$

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