Programmable Shunt Regulator

KA431S, KA431SA, **KA431SL**

Description

The KA431S / KA431SA / KA431SL are three-terminal adjustable regulator series with a guaranteed thermal stability over the operating temperature range. The output voltage can be set to any value between V_{REF} (approximately 2.5 V) and 36 V with two external resistors. These devices have a typical dynamic output impedance of 0.2Ω . Active output circuitry provides a sharp turn-on characteristic, making these devices excellent replacement for zener diodes in many applications.

Features

- Programmable Output Voltage to 36 V
- Low Dynamic Output Impedance 0.2 Ω (Typical)
- Sink Current Capability: 1.0 to 100 mA
- Equivalent Full-Range Temperature Coefficient of 50 ppm/°C (Typical)
- Temperature Compensated for Operation Over Full Rated Operating Temperature Range
- Low Output Noise Voltage
- Fast Turn-on Response
- These Devices are Pb-Free and Halogen Free

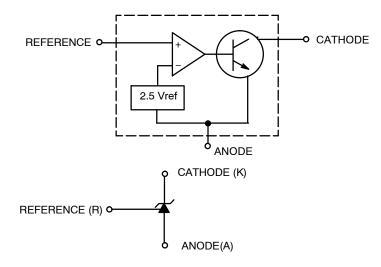


Figure 1. Block Diagram



MF 1. Cathode

MF2 1. Ref 2. Ref 3. Anode

2. Cathode 3. Anode

SOT23-FL3L CASE 318AB

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 2 of this data sheet.

ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

MARKING INFORMATION



Figure 2. Top Mark (per package)

ABSOLUTE MAXIMUM RATINGS

T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Unit
V _{KA}	Cathode Voltage	37	V
I _{KA}	Cathode Current Range (Continuous)	−100 ~ +150	mA
I _{REF}	Reference Input Current Range	−0.05 ~ +10	mA
$R_{ hetaJA}$	Thermal Resistance Junction-Air (Note 1) (Note 2) MF Suffix Package	350	°C/W
I _{REF}	Power Dissipation (Note 3) (Note 4) MF Suffix Package	350	mW
TJ	Junction Temperature	150	°C
T _{OPR}	Operating Temperature Range	−25 ~ +85	°C
T _{STG}	Storage Temperature Range	−65 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Thermal resistance test board:
 - Size: 1.6 mm x 76.2 mm x 114.3 mm (1S0P))
 - JEDEC Standard: JESD51-3, JESD51-7
- 2. Assume no ambient airflow.
- 3. $T_{JMAX} = 150$ °C; Ratings apply to ambient temperature at 25°C.
- 4. Power dissipation calculation: $P_D = (T_J T_A) / R_{\theta JA}$.

RECOMMENDED OPERATING RANGES

Symbol	Parameter	Min.	Max.	Unit
V _{KA}	Cathode Voltage	V_{REF}	36	V
I _{KA}	Cathode Current	1	100	mA

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

ELECTRICAL CHARACTERISTICS (Note 5)

Values are at $T_A = 25^{\circ}C$ unless otherwise noted

				KA431S		KA431SA			KA431SL				
Symbol	Parameter	Cor	nditions	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
V _{REF}	Reference Input Voltage	$V_{KA} = V_{REF}$, $I_{KA} = 10 \text{ mA}$		2.450	2.500	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V
ΔV _{REF} /ΔT	Deviation of Reference Input Voltage Over- Temperature	$V_{KA} = V_{REF}$, $I_{KA} = 10$ mA, $T_{MIN} \le T_A \le T_{MAX}$		-	4.5	17.0	-	4.5	17.0	-	4.5	17.0	mV
$\Delta V_{REF}/\Delta V_{KA}$	EF/ΔV _{KA} Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	I _{KA} = 10 mA	ΔV _{KA} = 10 V – V _{REF}	-	-1.0	-2.7	_	-1.0	-2.7	_	-1.0	-2.7	mV/V
			ΔV _{KA} = 36 V – 10 V	-	-0.5	-2.0	_	-0.5	-2.0	_	-0.5	-2.0	
I _{REF}	Reference Input Current		= 10 mA, kΩ, R2 = ∞	-	1.5	4.0	_	1.5	4.0	-	1.5	4.0	μΑ
ΔI _{REF} /ΔΤ	Deviation of Reference Input Current Over Full Temperature Range	I_{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞ T_A = Full Range		-	0.4	1.2	-	0.4	1.2	-	0.4	1.2	μА
I _{KA(MIN})	Minimum Cathode Current for Regulation	V _{KA} = V _{REF}		_	0.45	1.00	-	0.45	1.00	-	0.45	1.00	mA
I _{KA(OFF)}	Off-Stage Cathode Current	V _{KA} = 36	3 V, V _{REF} = 0	-	0.05	1.00	_	0.05	1.00	-	0.05	1.00	μΑ
Z _{KA}	Dynamic Impedance	I _{KA} = 1	= V _{REF} , to 100 mA, 1.0 kHz	_	0.15	0.50	-	0.15	0.50	-	0.15	0.50	Ω

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. $T_{MIN} = -25^{\circ}C$, $T_{MAX} = +85^{\circ}C$

TEST CIRCUITS

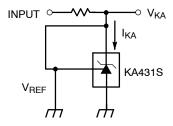


Figure 3. Test Circuit for $V_{KA} = V_{REF}$

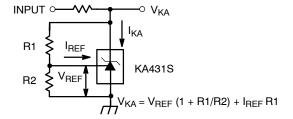


Figure 4. Test Circuit for $V_{KA} \ge V_{REF}$

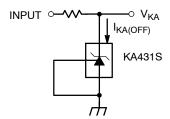


Figure 5. Test Circuit for I_{KA(OFF)}

TYPICAL APPLICATIONS

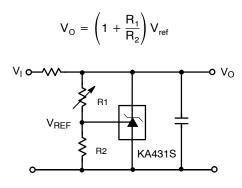


Figure 6. Shunt Regulator

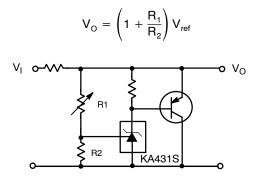


Figure 8. High Current Shunt Regulator

$$I_0 = \frac{V_{REF}}{R_S}$$

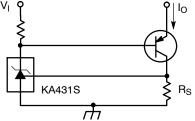


Figure 10. Constant-Current Sink

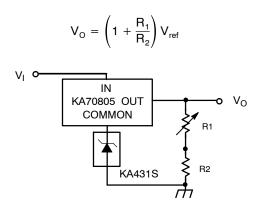


Figure 7. Output Control for Three-Terminal Fixed Regulator

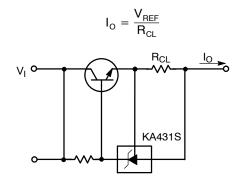


Figure 9. Current limit or Current Source

TYPICAL CHARACTERISTICS

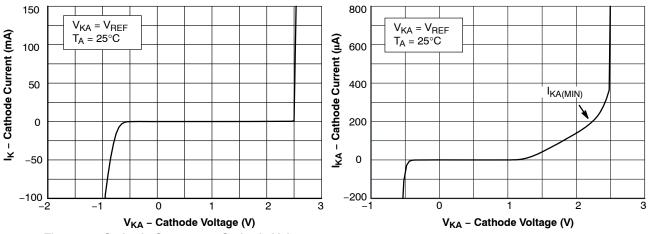


Figure 11. Cathode Current vs. Cathode Voltage

Figure 12. Cathode Current vs. Cathode Voltage

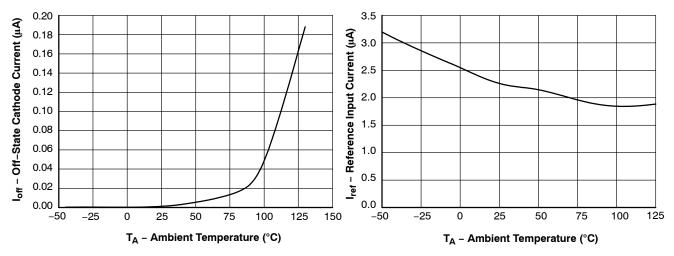


Figure 13. OFF-State Cathode Current vs.
Ambient Temperature

6
5
INPUT

0
0
4
8
12
16
20

T_A = 25°C l_{KA} = 10 mA

40
30
20
10
0
1k
10k
10k
10M
10M

Figure 15. Frequency vs. Small Signal Voltage Amplification

Frequency (Hz)

Figure 14. Reference Input Current vs. Ambient Temperature

Time (μs)
Figure 16. Pulse Response

60

TYPICAL CHARACTERISTICS (Continued)

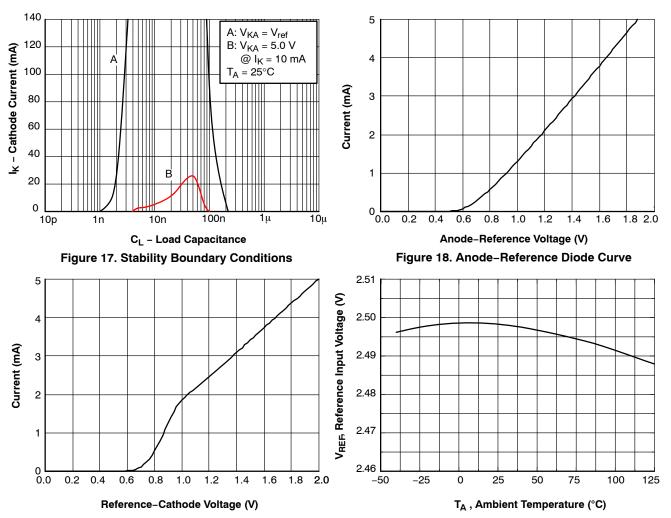


Figure 19. Reference-Cathode Diode Curve

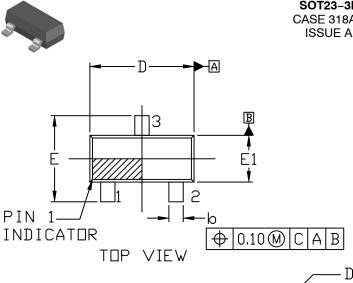
Figure 20. Reference Input Voltage vs. Ambient Temperature

ORDERING INFORMATION

Part Number	Output Voltage Tolerance	Operating Temperature Range	Top Mark	Package	Shipping [†]
KA431SMFTF	2%	−25 to +85°C	43A	SOT23-FL3L	3000 / Tape and Reel
KA431SMF2TF			43D	(Pb-Free)	
KA431SAMFTF	1%		43B		
KA431SAMF2TF			43E		
KA431SLMFTF	0.5%		43C		
KA431SLMF2TF			43F		

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



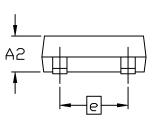


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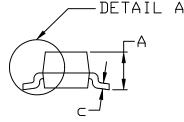
DATE 14 DEC 2021

- ITES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
 CONTROLLING DIMENSION: MILLIMETERS
 DIMENSION & DUES NOT INCLUDE DAMBAR PROTRUSION.
 ALLOWABLE PROTRUSION SHALL BE 0.127 mm IN EXCESS OF
 MAXIMUM MATERIAL CONDITION.
 DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH,
 PROTRUSIONS, DR GATE BURRS. MOLD FLASH, PROTRUSIONS, DR
 GATE BURRS SHALL NOT EXCEDE 0.15 mm PER SIDE. DIMENSIONS
 D AND E1 ARE DETERMINED AT DATUM F.
 DATUMS A AND B ARE TO BE DETERMINED AT DATUM F.
 AI IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING
 PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.
 LEAD THICKNESS (C) AND LEAD WIDTH (b) INCLUDE PLATING
 THICKNESS.

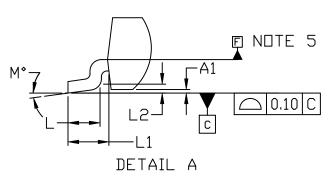
	MILLIMETERS					
DIM	MIN.	N□M.	MAX.			
Α			1.15			
A1	0.00		0.10			
A2	0.90	1.00	1.10			
b	0.30		0.50			
c	C	0.127 REF				
D	2.80	2.90	3.00			
E	2.25	2.40	2.55			
E1	1.20	1.30	1.40			
e		1.90 BSC				
L	0.30					
L1	0.55 REF					
L2	0.25 REF					
М	0*	8*				

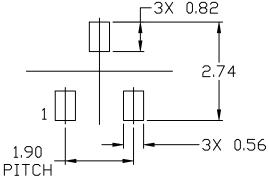


SIDE VIEW





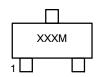




RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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